



# SPEC® CINT2006 Result

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## ACTION S.A.

**SPECint®2006 = 41.5**

ACTINA SOLAR 210 X5 (Intel Xeon E5-2630)

**SPECint\_base2006 = 38.9**

CPU2006 license: 9008

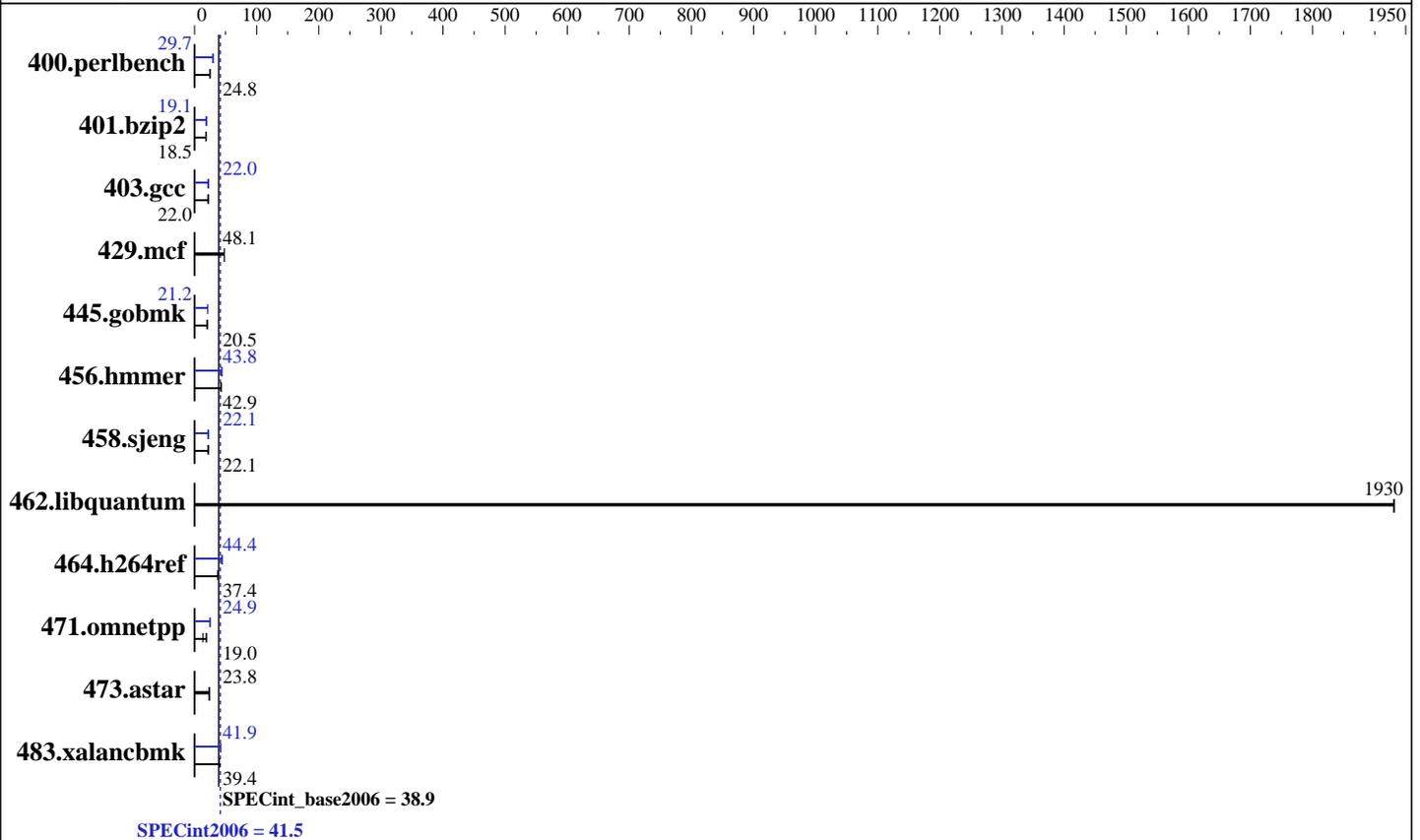
Test date: Oct-2012

Test sponsor: ACTION S.A.

Hardware Availability: Mar-2012

Tested by: ACTION S.A.

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Xeon E5-2630  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 2 TB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86\_64) 3.0.13-0.27-default  
 Compiler: C/C++; Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	393	24.9	394	24.8	<b><u>394</u></b>	<b><u>24.8</u></b>	<b><u>329</u></b>	<b><u>29.7</u></b>	329	29.7	329	29.7
401.bzip2	<b><u>523</u></b>	<b><u>18.5</u></b>	523	18.5	523	18.4	<b><u>506</u></b>	<b><u>19.1</u></b>	505	19.1	506	19.1
403.gcc	361	22.3	368	21.9	<b><u>367</u></b>	<b><u>22.0</u></b>	356	22.6	367	21.9	<b><u>366</u></b>	<b><u>22.0</u></b>
429.mcf	190	48.0	190	48.1	<b><u>190</u></b>	<b><u>48.1</u></b>	190	48.0	190	48.1	<b><u>190</u></b>	<b><u>48.1</u></b>
445.gobmk	<b><u>512</u></b>	<b><u>20.5</u></b>	513	20.5	512	20.5	495	21.2	<b><u>495</u></b>	<b><u>21.2</u></b>	494	21.2
456.hammer	218	42.8	<b><u>217</u></b>	<b><u>42.9</u></b>	217	42.9	213	43.8	<b><u>213</u></b>	<b><u>43.8</u></b>	213	43.9
458.sjeng	548	22.1	549	22.0	<b><u>548</u></b>	<b><u>22.1</u></b>	547	22.1	547	22.1	<b><u>547</u></b>	<b><u>22.1</u></b>
462.libquantum	10.7	1930	10.7	1930	<b><u>10.7</u></b>	<b><u>1930</u></b>	10.7	1930	10.7	1930	<b><u>10.7</u></b>	<b><u>1930</u></b>
464.h264ref	<b><u>591</u></b>	<b><u>37.4</u></b>	589	37.6	596	37.1	498	44.4	499	44.3	<b><u>498</u></b>	<b><u>44.4</u></b>
471.omnetpp	461	13.6	325	19.2	<b><u>329</u></b>	<b><u>19.0</u></b>	252	24.9	250	25.0	<b><u>251</u></b>	<b><u>24.9</u></b>
473.astar	295	23.8	<b><u>295</u></b>	<b><u>23.8</u></b>	293	23.9	295	23.8	<b><u>295</u></b>	<b><u>23.8</u></b>	293	23.9
483.xalancbmk	175	39.4	<b><u>175</u></b>	<b><u>39.4</u></b>	175	39.5	165	41.8	163	42.5	<b><u>165</u></b>	<b><u>41.9</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /cpu2006.1.2/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on linux-cpu2006 Wed Oct 31 07:41:40 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2630 0 @ 2.30GHz
 2 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 6
siblings : 12
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal: 132116920 kB
```

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## Platform Notes (Continued)

HugePages\_Total: 0  
Hugepagesize: 2048 kB

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 2
```

```
uname -a:
Linux linux-cpu2006 3.0.13-0.27-default #1 SMP Wed Feb 15 13:33:49 UTC 2012
(d73692b) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Oct 26 05:31 last=S
```

```
SPEC is set to: /cpu2006.1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext3  1.8T   67G  1.8T   4% /
```

Additional information from dmidecode:

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

```
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64"
OMP_NUM_THREADS = "12"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```



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## Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Base Optimization Flags

C benchmarks:  
 -xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:  
 -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
 -Wl,-z,muldefs -L/smartheap -lsmartheap64

## Base Other Flags

C benchmarks:  
 403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
 icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):  
 icpc -m32

473.astar: icpc -m64



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## Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
            -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
          -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
            -ansi-alias

456.hmmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
            -ansi-alias

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -unroll2 -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -opt-ra-region-strategy=block -ansi-alias
              -Wl,-z,muldefs -L/smartheap -lsmartheap

```

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## Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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