**SPEC® CFP2006 Result**

**Hewlett-Packard Company**

ProLiant ML350p Gen8
(2.00 GHz, Intel Xeon E5-2620)

<table>
<thead>
<tr>
<th>SPECfp®_rate2006</th>
<th>Not Run</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECfp_rate_base2006</td>
<td>322</td>
</tr>
</tbody>
</table>

**CPU2006 license:** 3

**Test sponsor:** Hewlett-Packard Company

**Tested by:** Hewlett-Packard Company

Test date: Sep-2012

**Hardware Availability:** Jun-2012

**Software Availability:** Dec-2011

<table>
<thead>
<tr>
<th>Software Availability</th>
<th>CPU Name: Intel Xeon E5-2620</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 2.50 GHz</td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>2000</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>12 cores, 2 chips, 6 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>1,2 chips</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB L1 + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>256 KB I+D on chip per core</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Copies</strong></td>
<td><strong>Red Hat Enterprise Linux Server release 6.2, (Santiago)</strong></td>
</tr>
<tr>
<td>410.bwaves</td>
<td></td>
</tr>
<tr>
<td>416.gamess</td>
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<tr>
<td>433.milc</td>
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<tr>
<td>434.zeusmp</td>
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<td>435.gromacs</td>
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<tr>
<td>436.cactusADM</td>
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<tr>
<td>437.leslie3d</td>
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<tr>
<td>444.namd</td>
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<tr>
<td>447.dealII</td>
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<tr>
<td>450.soplex</td>
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<td>453.povray</td>
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<td>454.calculix</td>
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<td>459.GemsFDTD</td>
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<td>465.tonto</td>
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<td>470.lbm</td>
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<td>481.wrf</td>
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<td>482.sphinx3</td>
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## Hewlett-Packard Company

ProLiant ML350p Gen8  
(2.00 GHz, Intel Xeon E5-2620)

**SPEC CFP2006 Result**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
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<th>Seconds</th>
<th>Ratio</th>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1 > /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
umactl --localalloc runspec <etc>
```

Continued on next page
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SPECfp_rate2006 = Not Run
SPECfp_rate_base2006 = 322

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
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Test date: Sep-2012
Hardware Availability: Jun-2012
Software Availability: Dec-2011

Operating System Notes (Continued)

Drive Write Cache set to Enabled in HP Array Configuration Utility,
CLI version
Accelerator Ratio for Reads/Writes set to = 100% Read / 0% Write
in HP Array Configuration Utility, CLI version

Platform Notes

BIOS Configuration:
HP Power Profile set to Custom
Thermal Configuration set to Maximum Cooling
Collaborative Power Control set to Disabled
Processor Power and Utilization Monitoring set to Disabled

Sysinfo program /cpu2006/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on ml350pGen8 Sat Sep 22 22:29:13 2012

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2620 0 @ 2.00GHz
    2 "physical id"s (chips)
    24 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The
    following excerpts from /proc/cpuinfo might not be reliable. Use with
    caution.)
    cpu cores : 6
    siblings : 12
    physical 0: cores 0 1 2 3 4 5
    physical 1: cores 0 1 2 3 4 5
    cache size : 15360 KB

From /proc/meminfo
    MemTotal:       65937904 kB
    HugePages_Total:       0
    Hugepagesize:       2048 kB

    /usr/bin/lsb_release -d
    Red Hat Enterprise Linux Server release 6.2 (Santiago)

    From /etc/*release* /etc/*version*
    redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
    system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)

    uname -a:
    Linux ml350pGen8 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
    x86_64 x86_64 x86_64 GNU/Linux

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Platform Notes (Continued)

run-level 3 Sep 22 15:00

SPEC is set to: /cpu2006
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 ext4 273G 23G 237G 9% /

Additional information from dmidecode:
BIOS HP P72 02/21/2012
Memory:
8x Not Specified Not Specified 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/cpu2006/libs2/32:/cpu2006/libs2/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Base Compiler Invocation

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
  433.milc: -DSPEC_CPU_LP64
  434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
  436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120829.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120829.xml

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For other inquiries, please contact webmaster@spec.org.

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