SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

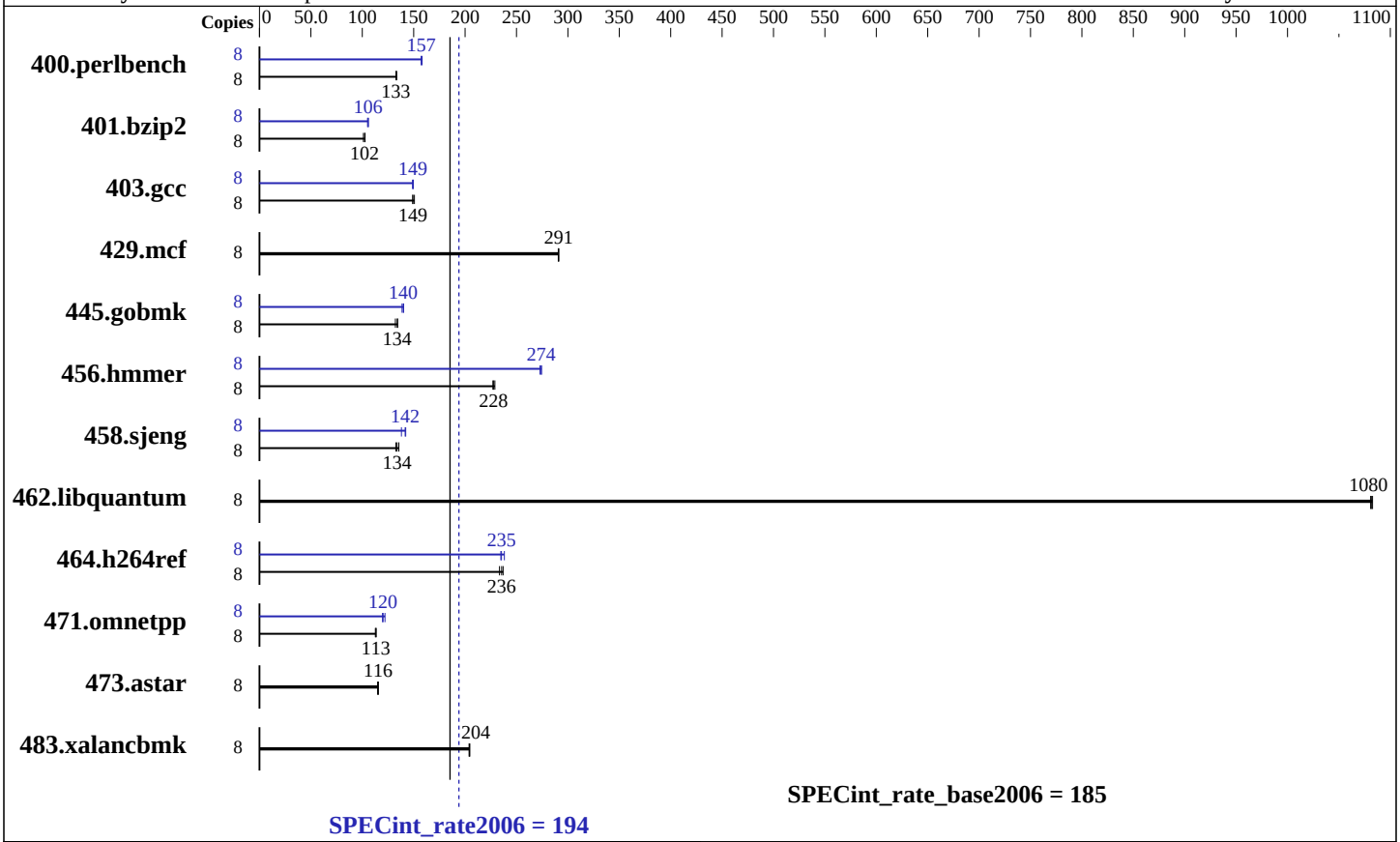
IBM System x3550 M4 (Intel Xeon E5-2643)

SPECint®_rate2006 = 194

SPECint_rate_base2006 = 185

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Dec-2012
Hardware Availability: Mar-2012
Software Availability: Jun-2012



Hardware

CPU Name:	Intel Xeon E5-2643
CPU Characteristics:	Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz:	3300
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	10 MB I+D on chip per chip
Other Cache:	None
Memory:	64 GB (8 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem:	1 x 1 TB SAS, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.3 (Santiago) 2.6.32-279.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

IBM System x3550 M4 (Intel Xeon E5-2643)

SPECint_rate2006 = 194

SPECint_rate_base2006 = 185

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Dec-2012
Hardware Availability: Mar-2012
Software Availability: Jun-2012

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	586	133	**588**	**133**	588	133	8	**496**	**157**	497	157	494	158
401.bzip2	8	752	103	763	101	**755**	**102**	8	729	106	**730**	**106**	734	105
403.gcc	8	**432**	**149**	432	149	427	151	8	430	150	432	149	**432**	**149**
429.mcf	8	251	291	251	291	**251**	**291**	8	251	291	251	291	**251**	**291**
445.gobmk	8	635	132	**626**	**134**	624	134	8	**600**	**140**	605	139	598	140
456.hmmer	8	**328**	**228**	326	229	329	227	8	272	274	**272**	**274**	274	273
458.sjeng	8	730	133	714	136	**723**	**134**	8	680	142	701	138	**683**	**142**
462.libquantum	8	153	1080	153	1080	**153**	**1080**	8	153	1080	153	1080	**153**	**1080**
464.h264ref	8	758	233	747	237	**752**	**236**	8	743	238	**752**	**235**	754	235
471.omnetpp	8	**441**	**113**	444	113	441	113	8	**416**	**120**	410	122	418	120
473.astar	8	**486**	**116**	486	116	489	115	8	**486**	**116**	486	116	489	115
483.xalancbmk	8	270	205	**270**	**204**	271	204	8	270	205	**270**	**204**	271	204

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Settings:
Operating Mode set to Maximum Performance
Sysinfo program /home/SPECcpu-v1.2/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on x3550M4 Fri Dec 14 13:29:55 2012

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2643 0 @ 3.30GHz
      1 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
```

Continued on next page

Platform Notes (Continued)

```
        cpu cores : 4
        siblings  : 8
        physical 0: cores 0 1 2 3
     cache size : 10240 KB

  From /proc/meminfo
     MemTotal:       66042472 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     Red Hat Enterprise Linux Server release 6.3 (Santiago)

  From /etc/*release* /etc/*version*
     redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
     system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

  uname -a:
     Linux x3550M4 2.6.32-279.el6.x86_64 #1 SMP Wed Jun 13 18:24:36 EDT 2012
     x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Dec 14 13:19

  SPEC is set to: /home/SPECcpu-v1.2
     Filesystem     Type    Size  Used Avail Use% Mounted on
     /dev/mapper/vg_x3550m4-lv_home
                    ext4    862G  4.3G  814G   1% /home

  Additional information from dmidecode:
    Memory:
     8x Samsung M393B1K70DH0-CK0 8 GB 1600 MHz 2 rank

  (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/home/SPECcpu-v1.2/libs/32:/home/SPECcpu-v1.2/libs/64"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RHEL5.5
  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html`
`http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml`
`http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 14:23:38 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 January 2013.