# SPEC® CINT2006 Result

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## Supermicro

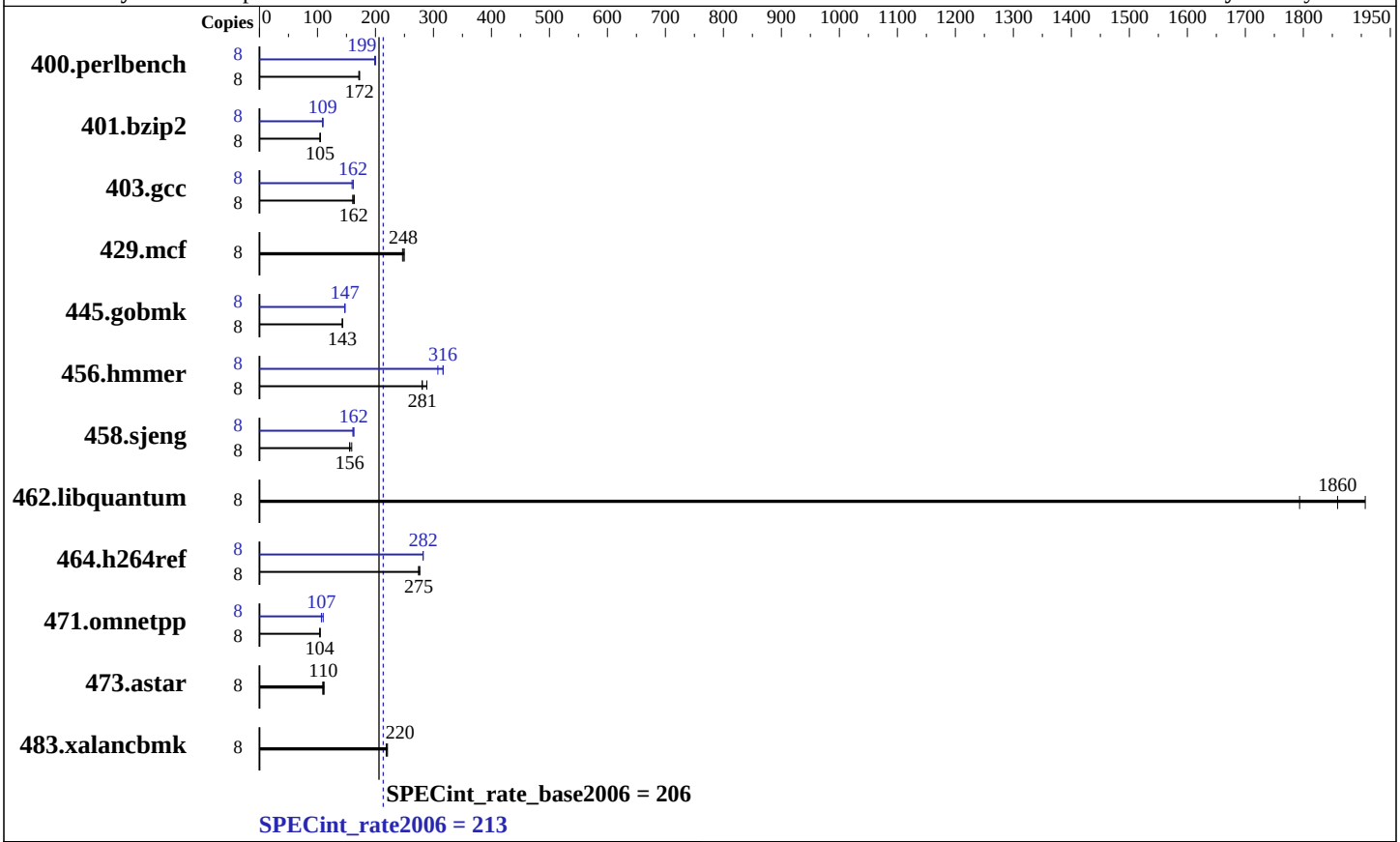
SuperServer 5018D-TLN4F
(X10SLM+-LN4F , Intel Xeon E3-1240 v3)

SPECint®_rate2006 = 213

SPECint_rate_base2006 = 206

**CPU2006 license:** 001176
**Test sponsor:** Supermicro
**Tested by:** Supermicro

**Test date:** Jun-2013
**Hardware Availability:** Jun-2013
**Software Availability:** May-2013




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E3-1240 v3 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.80 GHz |
| CPU MHz: | 3400 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 1 chip, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 8 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC) |
| Disk Subsystem: | 1 x 500 GB SATA III, 7200 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.4 (Santiago)<br>Kernel 2.6.32-358.el6.x86_64 |
| Compiler: | C/C++: Version 13.1.1.163 of Intel C++ Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V10.0 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 453 | 173 | 456 | 171 | **454** | **172** | 8 | 394 | 199 | **392** | **199** | 390 | 200 |
| 401.bzip2 | 8 | 740 | 104 | **737** | **105** | 736 | 105 | 8 | 711 | 109 | **709** | **109** | 704 | 110 |
| 403.gcc | 8 | 393 | 164 | **397** | **162** | 401 | 161 | 8 | **399** | **162** | 398 | 162 | 404 | 160 |
| 429.mcf | 8 | 292 | 250 | **294** | **248** | 296 | 247 | 8 | 292 | 250 | **294** | **248** | 296 | 247 |
| 445.gobmk | 8 | 588 | 143 | 586 | 143 | **586** | **143** | 8 | 568 | 148 | 571 | 147 | **570** | **147** |
| 456.hmmer | 8 | **266** | **281** | 266 | 280 | 259 | 289 | 8 | **236** | **316** | 236 | 317 | 243 | 308 |
| 458.sjeng | 8 | **622** | **156** | 608 | 159 | 622 | 156 | 8 | 594 | 163 | **597** | **162** | 601 | 161 |
| 462.libquantum | 8 | **89.2** | **1860** | 86.9 | 1910 | 92.4 | 1790 | 8 | **89.2** | **1860** | 86.9 | 1910 | 92.4 | 1790 |
| 464.h264ref | 8 | 640 | 277 | 645 | 274 | **644** | **275** | 8 | **627** | **282** | 627 | 282 | 628 | 282 |
| 471.omnetpp | 8 | 478 | 105 | **479** | **104** | 481 | 104 | 8 | 455 | 110 | **467** | **107** | 468 | 107 |
| 473.astar | 8 | 515 | 109 | 504 | 111 | **510** | **110** | 8 | 515 | 109 | 504 | 111 | **510** | **110** |
| 483.xalancbmk | 8 | 253 | 218 | 250 | 221 | **251** | **220** | 8 | 253 | 218 | 250 | 221 | **251** | **220** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

## Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

C++ benchmarks:

```
icpc -m32
```

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

Continued on next page

## Peak Portability Flags (Continued)

458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130702.html`
`http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130702.xml`
`http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.xml`


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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 16:05:41 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 24 September 2013.