SPEC® CFP2006 Result

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NEC Corporation

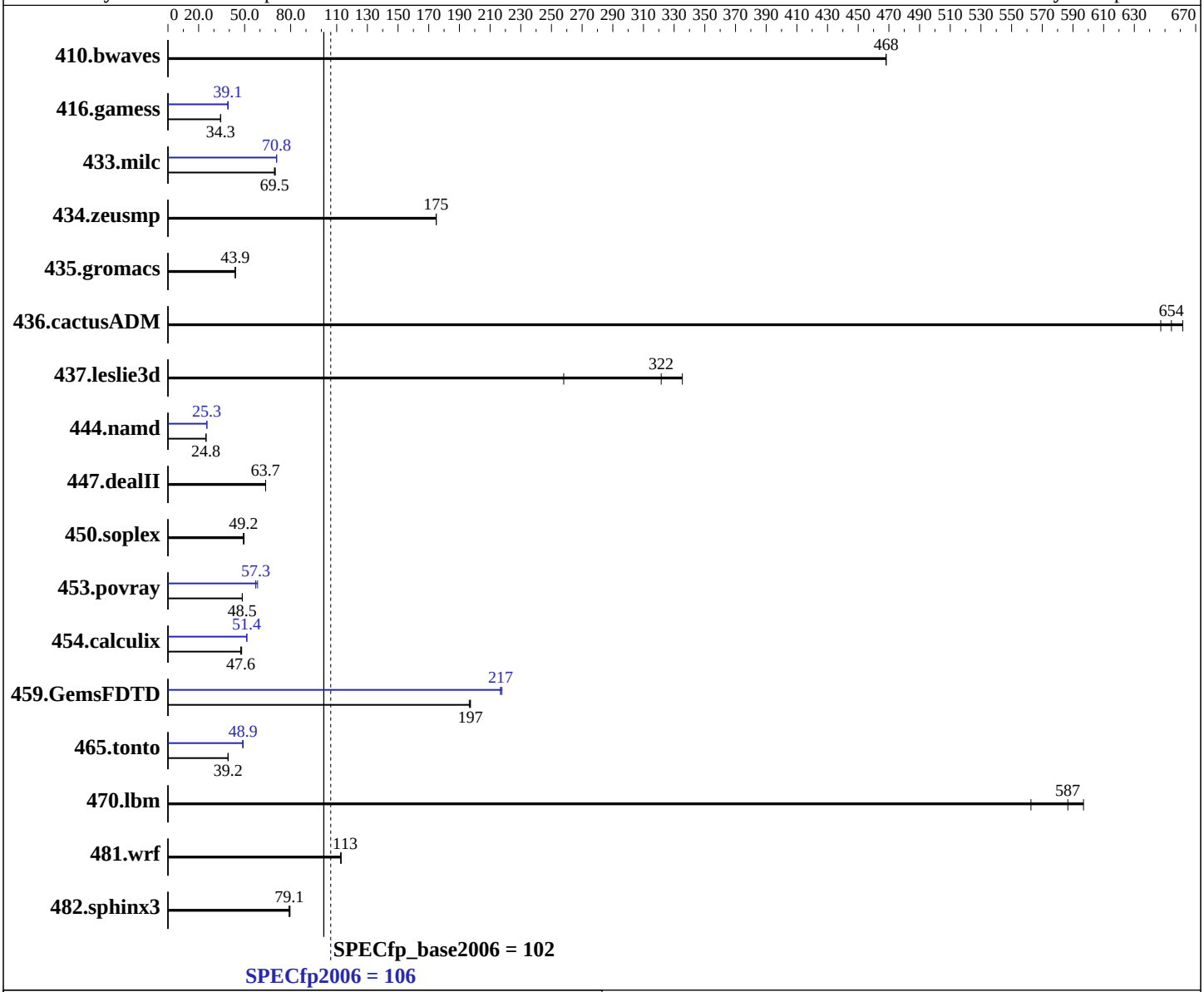
Express5800/R120e-2M (Intel Xeon E5-2690 v2)

SPECfp®2006 = 106

SPECfp_base2006 = 102

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Oct-2013
Hardware Availability: Sep-2013
Software Availability: Sep-2013



Hardware

CPU Name:	Intel Xeon E5-2690 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) Kernel 2.6.32-358.18.1.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (8 x 16 GB 2Rx4 PC3-14900R-13, ECC)
Disk Subsystem:	1 x 250 GB SATA, 7200 RPM
Other Hardware:	None

System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**29.0**	**468**	29.0	468	29.0	468	**29.0**	**468**	29.0	468	29.0	468
416.gamess	**570**	**34.3**	569	34.4	571	34.3	501	39.1	**501**	**39.1**	500	39.1
433.milc	131	70.0	132	69.5	**132**	**69.5**	130	70.8	**130**	**70.8**	130	70.8
434.zeusmp	52.0	175	**52.0**	**175**	52.0	175	52.0	175	**52.0**	**175**	52.0	175
435.gromacs	163	43.9	163	43.9	**163**	**43.9**	163	43.9	163	43.9	**163**	**43.9**
436.cactusADM	**18.3**	**654**	18.5	647	18.1	662	**18.3**	**654**	18.5	647	18.1	662
437.leslie3d	36.4	258	28.0	335	**29.2**	**322**	36.4	258	28.0	335	**29.2**	**322**
444.namd	323	24.8	324	24.8	**323**	**24.8**	**316**	**25.3**	316	25.3	316	25.3
447.dealII	180	63.7	**180**	**63.7**	180	63.7	180	63.7	**180**	**63.7**	180	63.7
450.soplex	168	49.6	**169**	**49.2**	170	49.2	168	49.6	**169**	**49.2**	170	49.2
453.povray	110	48.5	110	48.4	**110**	**48.5**	91.0	58.4	**92.8**	**57.3**	93.1	57.1
454.calculix	**173**	**47.6**	172	48.1	174	47.5	160	51.4	161	51.3	**161**	**51.4**
459.GemsFDTD	53.8	197	**53.8**	**197**	54.0	196	48.9	217	48.7	218	**48.9**	**217**
465.tonto	250	39.3	**251**	**39.2**	251	39.2	201	49.0	202	48.7	**201**	**48.9**
470.lbm	24.4	563	23.0	597	**23.4**	**587**	24.4	563	23.0	597	**23.4**	**587**
481.wrf	99.2	113	**99.0**	**113**	99.0	113	99.2	113	**99.0**	**113**	99.0	113
482.sphinx3	245	79.6	247	79.0	**246**	**79.1**	245	79.6	247	79.0	**246**	**79.1**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Settings:
 Energy Performance: Performance
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "20"
```

Continued on next page

General Notes (Continued)

```
Added glibc-static-2.12-1.107.el6.x86_64.rpm
to enable static linking

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
The Express5800/R120e-1M and
the Express5800/R120e-2M models are electronically equivalent.
The results have been measured on the Express5800/R120e-1M model.
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Fortran benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.xml

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