SPEC® CINT2006 Result

Fujitsu

PRIMERGY RX100 S8, Intel Xeon E3-1265L v3, 2.50 GHz

$SPECint®2006 =$ **59.0**

$SPECint\_base2006 =$ **56.4**

CPU2006 license: 19	**Test date:** Sep-2013
Test sponsor: Fujitsu	**Hardware Availability:** Sep-2013
Tested by: Fujitsu	**Software Availability:** Sep-2013

Graph scale: 0 50.0 100 150 200 250 300 350 400 450 500 550 600 650 700 750 800 850 900 950 1000 1100 1200 1350

- 400.perlbench: 49.3 / 41.2
- 401.bzip2: 28.0 / 27.8
- 403.gcc: 37.7 / 37.1
- 429.mcf: 74.6
- 445.gobmk: 30.3 / 26.6
- 456.hmmer: 74.4
- 458.sjeng: 36.0 / 35.7
- 462.libquantum: 1340
- 464.h264ref: 50.5
- 471.omnetpp: 33.6 / 27.7
- 473.astar: 35.0
- 483.xalancbmk: 71.4

SPECint_base2006 = 56.4

SPECint2006 = 59.0

Hardware

CPU Name:	Intel Xeon E3-1265L v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (4 x 8 GB 2Rx8 PC3L-12800E-11, ECC)
Disk Subsystem:	1 x SATA, 500 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) 2.6.32-358.11.1.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table
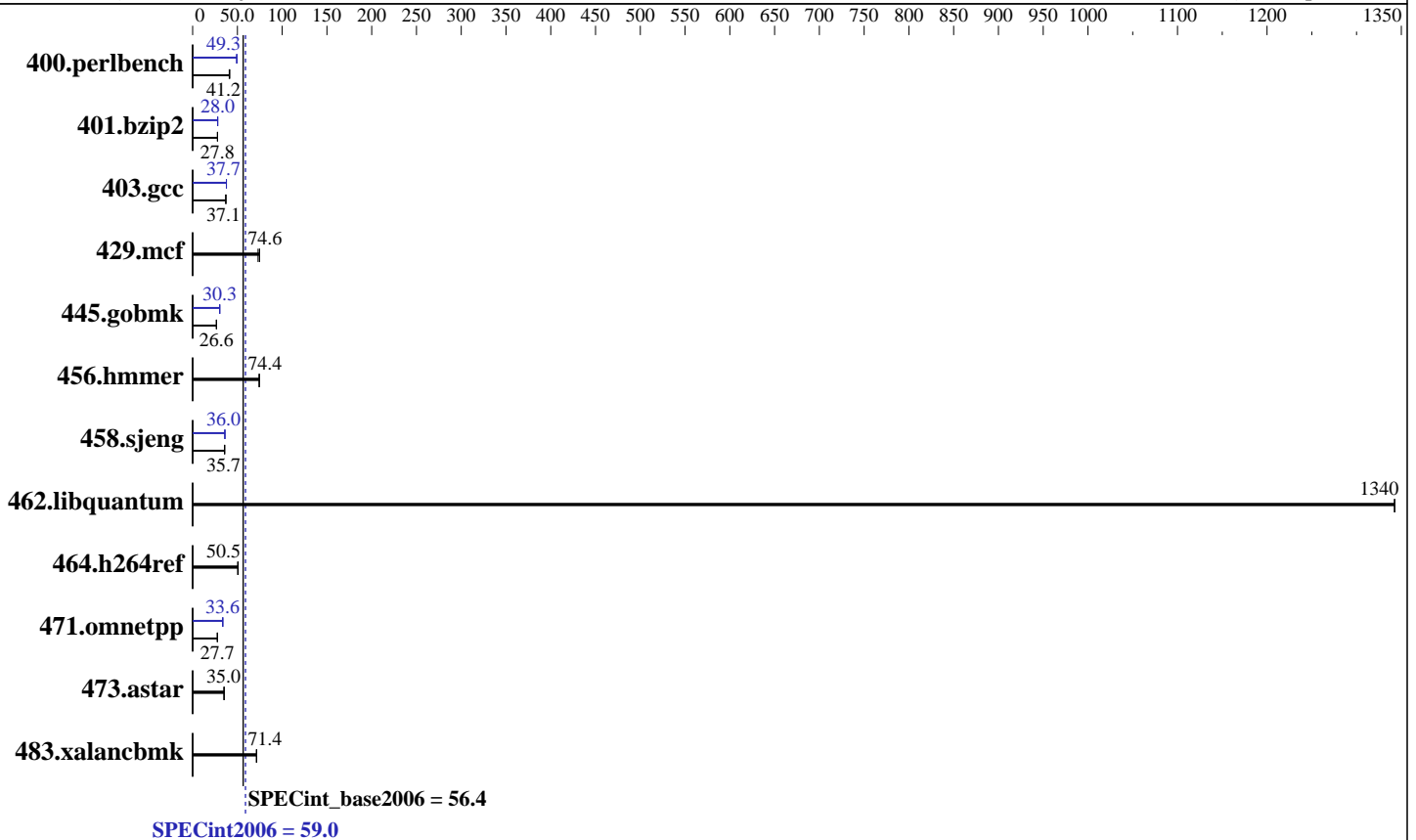
Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	236	41.5	**237**	**41.2**	239	40.9	**198**	**49.3**	198	49.4	199	49.2
401.bzip2	**347**	**27.8**	348	27.7	347	27.8	**345**	**28.0**	345	28.0	345	28.0
403.gcc	**217**	**37.1**	217	37.0	217	37.2	214	37.7	**213**	**37.7**	213	37.7
429.mcf	122	74.7	**122**	**74.6**	125	73.0	122	74.7	**122**	**74.6**	125	73.0
445.gobmk	394	26.6	395	26.5	**395**	**26.6**	347	30.2	**347**	**30.3**	347	30.3
456.hmmer	125	74.6	126	74.2	**125**	**74.4**	125	74.6	126	74.2	**125**	**74.4**
458.sjeng	**339**	**35.7**	339	35.7	339	35.7	**336**	**36.0**	336	36.0	336	36.1
462.libquantum	15.4	1340	**15.4**	**1340**	15.4	1340	15.4	1340	**15.4**	**1340**	15.4	1340
464.h264ref	**439**	**50.5**	435	50.9	439	50.4	**439**	**50.5**	435	50.9	439	50.4
471.omnetpp	228	27.5	**226**	**27.7**	225	27.8	**186**	**33.6**	185	33.8	186	33.5
473.astar	**200**	**35.0**	201	35.0	199	35.3	**200**	**35.0**	201	35.0	199	35.3
483.xalancbmk	96.4	71.6	97.0	71.1	**96.6**	**71.4**	96.4	71.6	97.0	71.1	**96.6**	**71.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64:/SPECcpu2006/sh"
OMP_NUM_THREADS = "4"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
        icc -m64

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m64

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
        401.bzip2: -DSPEC_CPU_LP64
          403.gcc: -DSPEC_CPU_LP64
          429.mcf: -DSPEC_CPU_LP64
       445.gobmk: -DSPEC_CPU_LP64
       456.hmmer: -DSPEC_CPU_LP64
        458.sjeng: -DSPEC_CPU_LP64
   462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
      464.h264ref: -DSPEC_CPU_LP64
      471.omnetpp: -DSPEC_CPU_LP64
         473.astar: -DSPEC_CPU_LP64
   483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
    -Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m64

    400.perlbench: icc -m32

    445.gobmk: icc -m32

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Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):
    icpc -m64

    471.omnetpp: icpc -m32

Peak Portability Flags

          400.perlbench: -DSPEC_CPU_LINUX_IA32
          401.bzip2: -DSPEC_CPU_LP64
          403.gcc: -DSPEC_CPU_LP64
          429.mcf: -DSPEC_CPU_LP64
          456.hmmer: -DSPEC_CPU_LP64
          458.sjeng: -DSPEC_CPU_LP64
          462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
          464.h264ref: -DSPEC_CPU_LP64
          473.astar: -DSPEC_CPU_LP64
          483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

          400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                    -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                    -opt-prefetch -ansi-alias

          401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                    -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
                    -opt-prefetch -ansi-alias

          403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
                    -opt-malloc-options=3 -auto-ilp32

          429.mcf: basepeak = yes

          445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
                    -ansi-alias

          456.hmmer: basepeak = yes

          458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                    -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                    -unroll4

          462.libquantum: basepeak = yes

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Peak Optimization Flags (Continued)

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
  -opt-ra-region-strategy=block   -ansi-alias
  -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.xml

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