**Fujitsu**

PRIMERGY BX924 S4, Intel Xeon E5-2630L v2, 2.40 GHz

<table>
<thead>
<tr>
<th>SPECint®_rate2006</th>
<th>459</th>
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<tbody>
<tr>
<td>SPECint_rate_base2006</td>
<td>442</td>
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</tbody>
</table>

**CPU2006 license:** 19  
**Test sponsor:** Fujitsu  
**Tested by:** Fujitsu  
**Test date:** Oct-2013  
**Hardware Availability:** Oct-2013  
**Software Availability:** Sep-2013

**CPU Name:** Intel Xeon E5-2630L v2  
**CPU Characteristics:** Intel Turbo Boost Technology up to 2.80 GHz  
**CPU MHz:** 2400  
**FPU:** Integrated  
**CPU(s) enabled:** 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
**CPU(s) orderable:** 1.2 chips  
**Primary Cache:** 32 KB I + 32 KB D on chip per core  
**Secondary Cache:** 256 KB I+D on chip per core  
**L3 Cache:** 15 MB I+D on chip per chip  
**Other Cache:** None  
**Memory:** 128 GB (16 x 8 GB 2x4 PC3-14900R-13, ECC, running at 1600 MHz and CL11)  
**Disk Subsystem:** 1 x SATA, 500 GB, 7200 RPM  
**Other Hardware:** None

**Operating System:** Red Hat Enterprise Linux Server release 6.4 (Santiago)  
**Compiler:** C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
**Auto Parallel:** No  
**File System:** ext4  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 32-bit  
**Peak Pointers:** 32/64-bit  
**Other Software:** Microquill SmartHeap V10.0
SPEC CINT2006 Result

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Results Table

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<tr>
<th>Benchmark</th>
<th>Copies</th>
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<th>Ratio</th>
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes
BIOS configuration:
Energy Performance = Performance

General Notes
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/1libs/32:/SPECcpu2006/1ibs/64:/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numaclt i.e.:
numactl --interleave=all runspec <etc>

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Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
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General Notes (Continued)

For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
\texttt{icc -m32}

C++ benchmarks:
\texttt{icpc -m32}

Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32
462.libquantum: -DSPEC\_CPU\_LINUX
483.xalancbmk: -DSPEC\_CPU\_LINUX

Base Optimization Flags

C benchmarks:
\texttt{-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3}

C++ benchmarks:
\texttt{-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3}
\texttt{-Wl,-z,muldefs -L/sh -lsmartheap}

Base Other Flags

C benchmarks:
\texttt{403.gcc -Dalloca=_alloca}

Peak Compiler Invocation

C benchmarks (except as noted below):
\texttt{icc -m32}

400.perlbench: \texttt{icc -m64}
401.bzip2: \texttt{icc -m64}
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Peak Compiler Invocation (Continued)

456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias
403.gcc: basepeak = yes
429.mcf: basepeak = yes
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32
462.libquantum: basepeak = yes
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

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Peak Optimization Flags (Continued)

C++ benchmarks:
- 471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
- -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
- -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
- -L/sh -lsmartheap

473.astar: basepeak = yes
483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:
- 403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20131009.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20131009.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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