IBM Corporation
IBM System x3650 M4 HD
(Intel Xeon E5-2667 v2, 3.30 GHz)

SPECint®_rate2006 = 806
SPECint_rate_base2006 = 778

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>32</td>
<td>531</td>
<td>589</td>
<td>531</td>
<td>589</td>
<td>32</td>
<td>449</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>32</td>
<td>721</td>
<td>428</td>
<td>722</td>
<td>612</td>
<td>423</td>
<td>609</td>
</tr>
<tr>
<td>403.gcc</td>
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<td>420</td>
<td>613</td>
<td>421</td>
<td>612</td>
<td>423</td>
<td>609</td>
</tr>
<tr>
<td>429.mcf</td>
<td>32</td>
<td>254</td>
<td>1150</td>
<td>253</td>
<td>1150</td>
<td>254</td>
<td>1150</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>32</td>
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<td>587</td>
<td>575</td>
<td>584</td>
<td>582</td>
<td>577</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>32</td>
<td>277</td>
<td>1080</td>
<td>277</td>
<td>1080</td>
<td>277</td>
<td>1080</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>32</td>
<td>681</td>
<td>568</td>
<td>681</td>
<td>568</td>
<td>680</td>
<td>569</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>32</td>
<td>129</td>
<td>5150</td>
<td>128</td>
<td>5160</td>
<td>128</td>
<td>5170</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>32</td>
<td>722</td>
<td>981</td>
<td>721</td>
<td>982</td>
<td>721</td>
<td>982</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>32</td>
<td>490</td>
<td>408</td>
<td>492</td>
<td>407</td>
<td>491</td>
<td>408</td>
</tr>
<tr>
<td>473.astar</td>
<td>32</td>
<td>506</td>
<td>444</td>
<td>508</td>
<td>442</td>
<td>506</td>
<td>444</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>32</td>
<td>262</td>
<td>844</td>
<td>261</td>
<td>845</td>
<td>262</td>
<td>843</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Zone reclaim mode enabled with:
echo 1 > /proc/sys/vm/zone_reclaim_mode

Platform Notes

BIOS setting:
Operating Mode set to Maximum Performance
Sysinfo program /home/SPECcpu-new/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on x3650M4Plus Thu Nov 7 16:03:50 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2667 v2 @ 3.30GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The Continued on next page
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Software Availability: Sep-2013

Platform Notes (Continued)

following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 8
  siblings : 16
  physical 0: cores 1 2 3 4 8 9 10 11
  physical 1: cores 1 2 3 4 8 9 10 11
  cache size : 25600 KB

From /proc/meminfo
  MemTotal: 264652816 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
  Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release*/etc/*version*
  redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
  system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)

uname -a:
  Linux x3650M4Plus 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
  x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 7 15:57

SPEC is set to: /home/SPECcpu-new
  Filesystem Type    Size   Used Avail Use% Mounted on
  /dev/mapper/vg_x3650m4plus-lv_home ext4 309G 197G 96G 68% /home

Additional information from dmidecode:
  BIOS IBM -[TESTBUILD-1.50]- 08/09/2013
  Memory:
    8x Not Specified Not Specified
    16x Samsung M393B2G70QH0-CMA 16 GB 1867 MHz 2 rank

(End of data from sysinfo program)
"Not Specified" memory information from dmidecode indicates unused DIMM slots.
The BIOS IBM -[TESTBUILD-1.50] is equivalent to production version [VVE134TUS-1.51]

General Notes

Environment variables set by runspec before the start of the run:
  LD_LIBRARY_PATH = "/home/SPECcpu-new/libs/32:/home/SPECcpu-new/libs/64:/home/SPECcpu-new/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

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SPEC CINT2006 Result

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General Notes (Continued)

Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

400.perlbench: icc -m64

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Peak Compiler Invocation (Continued)

401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
429.mcf: basepeak = yes
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32
462.libquantum: basepeak = yes

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Peak Optimization Flags (Continued)

464.h264ref: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-03 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-03 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes
483.xalanchbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml

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For other inquiries, please contact webmaster@spec.org.

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