SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, SPEC was notified that an attempt to reproduce the published result did not come within SPEC's requirements for run-to-run variation. Upon re-review, it was determined that the system configuration does not meet SPEC's requirements for documented and supported systems, and does not meet SPEC's requirements for general availability.

Non-Compliant
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, SPEC was notified that an attempt to reproduce the published result did not come within SPEC's requirements for run-to-run variation. Upon re-review, it was determined that the system configuration does not meet SPEC's requirements for documented and supported systems, and does not meet SPEC's requirements for general availability.

Results Table

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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.
Supermicro
SuperServer 6027AX-TRF
(X9DAX-iF, Intel Xeon E5-2697 v2, 2.70 GHz)

SPECint2006 = NC
SPECint_base2006 = NC

CPU2006 license: 001176
Test date: Oct-2013
Test sponsor: Supermicro
Hardware Availability: Sep-2013
Tested by: Supermicro
Software Availability: Sep-2013

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, SPEC was notified that an attempt to reproduce the published result did not come within SPEC's requirements for run-to-run variation. Upon re-review, it was determined that the system configuration does not meet SPEC's requirements for documented and supported systems, and does not meet SPEC's requirements for general availability.

Submit Notes
The config file option 'submit' was used.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

General Notes
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"
OMP_NUM_THREADS = "13"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation
C benchmarks:
  icc -m64
C++ benchmarks:
  icpc -m64
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, SPEC was notified that an attempt to reproduce the published result did not come within SPEC’s requirements for run-to-run variation. Upon re-review, it was determined that the system configuration does not meet SPEC's requirements for documented and supported systems, and does not meet SPEC's requirements for general availability.

### Base Portability Flags

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<td>-DSPEC_CPU_LINUX</td>
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</table>

### Base Optimization Flags

#### C benchmarks:

- xAVX -ipo -no-prec-div -parallel -opt-prefetch -auto-p32
- xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs
- L/sh -lsmartheap64

### Base Other Flags

#### C benchmarks:

403.gcc: -Dalloca=_alloca
Supermicro
SuperServer 6027AX-TRF
(X9DAX-iF, Intel Xeon E5-2697 v2, 2.70 GHz)

SPECint2006 = NC
SPECint_base2006 = NC

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, SPEC was notified that an attempt to reproduce the published result did not come within SPEC's requirements for run-to-run variation. Upon re-review, it was determined that the system configuration does not meet SPEC's requirements for documented and supported systems, and does not meet SPEC's requirements for general availability.

Peak Compiler Invocation

C benchmarks (except as noted below):
  icc -m64
  400.perlbench: icc -m32
  445.gobmk: icc -m32
  464.h264ref: icc -m32

C++ benchmarks (except as noted below):
  icpc -m32
  473.astar: icpc -64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
408.cactuse: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
SPEC CINT2006 Result

Supermicro
SuperServer 6027AX-TRF
(X9DAX-iF, Intel Xeon E5-2697 v2, 2.70 GHz)

SPECint2006 = NC
SPECint_base2006 = NC

CPU2006 license: 001176
Test date: Oct-2013

Test sponsor: Supermicro
Hardware Availability: Sep-2013

Tested by: Supermicro
Software Availability: Sep-2013

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, SPEC was notified that an attempt to reproduce the published result did not come within SPEC's requirements for run-to-run variation. Upon re-review, it was determined that the system configuration does not meet SPEC's requirements for documented and supported systems, and does not meet SPEC's requirements for general availability.

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias

401.bzip2: -xAVX(pass 2) -prof-deptree(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc -opt-allocation-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias

456.hmmer: -ipo -O3 -no-prec-div -unroll12 -auto-ilp32 -ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12 -ansi-alias

C++ benchmarks:

Continued on next page
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, SPEC was notified that an attempt to reproduce the published result did not come within SPEC's requirements for run-to-run variation. Upon re-review, it was determined that the system configuration does not meet SPEC's requirements for documented and supported systems, and does not meet SPEC's requirements for general availability.

Peak Optimization Flags (Continued)

471.omnetpp: -xAVX(pass 2) -prof-use(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-gen(pass 2) -opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap
473.astar: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64
483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

For C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.xml
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