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Platform Notes (Continued)

C States enabled
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d1025 e50a6c4d596a3cee98f191
running on localhost.localdomain Mon Feb 17 10:25:58 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-2850 v2 @ 2.30GHz
  2 "physical id"s (chips)
  48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 12
  siblings : 24
  physical 0: cores 1 2 3 4 5 8 9 10 11 12 13
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
  cache size : 24576 KB

From /proc/meminfo
MemTotal:       529354152 kB
HugePages_Total:       0
Hugepagesize:       2048 kB
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
/usr/bin/lsb_release -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Feb 17 10:25

Non-Compliant
SPEC CFP2006 Result

Dell Inc.

PowerEdge R920 (Intel Xeon E7-2850 v2, 2.30 GHz)

SPECfp2006 = NC
SPECfp_base2006 = NC

CPU2006 license: 55
Test sponsor: Dell Inc.
Test date: Feb-2014
Tested by: Dell Inc.
Hardware Availability: Mar-2014
Software Availability: Aug-2013

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Platform Notes (Continued)

SPEC is set to: /root/cpu2006-1.2
Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda2      ext4  271G  8.6G  249G   4% /
Additional information from dmidecode:
BIOS Dell Inc. 1.0.4 01/27/2014
Memory: 32x 00AD04B300AD HMT42GR7AFR4A-PB 16 GB 1333 MHz 2 rank
(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"
OMP_NUM_THREADS = "24"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.
Transparent Huge Pages disabled with:
echo never > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>       /proc/sys/vm/drop_caches

Base Compiler Invocation

icc   -m64
C++ benchmarks:
icp   -m64
Fortran benchmarks:
ifort -m64
Benchmarks using both Fortran and C:
icc   -m64 ifort -m64

Non-Compliant
Dell Inc.

PowerEdge R920 (Intel Xeon E7-2850 v2, 2.30 GHz)

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**Base Portability Flags**

- 410.bwaves: `-DSPEC_CPU_LP64`
- 416.gamess: `-DSPEC_CPU_LP64`
- 433.milc: `-DSPEC_CPU_LP64`
- 434.zeusmp: `-DSPEC_CPU_LP64`
- 435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
- 436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
- 437.leslie3d: `-DSPEC_CPU_LP64`
- 444.namd: `-DSPEC_CPU_LP64`
- 447.dealII: `-DSPEC_CPU_LP64`
- 450.soplex: `-DSPEC_CPU_LP64`
- 453.povray: `-DSPEC_CPU_LP64`
- 454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
- 459.GemsFDTD: `-DSPEC_CPU_LP64`
- 465.tonto: `-DSPEC_CPU_LP64`
- 470.lbm: `-DSPEC_CPU_LP64`
- 481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`
- 482.sphinx3: `-DSPEC_CPU_LP64`

**Base Optimization Flags**

- C benchmarks:
  
  `icc -m64`

- C++ benchmarks:
  
  `-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias`

- Fortran benchmarks:
  
  `-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch`

- Benchmarks using both Fortran and C:
  
  `-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias`

**Peak Compiler Invocation**

- C benchmarks:
  
  `icc -m64`

---

Non-compliant
Dell Inc.
PowerEdge R920 (Intel Xeon E7-2850 v2, 2.30 GHz)  SPECfp2006 = NC
SPECfp_base2006 = NC

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Peak Compiler Invocation (Continued)

C++ benchmarks:
   icpc  -m64

Fortran benchmarks:
   ifort  -m64

Benchmarks using both Fortran and C:
   icc   -m64 ifort  -m64

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
   433.milc: --prefix  -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) 
             -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32 
             -ansi-alias
   471.lbm: basepeak = yes

   492.hiop:  -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias 
             parallel

C++ benchmarks:
   444.amd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) 
            -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias 
            -auto-ilp32

   447.dealII: basepeak = yes
   450.soplex: basepeak = yes

Continued on next page

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes
416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=1 -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-calloc
-spec-all-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes
454.calculix: -xAVX -ipo -O3 -prec-div -auto-ilp32 -ansi-alias
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.html
# SPEC CFP2006 Result

**Dell Inc.**

PowerEdge R920 (Intel Xeon E7-2850 v2, 2.30 GHz)

<table>
<thead>
<tr>
<th>SPECfp2006 =</th>
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</tr>
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## CPU2006 license: 55
- **Test sponsor:** Dell Inc.
- **Tested by:** Dell Inc.
- **Test date:** Feb-2014
- **Hardware Availability:** Mar-2014
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You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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