SPEC® CINT2006 Result

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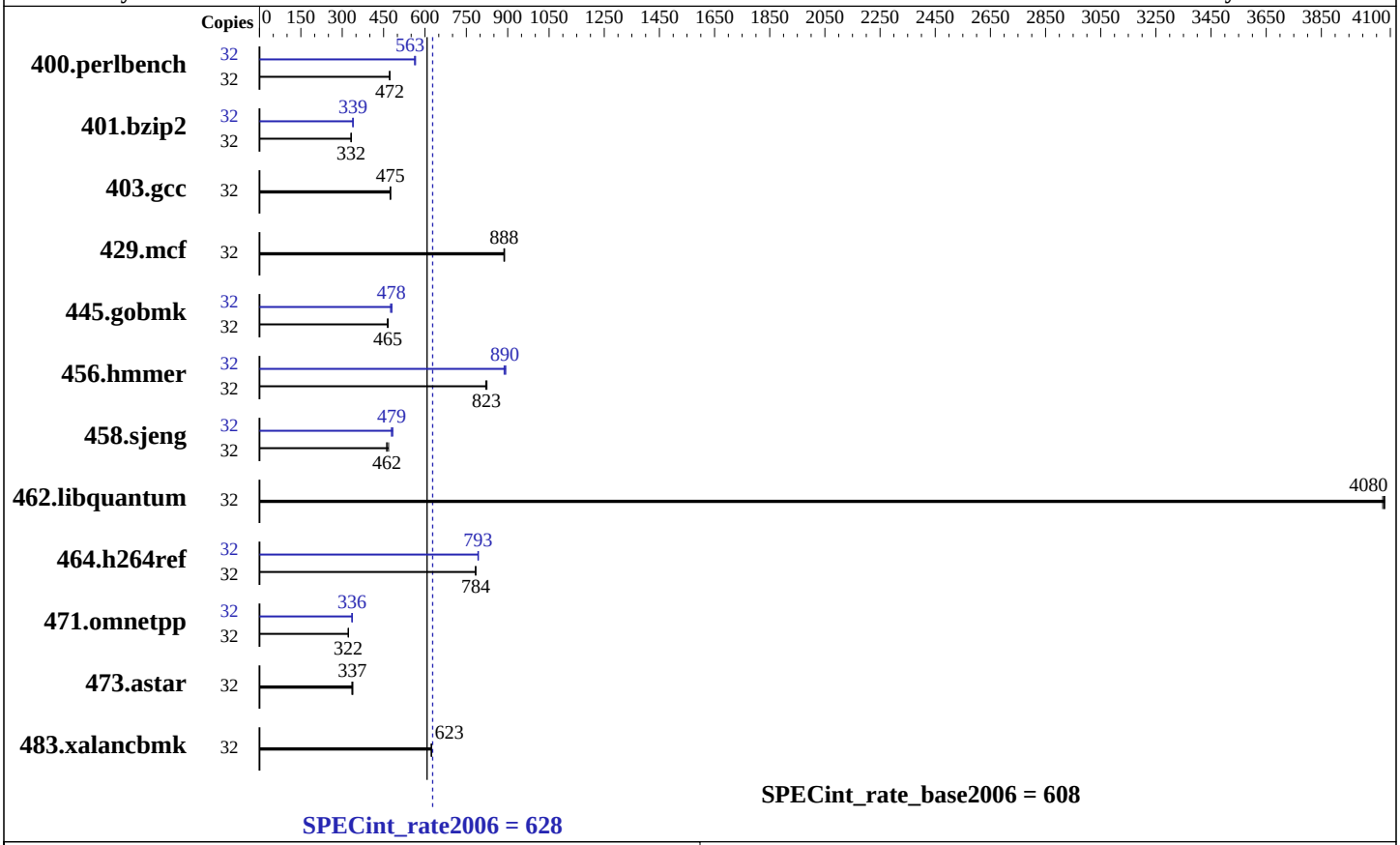
Huawei
Huawei Tecal RH2285H V2

SPECint®_rate2006 = 628

SPECint_rate_base2006 = 608

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Mar-2014
Hardware Availability: Jan-2014
Software Availability: Nov-2013



Hardware

CPU Name:	Intel Xeon E5-2450 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	20 MB I+D on chip per chip
Other Cache:	None
Memory:	96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem:	1 x 500 GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-431.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	661	473	663	471	**662**	**472**	32	552	566	556	563	**555**	**563**
401.bzip2	32	928	333	**929**	**332**	930	332	32	910	339	911	339	**911**	**339**
403.gcc	32	542	475	544	473	**542**	**475**	32	542	475	544	473	**542**	**475**
429.mcf	32	328	889	**329**	**888**	329	886	32	328	889	**329**	**888**	329	886
445.gobmk	32	724	464	**722**	**465**	720	467	32	**702**	**478**	699	480	706	475
456.hmmer	32	364	820	362	825	**363**	**823**	32	334	893	**335**	**890**	337	887
458.sjeng	32	827	468	839	462	**838**	**462**	32	809	479	**808**	**479**	801	483
462.libquantum	32	**163**	**4080**	163	4080	163	4070	32	**163**	**4080**	163	4080	163	4070
464.h264ref	32	903	784	**903**	**784**	904	783	32	894	792	892	794	**893**	**793**
471.omnetpp	32	621	322	620	323	**621**	**322**	32	596	335	**596**	**336**	594	336
473.astar	32	664	338	671	335	**666**	**337**	32	664	338	671	335	**666**	**337**
483.xalancbmk	32	354	624	**354**	**623**	355	623	32	354	624	**354**	**623**	355	623

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
Set Power Efficiency Mode to Performance
Sysinfo program /spec/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on localhost.localdomain Sat Mar 29 04:04:56 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2450 v2 @ 2.50GHz
      2 "physical id"s (chips)
      32 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
```

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Platform Notes (Continued)

```
        cpu cores : 8
        siblings  : 16
        physical 0: cores 0 1 2 3 4 5 6 7
        physical 1: cores 0 1 2 3 4 5 6 7
     cache size : 20480 KB

  From /proc/meminfo
     MemTotal:       99010156 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     Red Hat Enterprise Linux Server release 6.5 (Santiago)

  From /etc/*release* /etc/*version*
     redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
     system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

  uname -a:
     Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
     EST 2013 x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Mar 29 04:01

  SPEC is set to: /spec
     Filesystem                                                          Type
     Size  Used Avail Use% Mounted on
     /dev/mapper/ddf1_4c53492020202020100000619e5d2034711471157d3ceb3p2 ext4
     260G   12G  235G   5% /

  Additional information from dmidecode:
    BIOS Insyde Corp. RMIBV365 09/06/2013
    Memory:
     12x Micron 36JSF1G72PZ-1G6K1 8 GB 1600 MHz 2 rank

  (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RedHat EL 6.4
  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
  Filesystem page cache cleared with:
  echo 1>       /proc/sys/vm/drop_caches
  runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
```

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General Notes (Continued)

```
The Huawei RH2285H v2 and Huawei RH2285 v2 models are electronically equivalent.
The results have been measured on a Huawei RH2285H v2 model.
```

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

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Peak Compiler Invocation (Continued)

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

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Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml

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For other inquiries, please contact webmaster@spec.org.

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