SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.

Non-Compliant


SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.

Results Table

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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Non-compliant
Huawei

Huawei RH5885H v3 (Intel Xeon E7-4809 v2)

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Lock_step to disabled
Baseboard Management Controller used to adjust the fan speed to 100%
Sysinfo program /spec/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on speccpu Sat Apr 12 01:55:27 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-4809 v2 @ 1.90GHz
 4 "physical id"s (chips)
 48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores
siblings : 12
physical 1: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
cachesize : 12288 KB

From /proc/mminfo
HugePages_Total: 529107256 kB
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)

uname -a:

Continued on next page
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.

Platform Notes (Continued)

Linux speccpu 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Apr 12 01:53
SPEC is set to: /spec
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 ext4 259G 13G 233G 6% /
Additional information from dmidecode:
BIOS American Megatrends Inc. BLISV099 02/09/2014
Memory:
64x 8 GB
32x NO DIMM NO DIMM
27x Samsung M393B1K70CH0-CH9 8 GB 1333 MHz 2 rank
37x Samsung M393B1K70CD0-CH9 8 GB 1333 MHz 2 rank

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
umactl --interleave=all runspec <etc>
Huawei

Huawei RH5885H v3 (Intel Xeon E7-4809 v2)

SPECint\_rate2006 = NC
SPECint\_rate\_base2006 = NC

CPU\_2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei
Test date: Apr-2014
Hardware Availability: Feb-2014
Software Availability: Nov-2013

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.

### Base Compiler Invocation

**C benchmarks:**

\texttt{icc -m32}

**C++ benchmarks:**

\texttt{icpc -m32}

### Base Portability Flags

\begin{itemize}
\item 400.perlbench: -DSPEC\_CPU\_LINUX\_IA32
\item 462.libquantum: -DSPEC\_CPU\_LINUX
\item 483.xalancbmk: -DSPEC\_CPU\_LINUX
\end{itemize}

### Base Optimization Flags

**C benchmarks:**

\begin{itemize}
\item -xSSE4.2 -ipo -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
\end{itemize}

**C++ benchmarks:**

\begin{itemize}
\item -xSSE4.2 -ipo -3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
\item -Wl,-z,muldefs -L/sh -lsmartheap
\end{itemize}

### Base Other Flags

**C benchmarks:**

\begin{itemize}
\item 403.gcc: -Dalloca=_alloca
\end{itemize}

### Peak Compiler Invocation

C benchmarks (except as noted below):

\texttt{icc -m32}

Continued on next page
Huawei

Huawei RH5885H v3 (Intel Xeon E7-4809 v2)

SPECint_rate2006 = NC
SPECint_rate_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Apr-2014
Hardware Availability: Feb-2014
Software Availability: Nov-2013

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.

Peak Compiler Invocation (Continued)

400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias
403.gcc: basepeak = yes
429.mcf: basepeak = yes

Continued on next page
Huawei RH5885H v3 (Intel Xeon E7-4809 v2)

SPECint_rate2006 = NC
SPECint_rate_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Apr-2014
Hardware Availability: Feb-2014
Software Availability: Nov-2013

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.

Peak Optimization Flags (Continued)
445.gobmk: -xSSE4.2 (pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=pos=3

456.hmmer: -xSSE4.2 -ipo -O3 -prof-use -unroll12 -auto-ilp32

458.sjeng: -xSSE4.2 (pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-dv(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2 (pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:
471.omnetpp: -xSSE4.2 (pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-1/sh = samcheap

473.astar: basepeak = yes

483.xalanbmk: basepeak = yes

Peak Other Flags
C benchmarks:
483.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html

Non-Compliant
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.