SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.
Huawei
Huawei CH242 v3

<table>
<thead>
<tr>
<th>SPECfp2006 = NC</th>
<th>SPECfp_base2006 = NC</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU2006 license: 3175</td>
<td>Test date: Apr-2014</td>
</tr>
<tr>
<td>Test sponsor: Huawei</td>
<td>Hardware Availability: Feb-2014</td>
</tr>
<tr>
<td>Tested by: Huawei</td>
<td>Software Availability: Nov-2013</td>
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### Hardware

<table>
<thead>
<tr>
<th>Field</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon E7-4820 v2</td>
</tr>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 2.50 GHz</td>
</tr>
<tr>
<td>CPU MHz</td>
<td>2000</td>
</tr>
<tr>
<td>FPU</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled</td>
<td>32 cores, 4 chips, 8 cores/chip</td>
</tr>
<tr>
<td>CPU(s) orderable</td>
<td>2.4 chip</td>
</tr>
<tr>
<td>Primary Cache</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache</td>
<td>256 KB I+D on chip per core</td>
</tr>
<tr>
<td>L3 Cache</td>
<td>16 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other Cache</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>256 GB (32 x 8 GB 2Rx4, 2C3-10600R-09, ECC)</td>
</tr>
<tr>
<td>Disk Subsystem</td>
<td>1 X 300 GB SAS 10000RPM</td>
</tr>
<tr>
<td>Other Hardware</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Field</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating System</td>
<td>Red Hat Enterprise Linux Server release 6.5 (Santiago)</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux</td>
</tr>
<tr>
<td>Auto Parallel</td>
<td>Yes</td>
</tr>
<tr>
<td>File System</td>
<td>ext4</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other Software</td>
<td>None</td>
</tr>
</tbody>
</table>

Non-Compliant
Huawei
Huawei CH242 v3

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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
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<td></td>
</tr>
</tbody>
</table>

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set VMSE LockStep mode disable
Set Intel HT Technology Disable
Sysinfo program /spec/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
Huawei
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Platform Notes (Continued)

running on localhost.localdomain Tue Apr 29 01:35:46 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
4 "physical id"s (chips)
32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 8
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
physical 2: cores 0 1 2 3 4 5 6 7
physical 3: cores 0 1 2 3 4 5 6 7
cache size : 16384 KB

From /proc/meminfo
MemTotal: 264360280 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
Redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Apr 28 13:13
SPEC is set to: /spec

Non-Compliant
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Peak Compiler Invocation (Continued)

C++ benchmarks:
   icpc -m64

Fortran benchmarks:
   ifort -m64

Benchmarks using both Fortran and C:
  icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
   433.milc: -m64 -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
   -ansi-alias

47.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
parallel

C++ benchmarks:
   444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
   -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

Continued on next page
### SPEC CFP2006 Result

**Huawei**  
**Huawei CH242 v3**  

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### Peak Optimization Flags (Continued)

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll24 -ansi-alias

**Fortran benchmarks:**

410.bwaves: basepeak = yes  
416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level 1 -scalar-rep-

434.zeusmp: basepeak = yes  
437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level 1 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-special options=3 -auto -unroll4

**Benchmarks using both Fortran and C:**

435.gromacs: basepeak = yes  
436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html  
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You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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