SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.
Huawei RH5885H v3 (Intel Xeon E7-4850 v2)

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.

Operating System Notes
- Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes
- BIOS configuration:
  - Set Power Efficiency Mode to Performance
  - Set Lock_step to disabled
  - Baseboard Management Controller used to adjust the fan speed to 100%
  - Set Intel Hyper Threading to disabled
  - Sysinfo program /spec/config/sysinfo.rev6800
  
Continued on next page

Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
Huawei

Huawei RH5885H v3 (Intel Xeon E7-4850 v2)

SPECint2006 = NC
SPECint_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.

Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-4850 v2 @ 2.30GHz
  4 "physical id"s (chips)
  48 "processors"
cores, siblings (Caution: counting this is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 12
siblings : 12
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 24576 KB

From /proc/meminfo
MemTotal:       529107272 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

/run/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)

uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 12 16:26

SPEC is set to: /spec
Filesystem     Type  Size  Used  Avail  Use%  Mounted on
/dev/sda2      ext4  154G  30G  117G  21% /
Huawei RH5885H v3 (Intel Xeon E7-4850 v2)

**SPEC CINT2006 Result**

**SPECint2006 =** NC

**SPECint_base2006 =** NC

**CPU2006 license:** 3175

**Test sponsor:** Huawei

**Test date:** May-2014

**Hardware Availability:** Feb-2014

**Tested by:** Huawei

**Software Availability:** Nov-2013

**CPU2006 license:** 3175

**Test sponsor:** Huawei

**Test date:** May-2014

**Hardware Availability:** Feb-2014

**Tested by:** Huawei

**Software Availability:** Nov-2013

**SPEC has determined that this result is not in compliance with the SPEC**

**CPU2006 run and reporting rules. Specifically, the submitter has notified**

**SPEC that the system was run with a BIOS which included a version of the**

**Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.**

**Platform Notes (Continued)**

Additional information from dmidecode:
- **Memory:**
  - 10x Samsung M393B1K70CH0-CH9 8 GB 1333 MHz 2 rank
  - 54x Samsung M393B1K70DH0-CH9 8 GB 1333 MHz 2 rank

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have two lines reading as:
- 10x Samsung M393B1K70CH0-CH9 8 GB 1333 MHz 2 rank
- 54x Samsung M393B1K70CH0-CH9 8 GB 1333 MHz 2 rank

**General Notes**

Environment variables set by runspec before the start of the run:
- **KMP_AFFINITY** = "granularity=fine,compact,0,1"
- **LD_LIBRARY_PATH** = "/spec/libs/32:/spec/libs/64"
- **OMP_NUM_THREADS** = "48"

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory using RHEL 6.1

Transparent huge pages enabled with:
- `echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled`

**Base Compiler Invocation**

C benchmarks:
- `icc -m64`

C++ benchmarks:
- `icpc -m64`

**Base Portability Flags**

400.perlbench: `-DSPEC_CPU_LP64` `-DSPEC_CPU_LINUX_X64`

Non compliant
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.
Huawei

Huawei RH5885H v3 (Intel Xeon E7-4850 v2)

SPECint2006 = NC
SPECint_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: May-2014
Hardware Availability: Feb-2014
Software Availability: Nov-2013

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.

Peak Compiler Invocation (Continued)

445.gobmk: icc -m32
464.h264ref: icc -m32

C++ benchmarks (except as noted below):
icc -m32
473.astar: icc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX=
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias
403.gcc: -xAVX -ipo -03 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

Continued on next page
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.

Peak Optimization Flags (Continued)

429.mcf: basepeak = yes
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32 -ansi-alias
458.sjeng: basepeak = yes
462.libquantum: basepeak = yes
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:
471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs -L/smartheap -lsmartheap
473.astar: basepeak = yes
483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

403.gcc: -Dalloca=_alloca
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.

The flags files that were used to format this result can be browsed at:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml