SPEC® CINT2006 Result

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Hewlett-Packard Company

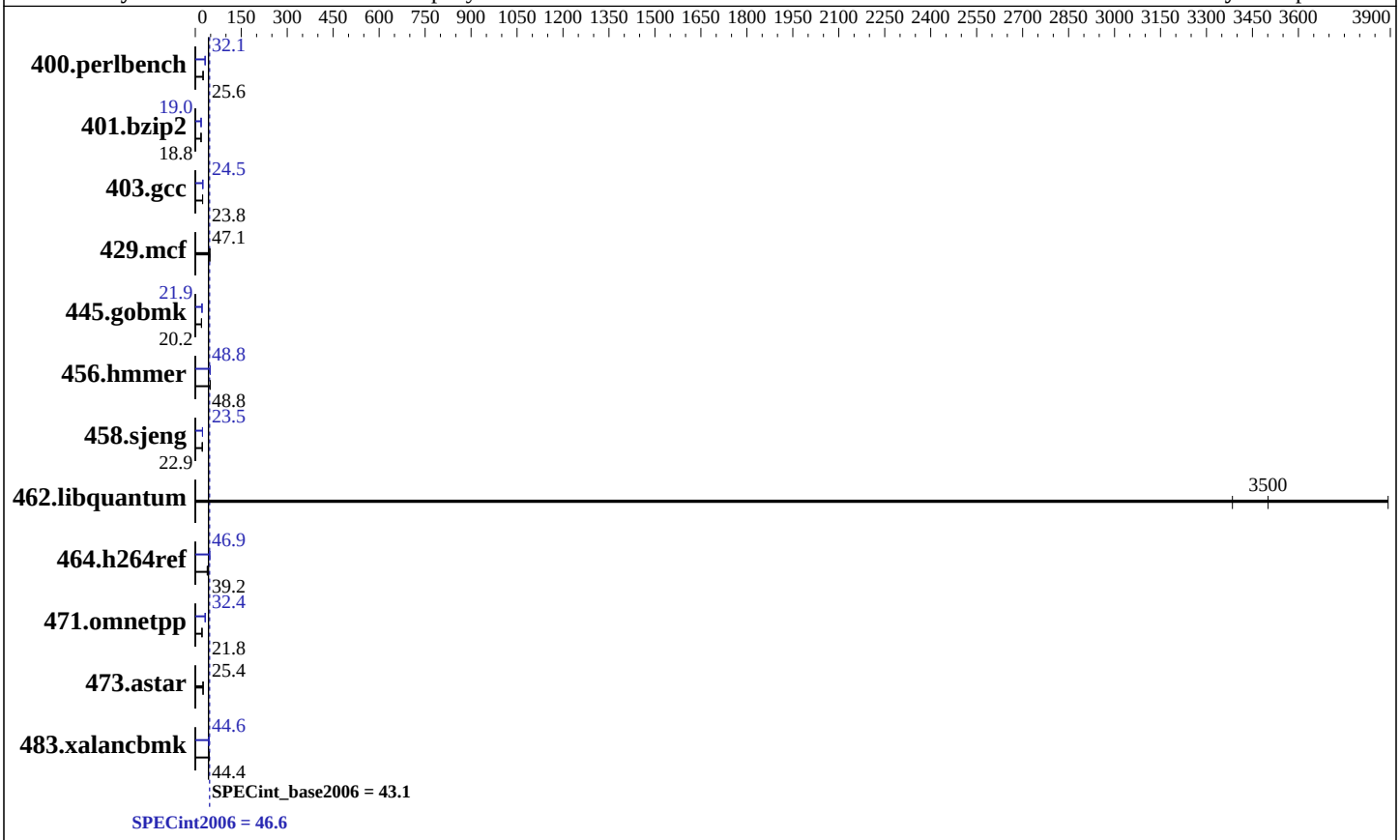
ProLiant DL580 Gen8
(2.30 GHz, Intel Xeon E7-4850 v2)

SPECint®2006 = 46.6

SPECint_base2006 = 43.1

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: May-2014
Hardware Availability: Feb-2014
Software Availability: Sep-2013



Hardware

CPU Name:	Intel Xeon E7-4850 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	48 cores, 4 chips, 12 cores/chip
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	24 MB I+D on chip per chip
Other Cache:	None
Memory:	1 TB (64 x 16 GB 2Rx4 PC3-14900R-13, ECC, running at 1333 MHz and CL9)
Disk Subsystem:	1 x 400 GB SSD SAS, RAID 0
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP3 Kernel 3.0.76-0.11-default
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	381	25.6	**382**	**25.6**	389	25.1	**304**	**32.1**	304	32.1	304	32.2
401.bzip2	**515**	**18.8**	515	18.7	514	18.8	509	18.9	**509**	**19.0**	509	19.0
403.gcc	339	23.8	337	23.9	**338**	**23.8**	329	24.5	328	24.5	**329**	**24.5**
429.mcf	193	47.3	194	47.0	**194**	**47.1**	193	47.3	194	47.0	**194**	**47.1**
445.gobmk	**519**	**20.2**	519	20.2	518	20.2	479	21.9	**479**	**21.9**	479	21.9
456.hmmer	191	48.8	192	48.6	**191**	**48.8**	191	48.8	192	48.5	**191**	**48.8**
458.sjeng	529	22.9	528	22.9	**528**	**22.9**	515	23.5	515	23.5	**515**	**23.5**
462.libquantum	**5.92**	**3500**	6.12	3380	5.32	3890	**5.92**	**3500**	6.12	3380	5.32	3890
464.h264ref	565	39.1	**565**	**39.2**	563	39.3	**472**	**46.9**	472	46.9	473	46.8
471.omnetpp	285	21.9	288	21.7	**287**	**21.8**	192	32.5	**193**	**32.4**	194	32.2
473.astar	276	25.5	277	25.4	**276**	**25.4**	276	25.5	277	25.4	**276**	**25.4**
483.xalancbmk	154	44.7	156	44.3	**155**	**44.4**	155	44.5	155	44.6	**155**	**44.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
 echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
Disabled unused Linux services through "stop_services.sh" before running.
```

Platform Notes

```
 Intel Hyperthreading Options set to Disabled
 HP Power Profile set to Maximum Performance
 Minimum Processor Idle Power Core State set to C1E State to Enabled
 Minimum Processor Idle Power Packages State set to Package C6 (non-retention) State
 Collaborative Power Control set to Disabled
 Thermal Configuration set to Maximum Cooling
 Processor Power and Utilization Monitoring set to Disabled
 Memory Refresh Rate set to Disabled

Sysinfo program /cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on DL580-Gen8-sr Sat May 10 00:11:04 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo
```

Continued on next page

Platform Notes (Continued)

```
From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E7-4850 v2 @ 2.30GHz
      4 "physical id"s (chips)
      48 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 12
      siblings  : 12
      physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13
   cache size : 24576 KB

From /proc/meminfo
   MemTotal:       1058855444 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 11 (x86_64)
      VERSION = 11
      PATCHLEVEL = 3

uname -a:
   Linux DL580-Gen8-sr 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
   (ccab990) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 10 00:06 last=S

SPEC is set to: /cpu2006
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda3      ext3  365G   15G  332G   5% /

Additional information from dmidecode:
  BIOS HP P79 02/21/2014
  Memory:
   64x HP 712383-081 16 GB 1333 MHz
   32x UNKNOWN NOT AVAILABLE

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of
memory is 1 TB and the dmidecode description should have one line reading as:
   64x HP 712383-081 16 GB 1333 MHz 2 rank
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"
OMP_NUM_THREADS = "48"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4

Assuming that the memory populations rules found in the DL580 Gen8
QuickSpecs are followed, HP supports memory running at 1333 MHz on
the E7-4850 v2, E7-4830 v2, E7-4820 v2, or E7-4809 v2 processors with
any BIOS prior to the 1.03_06-27-2014 ROM. Any BIOS that is the
1.03_06-27-2014 ROM or later, does not support the memory running at
1333 MHz due to a change in the Intel MRC (Memory Reference Code).
```

Base Compiler Invocation

C benchmarks:
```
icc  -m64
```

C++ benchmarks:
```
icpc -m64
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
445.gobmk: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
471.omnetpp: `-DSPEC_CPU_LP64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-opt-prefetch -ansi-alias`

401.bzip2: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div -prof-use`(pass 2) `-auto-ilp32`
`-opt-prefetch -ansi-alias`

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Peak Optimization Flags (Continued)

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-prof-use`(pass 2) `-ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32 -ansi-alias`

458.sjeng: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4`

462.libquantum: `basepeak = yes`

464.h264ref: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html`
`http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revD.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml`
`http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revD.xml`

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