SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.

---

Non-Compliant
SPEC CINT2006 Result

Huawei
Huawei CH242 v3

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Huawei
Huawei CH242 v3

SPECint2006 = NC
SPECint_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei
Test date: May-2014
Hardware Availability: Jan-2014
Software Availability: Nov-2013

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.

Memory: 256 GB (32 x 8 GB 2Rx4 PC3-10600R-09, ECC)
Disk Subsystem: 1 X 300 GB SAS 10000RPM
Other Software: Microquill SmartHeap V9.01

Results Table

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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set VMSE LockStep mode disable
Sysinfo program /spec/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 $# 6f2ebdf5032aaa42ec583f96b07f99d3
running on localhost.localdomain Sat May 24 13:25:01 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

Continued on next page
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.

Platform Notes (Continued)

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-4830 v2 @ 2.20GHz
4 "physical id"s (chips)
40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 10
physical 0: cores 0 2 3 4 8 9 10 11 12
physical 1: cores 0 2 3 4 8 9 10 11 12
physical 2: cores 0 2 3 4 8 9 10 11 12
physical 3: cores 0 2 3 4 8 9 10 11 12
cache size : 20480 KB

From /proc/meminfo
MemTotal: 264359288 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)

uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 24 13:00

SPEC is set to: /spec
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 ext4 255G 8.0G 234G 4% /

Additional information from dmidecode:
Memory:

Continued on next page
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### Base Portability Flags (Continued)

- 464.h264ref: -DSPEC_CPU_LP64
- 471.omnetpp: -DSPEC_CPU_LP64
- 473.astar: -DSPEC_CPU_LP64
- 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

### Base Optimization Flags

**C benchmarks:**
- -xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

**C++ benchmarks:**
- -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
- -Wl,-z,muldefs -L/smartheap -lsmartheap64

### Base Other Flags

**C benchmarks:**
- 403.gcc: -Dalloca=_alloca

### Peak Compiler Invocation

**C benchmarks (except as noted below):**
- icc -m64
- 400.perlbench: icc -m32
- 445.gobmk: icc -m32
- 464.h264ref: icc -m32

**C++ benchmarks (except as noted below):**
- icpc -m32
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.

Peak Compiler Invocation (Continued)

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
    -opt-prefetch -ansi-alias
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
    -opt-prefetch -ansi-alias
403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-cALLOC
    -opt-malloc-options=3 -auto-ilp32
429.mcf: basepeak = yes
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
    -ansi-alias
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
    -ansi-alias

Continued on next page
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.

Peak Optimization Flags (Continued)

458.sjeng: basepeak = yes
462.libquantum: -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
464.h264ref: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:
471.omnetpp: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs -L/smartheap -lsmartheap
473.astar: basepeak = yes
483.xalancbmk: -xsse4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

492. -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml
### SPEC CINT2006 Result

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<tbody>
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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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