



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

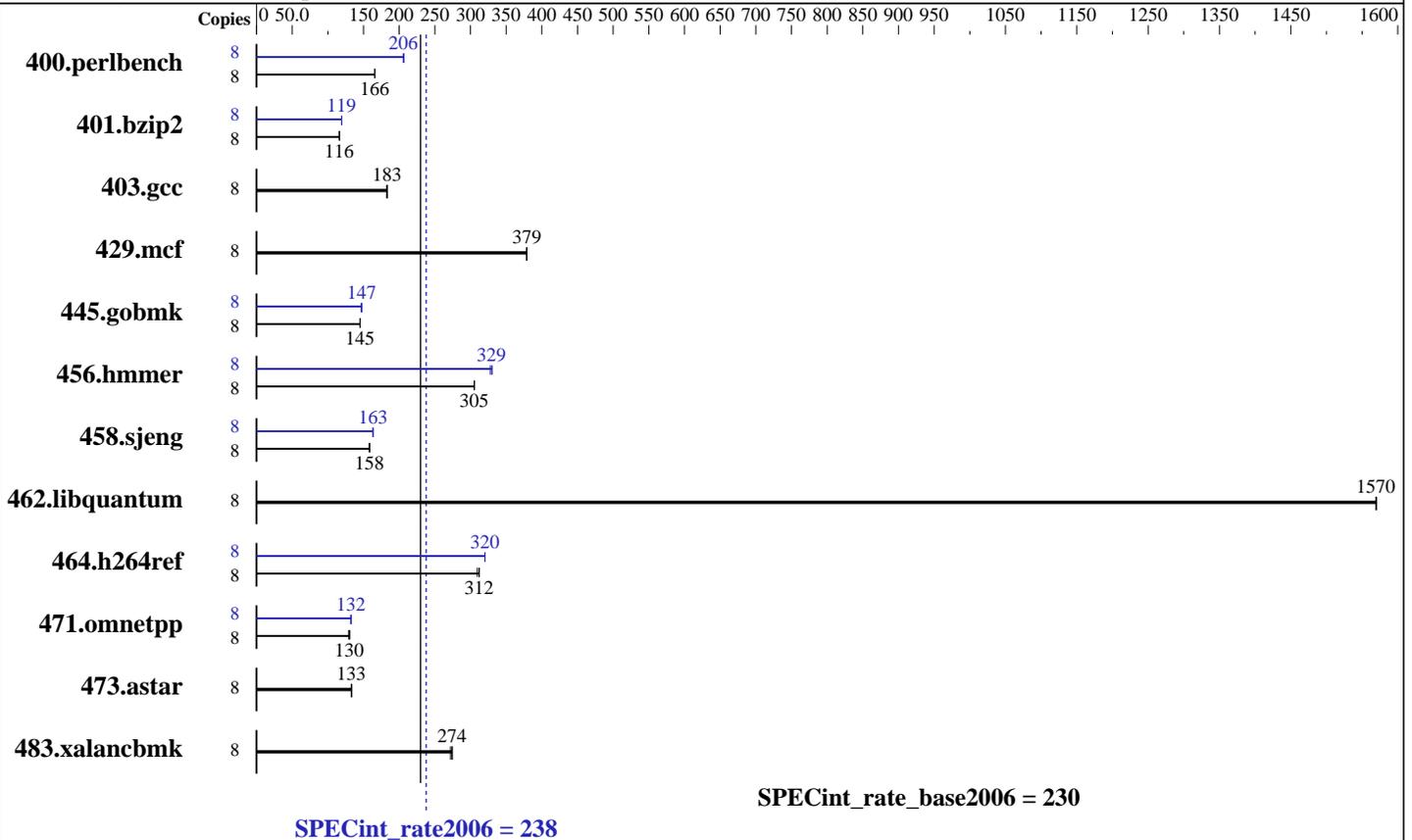
IBM System x3630 M4
(Intel Xeon E5-2407 v2, 2.40 GHz)

SPECint®_rate2006 = 238

SPECint_rate_base2006 = 230

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: May-2014
Hardware Availability: Mar-2014
Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E5-2407 v2
 CPU Characteristics:
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 10 MB I+D on chip per chip
 Other Cache: None
 Memory: 192 GB (12 x 16 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)
 Disk Subsystem: 1 x 2 TB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

IBM System x3630 M4
(Intel Xeon E5-2407 v2, 2.40 GHz)

SPECint_rate2006 = 238

SPECint_rate_base2006 = 230

CPU2006 license: 11

Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: May-2014

Hardware Availability: Mar-2014

Software Availability: Nov-2013

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	472	166	471	166	<u>471</u>	<u>166</u>	8	<u>379</u>	<u>206</u>	379	206	378	207
401.bzip2	8	664	116	<u>664</u>	<u>116</u>	666	116	8	646	119	<u>647</u>	<u>119</u>	648	119
403.gcc	8	352	183	352	183	<u>352</u>	<u>183</u>	8	352	183	352	183	<u>352</u>	<u>183</u>
429.mcf	8	193	379	<u>193</u>	<u>379</u>	193	379	8	193	379	<u>193</u>	<u>379</u>	193	379
445.gobmk	8	<u>578</u>	<u>145</u>	579	145	578	145	8	571	147	<u>570</u>	<u>147</u>	569	147
456.hammer	8	<u>244</u>	<u>305</u>	244	305	244	305	8	226	330	<u>227</u>	<u>329</u>	228	328
458.sjeng	8	<u>611</u>	<u>158</u>	611	158	611	158	8	594	163	<u>593</u>	<u>163</u>	593	163
462.libquantum	8	106	1570	<u>106</u>	<u>1570</u>	106	1570	8	106	1570	<u>106</u>	<u>1570</u>	106	1570
464.h264ref	8	<u>567</u>	<u>312</u>	572	310	567	312	8	<u>553</u>	<u>320</u>	553	320	553	320
471.omnetpp	8	<u>384</u>	<u>130</u>	387	129	383	131	8	378	132	<u>378</u>	<u>132</u>	376	133
473.astar	8	<u>422</u>	<u>133</u>	423	133	421	133	8	<u>422</u>	<u>133</u>	423	133	421	133
483.xalancbmk	8	201	274	<u>201</u>	<u>274</u>	203	272	8	201	274	<u>201</u>	<u>274</u>	203	272

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Zone reclaim mode enabled with:
echo 1 > /proc/sys/vm/zone_reclaim_mode

Platform Notes

BIOS setting:
Operating Mode set to Maximum Performance
Sysinfo program /home/SPECcpu-20140116-ic14.0/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on x3630M4 Fri May 16 18:39:40 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2407 v2 @ 2.40GHz
2 "physical id"s (chips)
8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint_rate2006 = 238

IBM System x3630 M4
(Intel Xeon E5-2407 v2, 2.40 GHz)

SPECint_rate_base2006 = 230

CPU2006 license: 11

Test date: May-2014

Test sponsor: IBM Corporation

Hardware Availability: Mar-2014

Tested by: IBM Corporation

Software Availability: Nov-2013

Platform Notes (Continued)

following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 4
siblings : 4
physical 0: cores 0 1 2 3
physical 1: cores 0 1 2 3
cache size : 10240 KB
```

From /proc/meminfo

```
MemTotal: 198464976 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

/usr/bin/lsb_release -d

```
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

From /etc/*release* /etc/*version*

```
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux x3630M4 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 May 15 11:24

SPEC is set to: /home/SPECcpu-20140116-ic14.0

```
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/vg_x3630m4-lv_home ext4 1.8T 11G 1.7T 1% /home
```

Additional information from dmidecode:

```
BIOS IBM -[BEE135VUS-1.60]- 01/14/2014
```

Memory:

```
12x Samsung M393B2G70QH0-YK0 16 GB 1333 MHz 2 rank
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/SPECcpu-20140116-ic14.0/libs/32:/home/SPECcpu-20140116-ic14.0/libs/64:/home/SPECcpu-20140116-ic14.0/sh"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

IBM System x3630 M4
(Intel Xeon E5-2407 v2, 2.40 GHz)

SPECint_rate2006 = 238

SPECint_rate_base2006 = 230

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: May-2014
Hardware Availability: Mar-2014
Software Availability: Nov-2013

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint_rate2006 = 238

IBM System x3630 M4
(Intel Xeon E5-2407 v2, 2.40 GHz)

SPECint_rate_base2006 = 230

CPU2006 license: 11

Test date: May-2014

Test sponsor: IBM Corporation

Hardware Availability: Mar-2014

Tested by: IBM Corporation

Software Availability: Nov-2013

Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
            -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -unroll2 -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
              -L/sh -lsmartheap

473.astar: basepeak = yes

```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

IBM System x3630 M4
(Intel Xeon E5-2407 v2, 2.40 GHz)

SPECint_rate2006 = 238

SPECint_rate_base2006 = 230

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: May-2014
Hardware Availability: Mar-2014
Software Availability: Nov-2013

Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-B.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-B.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 23:40:47 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 18 June 2014.