# SPEC® CINT2006 Result

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## Cisco Systems

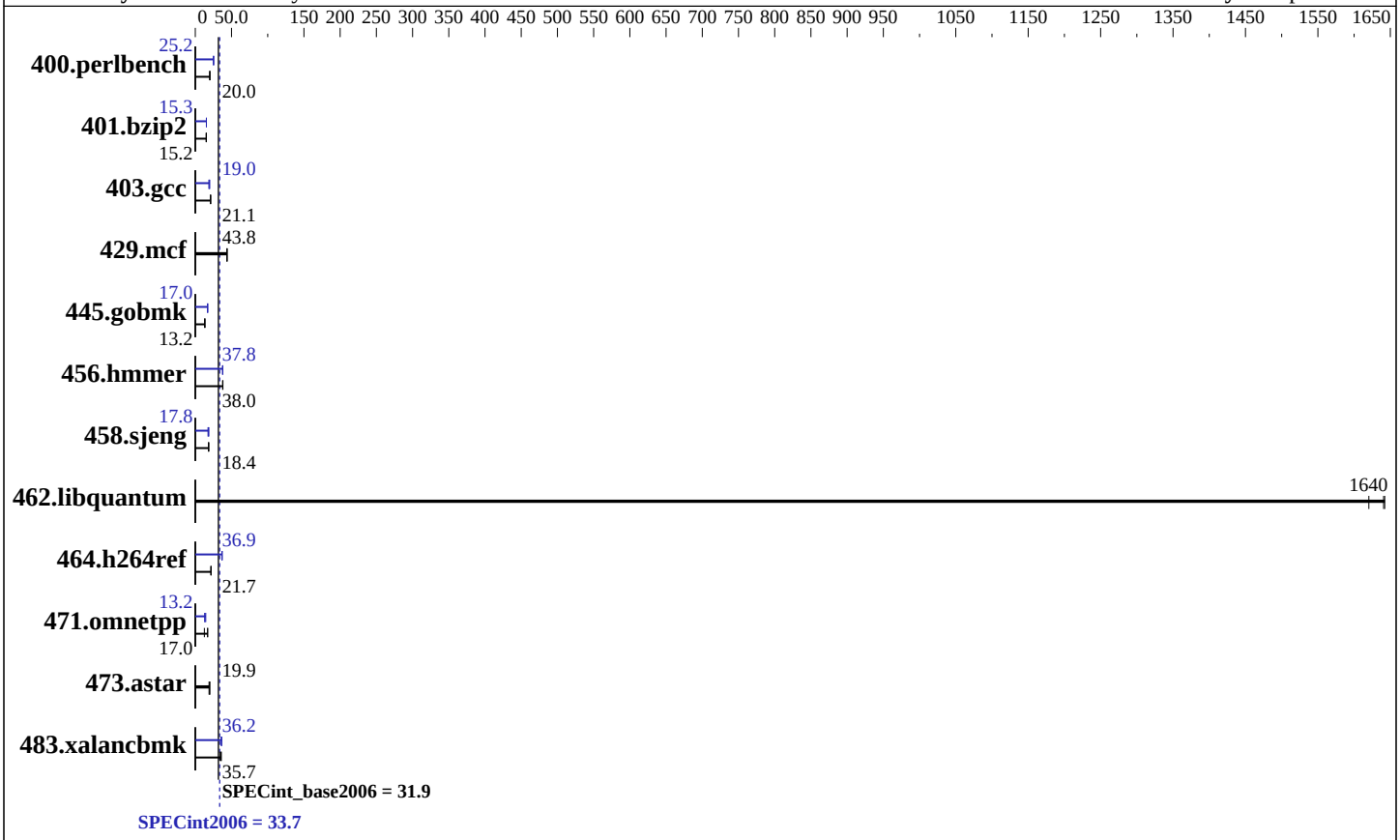
Cisco UCS B420 M3 (Intel Xeon E5-4603 v2, 2.20 GHz)

SPECint®2006 = 33.7

SPECint_base2006 = 31.9

**CPU2006 license:** 9019
**Test sponsor:** Cisco Systems
**Tested by:** Cisco Systems

**Test date:** Jun-2014
**Hardware Availability:** Dec-2013
**Software Availability:** Apr-2014




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-4603 v2 |
| CPU Characteristics: | |
| CPU MHz: | 2200 |
| FPU: | Integrated |
| CPU(s) enabled: | 16 cores, 4 chips, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2,3,4 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 10 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 256 GB (32 x 8 GB 2Rx4 PC3-14900R-13, ECC, running at 1333 MHz and CL9) |
| Disk Subsystem: | 1 X 300 GB 15000 RPM SAS |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.4 (Santiago)<br>2.6.32-358.el6.x86_64 |
| Compiler: | C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux |
| Auto Parallel: | Yes |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32/64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V10.0 |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 486 | 20.1 | 488 | 20.0 | **488** | **20.0** | **388** | **25.2** | 385 | 25.4 | 389 | 25.1 |
| 401.bzip2 | 636 | 15.2 | **636** | **15.2** | 636 | 15.2 | **630** | **15.3** | 629 | 15.3 | 630 | 15.3 |
| 403.gcc | 381 | 21.1 | **381** | **21.1** | 382 | 21.1 | 426 | 18.9 | **423** | **19.0** | 403 | 20.0 |
| 429.mcf | **208** | **43.8** | 208 | 43.7 | 208 | 43.8 | **208** | **43.8** | 208 | 43.7 | 208 | 43.8 |
| 445.gobmk | **797** | **13.2** | 797 | 13.2 | 796 | 13.2 | **617** | **17.0** | 616 | 17.0 | 618 | 17.0 |
| 456.hmmer | **245** | **38.0** | 245 | 38.1 | 248 | 37.7 | **247** | **37.8** | 247 | 37.8 | 246 | 37.9 |
| 458.sjeng | 658 | 18.4 | **657** | **18.4** | 657 | 18.4 | **679** | **17.8** | 684 | 17.7 | 645 | 18.8 |
| 462.libquantum | 12.6 | 1640 | **12.6** | **1640** | 12.8 | 1620 | 12.6 | 1640 | **12.6** | **1640** | 12.8 | 1620 |
| 464.h264ref | 1021 | 21.7 | **1021** | **21.7** | 1021 | 21.7 | 599 | 36.9 | 599 | 36.9 | **599** | **36.9** |
| 471.omnetpp | 366 | 17.1 | 500 | 12.5 | **368** | **17.0** | 476 | 13.1 | **474** | **13.2** | 432 | 14.5 |
| 473.astar | **353** | **19.9** | 353 | 19.9 | 370 | 19.0 | **353** | **19.9** | 353 | 19.9 | 370 | 19.0 |
| 483.xalancbmk | 204 | 33.9 | **193** | **35.7** | 193 | 35.7 | 190 | 36.3 | **190** | **36.2** | 192 | 36.0 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
Intel HT Technology = Enabled
CPU performance set to HPC
Power Technology set to Custom
CPU Power State C6 set to Disabled
CPU Power State C1 Enhanced set to Disabled
Memory RAS configuration set to Maximum Performance
DRAM Clock Throttling Set to Performance
 Sysinfo program /opt/cpu2006-1.2/config/sysinfo.rev6818
 $Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
 running on rhel6.4 Mon Jun  9 05:40:22 2014

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-4603 v2 @ 2.20GHz
       4 "physical id"s (chips)
       32 "processors"
    cores, siblings (Caution: counting these is hw and system dependent.  The
    following excerpts from /proc/cpuinfo might not be reliable.  Use with
    caution.)
       cpu cores : 4
```

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## Platform Notes (Continued)

```
        siblings  : 8
        physical 0: cores 0 1 2 3
        physical 1: cores 0 1 2 3
        physical 2: cores 0 1 2 3
        physical 3: cores 0 1 2 3
     cache size : 10240 KB

  From /proc/meminfo
     MemTotal:       264501936 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     Red Hat Enterprise Linux Server release 6.4 (Santiago)

  From /etc/*release* /etc/*version*
     redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
     system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

  uname -a:
     Linux rhel6.4 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
     x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Jun 8 21:09

  SPEC is set to: /opt/cpu2006-1.2
     Filesystem     Type    Size  Used Avail Use% Mounted on
     /dev/sda1      ext4    275G   43G  219G  17% /

  Additional information from dmidecode:
    BIOS Cisco Systems, Inc. B420M3.2.2.1.8.042120142113 04/21/2014
    Memory:
     32x 0xAD00 HMT31GR7EFR4C-RD 8 GB 1333 MHz 2 rank
     16x NO DIMM NO DIMM

  (End of data from sysinfo program)
```

## General Notes

```
 Environment variables set by runspec before the start of the run:
 KMP_AFFINITY = "granularity=fine,compact,1,0"
 LD_LIBRARY_PATH = "/opt/cpu2006-1.2/libs/32:/opt/cpu2006-1.2/libs/64:/opt/cpu2006-1.2/sh"
 OMP_NUM_THREADS = "32"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RedHat EL 6.4
  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
  runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:
```
icc  -m64
```

C++ benchmarks:
```
icpc -m64
```

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
445.gobmk: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
471.omnetpp: `-DSPEC_CPU_LP64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64
```

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m64
```

Continued on next page

## Peak Compiler Invocation (Continued)

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias

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# Peak Optimization Flags (Continued)

458.sjeng: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4`

462.libquantum: `basepeak = yes`

464.h264ref: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap`

# Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html`
`http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revB.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml`
`http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revB.xml`


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