IBM Corporation

IBM System x3530 M4
(Intel Xeon E5-2418L v2, 2.00 GHz)

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

CPU Name: Intel Xeon E5-2418L v2
CPU Characteristics:
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
2.6.32-431.el6.x86_64
Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext4

SPECfp®_rate2006 = 302
SPECfp_rate_base2006 = 295

Test date: Jun-2014
Hardware Availability: Mar-2014
Software Availability: Nov-2013

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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 192 GB (12 x 16 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
Other Hardware: None
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Base</td>
<td></td>
<td>Peak</td>
<td></td>
</tr>
<tr>
<td>410.bwaves</td>
<td>24</td>
<td>1160</td>
<td>281</td>
<td>1163</td>
<td>280</td>
<td>1161</td>
<td>281</td>
</tr>
<tr>
<td>416.gamess</td>
<td>24</td>
<td>1750</td>
<td>268</td>
<td>1757</td>
<td>268</td>
<td>1756</td>
<td>268</td>
</tr>
<tr>
<td>433.milc</td>
<td>24</td>
<td>812</td>
<td>271</td>
<td>812</td>
<td>271</td>
<td>812</td>
<td>271</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>24</td>
<td>622</td>
<td>351</td>
<td>622</td>
<td>352</td>
<td>619</td>
<td>353</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>24</td>
<td>514</td>
<td>333</td>
<td>514</td>
<td>333</td>
<td>514</td>
<td>333</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>24</td>
<td>789</td>
<td>363</td>
<td>787</td>
<td>364</td>
<td>790</td>
<td>363</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>24</td>
<td>1187</td>
<td>190</td>
<td>1184</td>
<td>191</td>
<td>1183</td>
<td>191</td>
</tr>
<tr>
<td>444.namd</td>
<td>24</td>
<td>913</td>
<td>211</td>
<td>913</td>
<td>211</td>
<td>920</td>
<td>209</td>
</tr>
<tr>
<td>447.dealII</td>
<td>24</td>
<td>567</td>
<td>484</td>
<td>571</td>
<td>481</td>
<td>572</td>
<td>480</td>
</tr>
<tr>
<td>450.soplex</td>
<td>24</td>
<td>1037</td>
<td>193</td>
<td>1031</td>
<td>194</td>
<td>1032</td>
<td>194</td>
</tr>
<tr>
<td>453.povray</td>
<td>24</td>
<td>345</td>
<td>370</td>
<td>344</td>
<td>371</td>
<td>346</td>
<td>369</td>
</tr>
<tr>
<td>454.calculix</td>
<td>24</td>
<td>488</td>
<td>406</td>
<td>483</td>
<td>410</td>
<td>484</td>
<td>409</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>24</td>
<td>1429</td>
<td>178</td>
<td>1429</td>
<td>178</td>
<td>1429</td>
<td>178</td>
</tr>
<tr>
<td>465.tonto</td>
<td>24</td>
<td>754</td>
<td>313</td>
<td>753</td>
<td>314</td>
<td>755</td>
<td>313</td>
</tr>
<tr>
<td>470.lbm</td>
<td>24</td>
<td>971</td>
<td>340</td>
<td>968</td>
<td>341</td>
<td>969</td>
<td>340</td>
</tr>
<tr>
<td>481.wrf</td>
<td>24</td>
<td>815</td>
<td>329</td>
<td>821</td>
<td>327</td>
<td>812</td>
<td>330</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>24</td>
<td>1496</td>
<td>313</td>
<td>1502</td>
<td>311</td>
<td>1504</td>
<td>311</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"
Zone reclaim mode enabled with:
echo 1 > /proc/sys/vm/zone_reclaim_mode
Intel Idle Driver disabled with the following Linux kernel parameter in /etc/grub.conf:
native_idle.max_cstate=0
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IBM System x3530 M4
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SPECfp_rate2006 = 302
SPECfp_rate_base2006 = 295

IBM Corporation (Intel Xeon E5-2418L v2, 2.00 GHz)
IBM System x3530 M4

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Platform Notes

BIOS setting:
Operating Mode set to Maximum Performance

Sysinfo program /home/SPECcpu-20140116-ic14.0/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on x3530M4 Fri Jun 13 21:13:06 2014

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2418L v2 @ 2.00GHz
  2 "physical id"s (chips)
  24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 6
    siblings : 12
    physical 0: cores 0 1 2 3 4 5
    physical 1: cores 0 1 2 3 4 5
    cache size : 15360 KB

From /proc/meminfo
MemTotal: 198317828 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release*/etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
uname -a:
Linux x3530M4 2.6.32-431.e16.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 11 17:47

SPEC is set to: /home/SPECcpu-20140116-ic14.0
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/vg_x3530m4-lv_home ext4 404G 16G 368G 4% /home

Additional information from dmidecode:
BIOS IBM -[BEE136CUS-1.60]- 01/23/2014
Memory:
  12x Samsung M393B2G70QH0-YK0 16 GB 1333 MHz 2 rank

(End of data from sysinfo program)
**SPEC CFP2006 Result**

IBM Corporation

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### General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "*/home/SPECcpu-20140116-ic14.0/libs/32:/home/SPECcpu-20140116-ic14.0/libs/64:/home/SPECcpu-20140116-ic14.0/sh"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```
Filesystem page cache cleared with:  
```
echo 1 > /proc/sys/vm/drop_caches
```
runspec command invoked through numactl i.e.:  
```
umactl --interleave=all runspec <etc>
```

### Base Compiler Invocation

- **C benchmarks:**
  - `icc -m64`
- **C++ benchmarks:**
  - `icpc -m64`
- **Fortran benchmarks:**
  - `ifort -m64`
- **Benchmarks using both Fortran and C:**
  - `icc -m64 ifort -m64`

### Base Portability Flags

- 410.bwaves: `-DSPEC_CPU_LP64`
- 416.gamess: `-DSPEC_CPU_LP64`
- 433.milc: `-DSPEC_CPU_LP64`
- 434.zeusmp: `-DSPEC_CPU_LP64`
- 435.gromacs: `-DSPEC_CPU_LP64
  -nofor_main`
- 436.cactusADM: `-DSPEC_CPU_LP64
  -nofor_main`
- 437.leslie3d: `-DSPEC_CPU_LP64`
- 444.namd: `-DSPEC_CPU_LP64`
- 447.dealII: `-DSPEC_CPU_LP64`
- 450.soplex: `-DSPEC_CPU_LP64`
- 453.povray: `-DSPEC_CPU_LP64`
- 454.calculix: `-DSPEC_CPU_LP64
  -nofor_main`
- 459.GemsFDTD: `-DSPEC_CPU_LP64`
- 465.tonto: `-DSPEC_CPU_LP64`
- 470.lbm: `-DSPEC_CPU_LP64`
- 481.wrf: `-DSPEC_CPU_LP64
  -DSPEC_CPU_CASE_FLAG
  -DSPEC_CPU_LINUX`
- 482.sphinx3: `-DSPEC_CPU_LP64`
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Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc   -m64

C++ benchmarks (except as noted below):
icpc  -m64

450.soplex: icpc -m32

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc   -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64 -nofor_main
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64

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Hardware Availability: Mar-2014
Software Availability: Nov-2013

Peak Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes
450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -opt-malloc-options=3

Fortran benchmarks:

410.bwaves: basepeak = yes
416.game3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch
434.zeusmp: basepeak = yes
437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch
459.GemsFDTD: basepeak = yes

Continued on next page
Peak Optimization Flags (Continued)

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-ino-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-ino-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-B.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-B.xml