



# SPEC® CINT2006 Result

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## Hewlett-Packard Company

**SPECint®\_rate2006 = 616**

ProLiant DL385p Gen8  
(2.80 GHz AMD Opteron 6386 SE)

**SPECint\_rate\_base2006 = 532**

CPU2006 license: 3

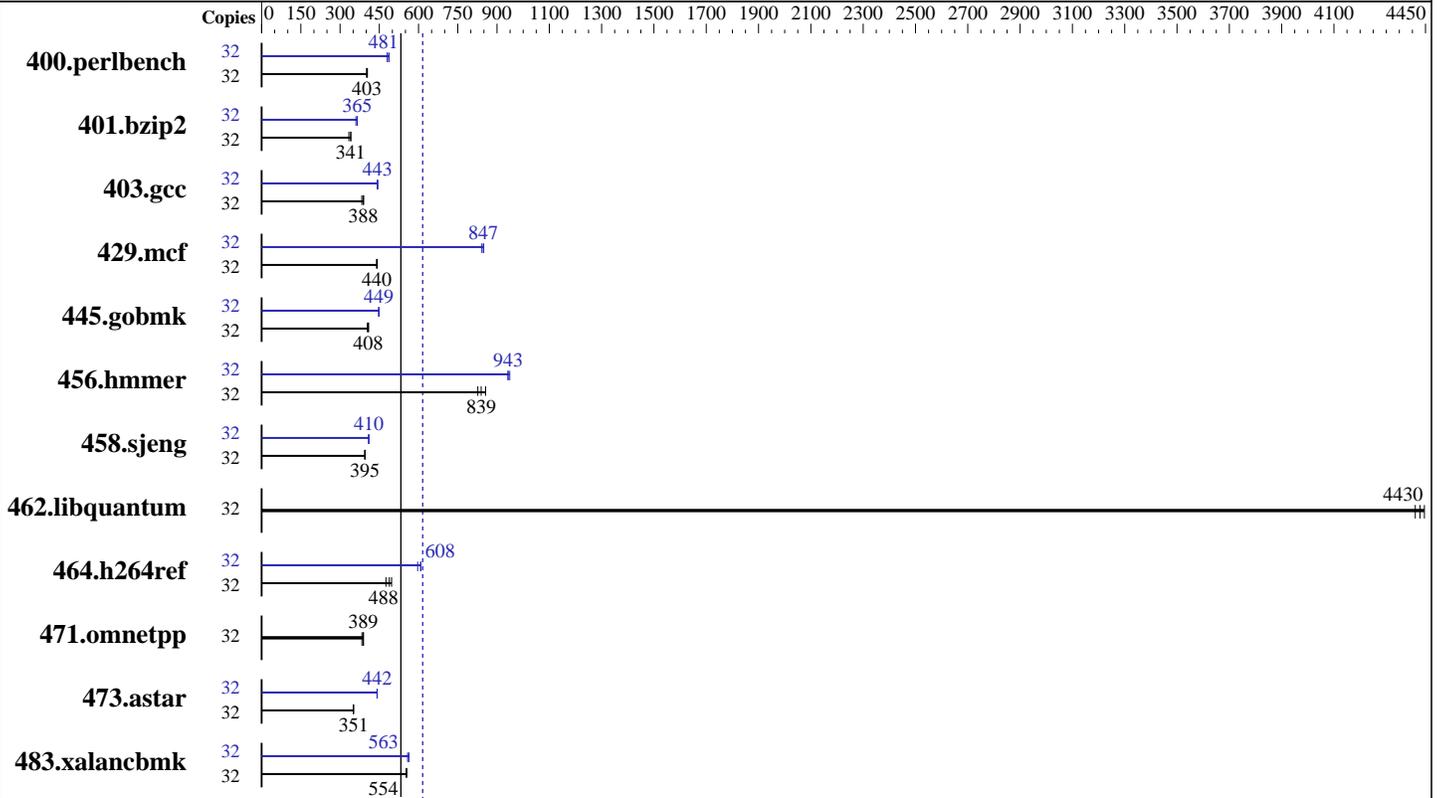
Test date: Jun-2014

Test sponsor: Hewlett-Packard Company

Hardware Availability: Nov-2012

Tested by: Hewlett-Packard Company

Software Availability: Nov-2013



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### Hardware

CPU Name: AMD Opteron 6386 SE  
 CPU Characteristics: AMD Turbo CORE technology up to 3.50 GHz  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 512 KB I on chip per chip,  
64 KB I shared / 2 cores;  
16 KB D on chip per core  
 Secondary Cache: 16 MB I+D on chip per chip, 2 MB shared / 2 cores  
 L3 Cache: 16 MB I+D on chip per chip, 8 MB shared / 8 cores  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5,  
Kernel 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 4.5.2 of x86 Open64  
Compiler Suite (from AMD)  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap 10.0 32-bit Library for Linux



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	32	779	401	773	405	<b>776</b>	<b>403</b>	32	651	480	641	487	<b>649</b>	<b>481</b>		
401.bzip2	32	905	341	926	334	<b>906</b>	<b>341</b>	32	856	361	844	366	<b>845</b>	<b>365</b>		
403.gcc	32	<b>664</b>	<b>388</b>	673	383	660	390	32	580	444	<b>581</b>	<b>443</b>	583	442		
429.mcf	32	663	440	660	442	<b>663</b>	<b>440</b>	32	347	842	344	849	<b>344</b>	<b>847</b>		
445.gobmk	32	831	404	<b>824</b>	<b>408</b>	820	409	32	748	449	750	447	<b>748</b>	<b>449</b>		
456.hammer	32	349	856	<b>356</b>	<b>839</b>	361	827	32	315	949	317	942	<b>317</b>	<b>943</b>		
458.sjeng	32	<b>980</b>	<b>395</b>	980	395	981	395	32	945	410	<b>945</b>	<b>410</b>	945	410		
462.libquantum	32	149	4450	150	4410	<b>150</b>	<b>4430</b>	32	149	4450	150	4410	<b>150</b>	<b>4430</b>		
464.h264ref	32	<b>1450</b>	<b>488</b>	1490	475	1425	497	32	<b>1165</b>	<b>608</b>	1187	597	1163	609		
471.omnetpp	32	521	384	514	389	<b>514</b>	<b>389</b>	32	521	384	514	389	<b>514</b>	<b>389</b>		
473.astar	32	638	352	<b>639</b>	<b>351</b>	641	350	32	<b>508</b>	<b>442</b>	509	441	508	442		
483.xalancbmk	32	<b>398</b>	<b>554</b>	398	555	399	553	32	<b>392</b>	<b>563</b>	396	558	392	563		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set transparent\_hugepage=never as a boot parameter in /boot/grub/menu.lst

Set vm/nr\_hugepages=28672 in /etc/sysctl.conf  
mount -t hugetlbfs nodev /mnt/hugepages

## Platform Notes

BIOS Configuration:  
HP Power Profile set to Maximum Performance  
Minimum Processor Idle Power State set to C1E State (AMD C1 Clock Ramping)  
Thermal Configuration set to Increased Cooling  
Processor Power and Utilization Monitoring set to Disabled  
Memory Refresh Rate set to 1x Refresh



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## General Notes

Environment variables set by runspec before the start of the run:

HUGETLB\_LIMIT = "896"

LD\_LIBRARY\_PATH = "/cpu2006/amd1206-rate-libs-revA/32:/cpu2006/amd1206-rate-libs-revA/64"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at <http://developer.amd.com/cpu/open64>

Binaries were compiled on a system with 2x AMD Opteron 6386SE chips + 128GB Memory using RHEL 6.3

## Base Compiler Invocation

C benchmarks:  
openc

C++ benchmarks:  
openCC

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-Ofast -CG:local\_sched\_alg=1 -INLINE:aggressive=ON -IPA:plimit=8000  
-IPA:small\_pu=100 -HP:bd=2m:heap=2m -mso -LNO:prefetch=2  
-march=bdver1

C++ benchmarks:  
-Ofast -m32 -INLINE:aggressive=on -CG:cmp\_peep=on -D\_\_OPEN64\_FAST\_SET  
-march=bdver1 -L/root/work/libraries/SmartHeap-10/lib -lsmartheap



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## Peak Compiler Invocation

C benchmarks:  
opencc

C++ benchmarks:  
openCC

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

## Peak Optimization Flags

C benchmarks:

```
400.perlbench: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:prefetch=2 -LNO:opt=0 -IPA:plimit=20000
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -WOPT:if_conv=0
-WOPT:sib=on -CG:local_sched_alg=1 -CG:unroll_fb_req=on
-CG:movext_icmp=off -HP:bd=2m:heap=2m -march=bdver1
-GRA:aggr_loop_splitting=off -GRA:loop_splitting=off

401.bzip2: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -O3
-LNO:prefetch=2 -LNO:pf2=0 -OPT:alias=disjoint
-OPT:goto=off -CG:local_sched_alg=1 -HP:bd=2m:heap=2m
-march=bdver2

403.gcc: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:trip_count=256 -CG:cmp_peep=on -CG:pre_minreg_level=2
-m32 -HP:bd=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200
-WOPT:sib=on -march=bdver2 -mno-fma4

429.mcf: -O3 -OPT:unroll_times_max=5 -ipa -INLINE:aggressive=on
-CG:gcm=off -CG:dsched=on -GRA:prioritize_by_density=on
-m32 -HP:bd=2m:heap=2m -mso -march=bdver1

445.gobmk: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-OPT:unroll_size=256 -OPT:unroll_times_max=8
-OPT:keep_ext=on -IPA:plimit=750 -IPA:min_hotness=300
```

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## Peak Optimization Flags (Continued)

445.gobmk (continued):

-IPA:pu\_reorder=1 -LNO:ignore\_feedback=off -WOPT:if\_conv=2  
-HP:bd=2m:heap=2m -march=bdver1

456.hmmr: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -Ofast

-LNO:prefetch=2 -OPT:alias=disjoint  
-OPT:unroll\_times\_max=16 -OPT:unroll\_size=512  
-OPT:unroll\_level=2 -OPT:keep\_ext=on -CG:cflow=0  
-CG:cmp\_peep=on -CG:pre\_local\_sched=off -HP:bdt=2m:heap=2m  
-CG:p2align=0 -CG:load\_exe=3 -CG:dsched=on -march=bdver1

458.sjeng: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -Ofast

-CG:ptr\_load\_use=0 -CG:divrem\_opt=on -CG:movext\_icmp=off  
-CG:locs\_best=on -LNO:full\_unroll=10 -IPA:pu\_reorder=2  
-HP:heap=2m:bd=2m -WOPT:sib=on -march=bdver1

462.libquantum: basepeak = yes

464.h264ref: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -O3

-OPT:unroll\_size=256 -OPT:unroll\_times\_max=2  
-IPA:plimit=20000 -OPT:alias=disjoint -CG:ptr\_load\_use=0  
-CG:local\_sched\_alg=1 -HP:bdt=2m:heap=2m -march=bdver1

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -Ofast

-WOPT:if\_conv=0 -WOPT:sib=on -CG:divrem\_opt=on  
-CG:p2align=1 -CG:dsched=on -GRA:optimize\_boundary=on  
-OPT:alias=disjoint -INLINE:aggressive=on  
-IPA:small\_pu=3000 -IPA:plimit=3000 -HP:bdt=2m:heap=2m  
-march=bdver1

483.xalancbmk: -Ofast -LNO:prefetch=2 -OPT:unroll\_size=512

-OPT:unroll\_times\_max=8 -D\_OPEN64\_FAST\_SET  
-INLINE:aggressive=on -m32 -CG:cmp\_peep=on  
-CG:local\_sched=off -CG:p2align=1 -GRA:unspill=on  
-TENV:frame\_pointer=off -fno-emit-exceptions -march=bdver2  
-mno-fma4  
-L/root/work/libraries/SmartHeap-10/lib -lsmarheap

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-AMD-V1.2-revC.html>

<http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-III.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-AMD-V1.2-revC.xml>

<http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-III.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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