SPEC® CFP2006 Result

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Hewlett-Packard Company

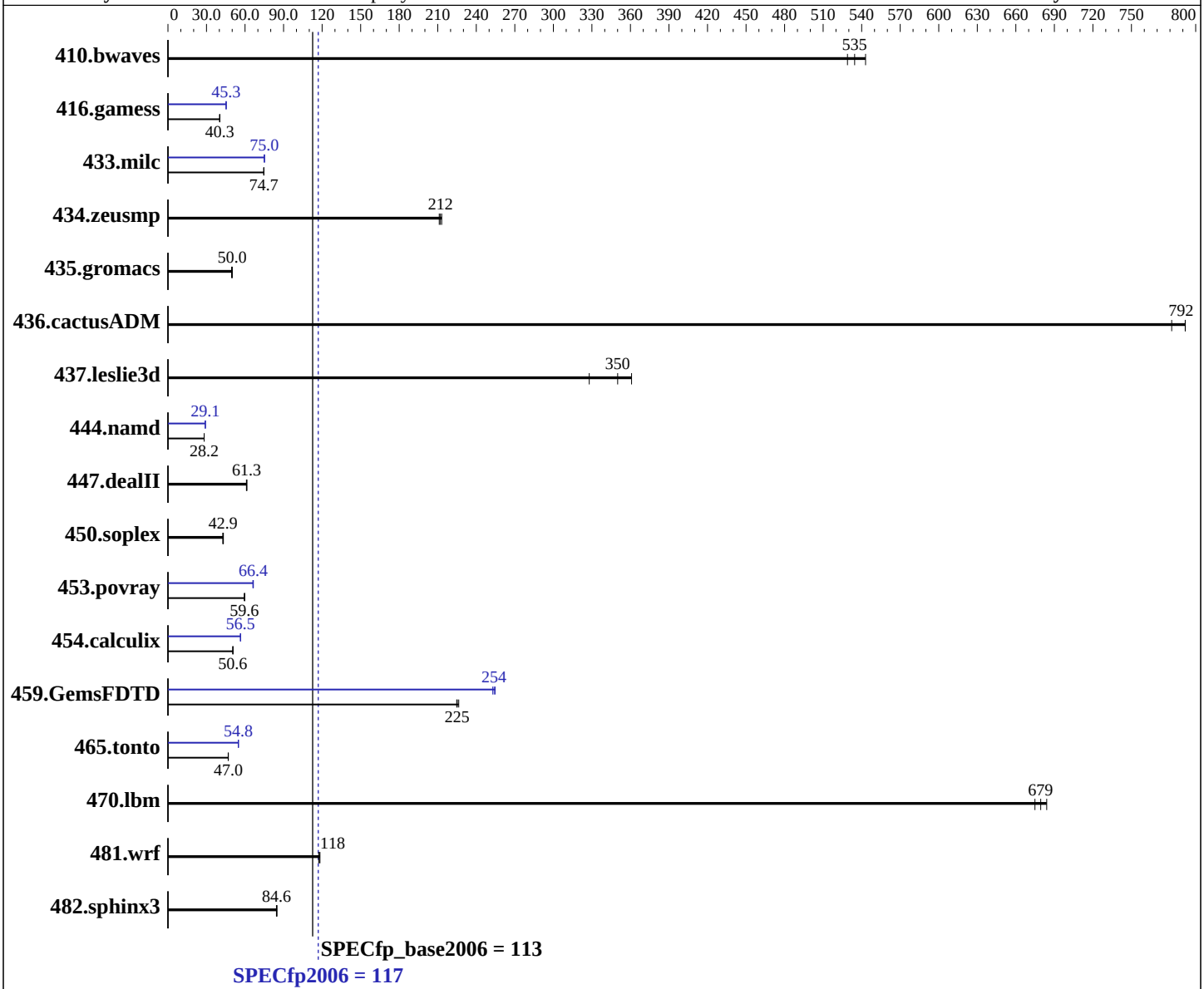
ProLiant BL460c Gen9
(3.20 GHz, Intel Xeon E5-2667 v3)

SPECfp®2006 = 117

SPECfp_base2006 = 113

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Jul-2014
Hardware Availability: Sep-2014
Software Availability: Jun-2014



Hardware

CPU Name:	Intel Xeon E5-2667 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 7.0 (Maipo) Kernel 3.10.0-123.el7.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs

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L3 Cache:	20 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (8 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem:	2 x 400 GB SAS SSD, RAID 1
Other Hardware:	None

System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	25.7	529	25.0	543	**25.4**	**535**	25.7	529	25.0	543	**25.4**	**535**
416.gamess	484	40.4	**486**	**40.3**	487	40.2	**432**	**45.3**	432	45.3	433	45.2
433.milc	123	74.5	123	74.8	**123**	**74.7**	**122**	**75.0**	122	75.0	122	75.2
434.zeusmp	42.7	213	**42.9**	**212**	43.1	211	42.7	213	**42.9**	**212**	43.1	211
435.gromacs	**143**	**50.0**	143	50.0	144	49.5	**143**	**50.0**	143	50.0	144	49.5
436.cactusADM	15.1	792	**15.1**	**792**	15.3	781	15.1	792	**15.1**	**792**	15.3	781
437.leslie3d	26.0	361	28.7	328	**26.9**	**350**	26.0	361	28.7	328	**26.9**	**350**
444.namd	285	28.2	285	28.2	**285**	**28.2**	276	29.1	**276**	**29.1**	276	29.1
447.dealII	187	61.1	**187**	**61.3**	186	61.4	187	61.1	**187**	**61.3**	186	61.4
450.soplex	193	43.2	195	42.7	**195**	**42.9**	193	43.2	195	42.7	**195**	**42.9**
453.povray	89.0	59.8	89.5	59.4	**89.2**	**59.6**	80.0	66.5	**80.1**	**66.4**	80.5	66.1
454.calculix	163	50.6	163	50.5	**163**	**50.6**	**146**	**56.5**	146	56.5	146	56.5
459.GemsFDTD	**47.1**	**225**	46.9	226	47.2	225	42.0	253	**41.8**	**254**	41.6	255
465.tonto	209	47.0	210	46.9	**210**	**47.0**	180	54.7	179	54.9	**180**	**54.8**
470.lbm	20.1	684	**20.2**	**679**	20.4	675	20.1	684	**20.2**	**679**	20.4	675
481.wrf	**94.8**	**118**	94.4	118	95.2	117	**94.8**	**118**	94.4	118	95.2	117
482.sphinx3	231	84.4	230	84.9	**230**	**84.6**	231	84.4	230	84.9	**230**	**84.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Platform Notes

```
BIOS Configuration:
 Intel Hyperthreading Options set to Disabled
 HP Power Profile set to Custom
 HP Power Regulator set to HP Static High Performance Mode
 Minimum Processor Idle Power Package C-State set to No Package State
 Energy/Performance Bias set to Maximum Performance
 QPI Snoop Configuration set to Home Snoop
 Thermal Configuration set to Maximum Cooling
```

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Platform Notes (Continued)

```
Processor Power and Utlization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh

Sysinfo program /cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on BL460cGen9-VP2 Mon Aug 11 18:35:06 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2667 v3 @ 3.20GHz
      2 "physical id"s (chips)
      16 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 8
      physical 0: cores 0 1 2 3 4 5 6 7
      physical 1: cores 0 1 2 3 4 5 6 7
   cache size : 20480 KB

From /proc/meminfo
   MemTotal:       131733912 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.0 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.0"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
   Linux BL460cGen9-VP2 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 11 13:26

SPEC is set to: /cpu2006
   Filesystem           Type  Size  Used Avail Use% Mounted on
   /dev/mapper/rhel-root ext4  310G  140G  154G  48% /
```

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Platform Notes (Continued)

```
 Additional information from dmidecode:
   BIOS HP I36 07/11/2014
   Memory:
    8x HP NOT AVAILABLE 16 GB 2133 MHz 2 rank
    8x UNKNOWN NOT AVAILABLE

 (End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of
memory is 128 GB and the dmidecode description should have one line reading as:
 8x HP NOT AVAILABLE 16 GB 2133 MHz 2 rank
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"
OMP_NUM_THREADS = "16"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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Peak Optimization Flags (Continued)

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html`
`http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revA.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml`
`http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revA.xml`

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For other inquiries, please contact webmaster@spec.org.

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