SPEC® CINT2006 Result

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Hewlett-Packard Company

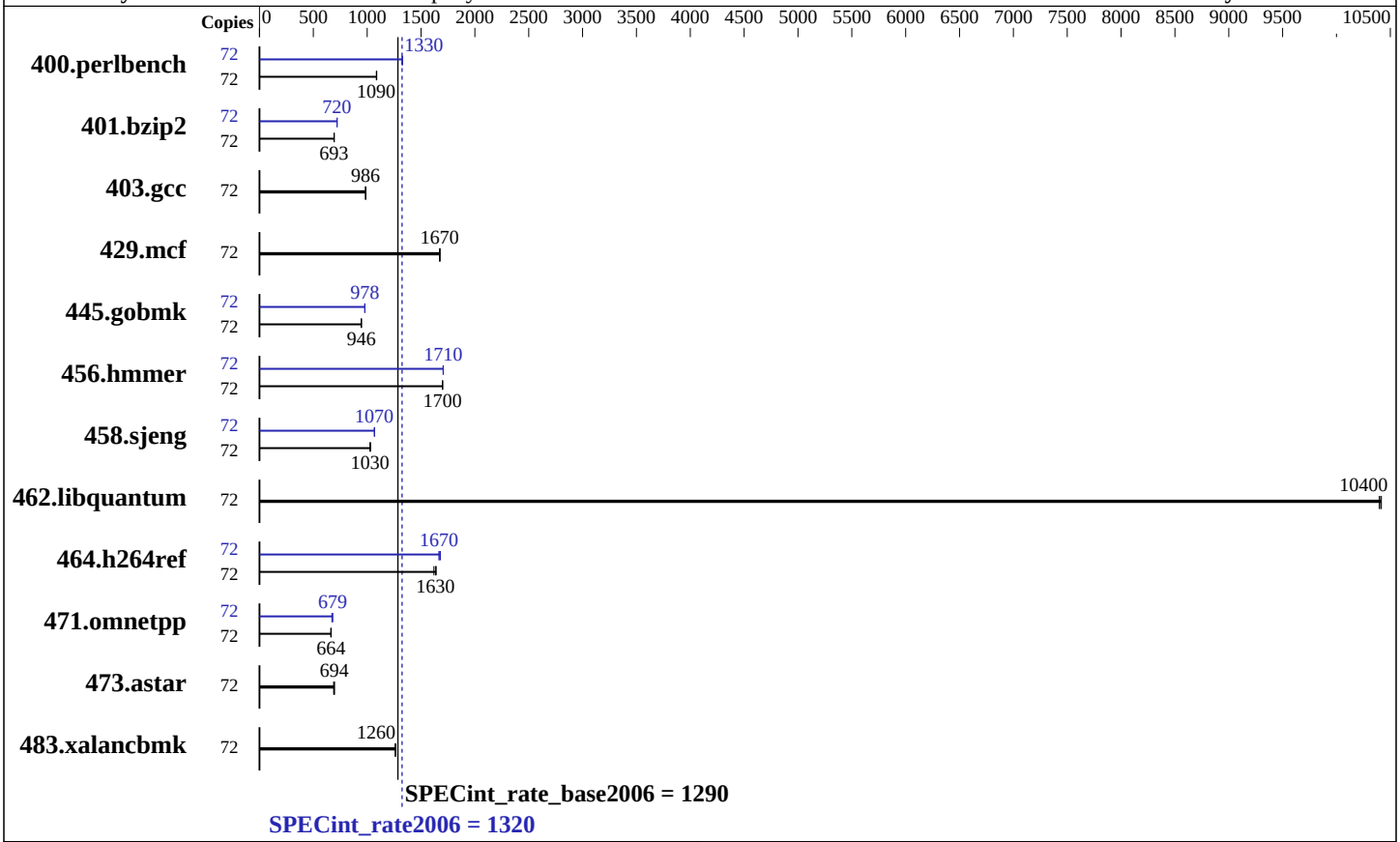
ProLiant BL460c Gen9
(2.30 GHz, Intel Xeon E5-2699 v3)

SPECint®_rate2006 = 1320

SPECint_rate_base2006 = 1290

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Jul-2014
Hardware Availability: Sep-2014
Software Availability: Jun-2014



Hardware

CPU Name:	Intel Xeon E5-2699 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	36 cores, 2 chips, 18 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	45 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (8 x 32 GB 4DRx4 PC4-2133P-L)
Disk Subsystem:	2 x 400 GB SAS SSD, RAID 1
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 7.0 (Maipo) Kernel 3.10.0-123.el7.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	72	**647**	**1090**	648	1090	646	1090	72	529	1330	**531**	**1330**	531	1320
401.bzip2	72	1003	693	**1002**	**693**	1001	694	72	963	722	966	719	**965**	**720**
403.gcc	72	587	987	589	984	**588**	**986**	72	587	987	589	984	**588**	**986**
429.mcf	72	**392**	**1670**	392	1680	393	1670	72	**392**	**1670**	392	1680	393	1670
445.gobmk	72	799	945	798	946	**799**	**946**	72	771	980	**772**	**978**	773	978
456.hmmer	72	**395**	**1700**	395	1700	396	1700	72	**394**	**1710**	394	1700	394	1710
458.sjeng	72	849	1030	846	1030	**847**	**1030**	72	816	1070	**816**	**1070**	816	1070
462.libquantum	72	143	10400	**143**	**10400**	143	10400	72	143	10400	**143**	**10400**	143	10400
464.h264ref	72	**975**	**1630**	984	1620	971	1640	72	956	1670	949	1680	**955**	**1670**
471.omnetpp	72	676	666	**677**	**664**	679	662	72	670	672	**663**	**679**	662	679
473.astar	72	**728**	**694**	734	689	726	696	72	**728**	**694**	734	689	726	696
483.xalancbmk	72	394	1260	**394**	**1260**	393	1260	72	394	1260	**394**	**1260**	393	1260

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
 echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Platform Notes

```
BIOS Configuration:
 HP Power Profile set to Custom
 HP Power Regulator set to HP Static High Performance Mode
 Minimum Processor Idle Power Package C-State set to No Package State
 Energy/Performance Bias set to Maximum Performance
 QPI Snoop Configuration set to Cluster on Die
 Thermal Configuration set to Maximum Cooling
 Processor Power and Utlization Monitoring set to Disabled
 Memory Refresh Rate set to 1x Refresh

 Sysinfo program /cpu2006/config/sysinfo.rev6818
```

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Platform Notes (Continued)

```
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on BL460cGen9-VP2 Fri Jul 25 14:00:28 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2699 v3 @ 2.30GHz
      2 "physical id"s (chips)
      72 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 9
      siblings  : 18
      physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
      physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
   cache size : 23040 KB

From /proc/meminfo
   MemTotal:       263839224 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.0 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.0"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
   Linux BL460cGen9-VP2 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 25 13:56

SPEC is set to: /cpu2006
   Filesystem            Type  Size  Used Avail Use% Mounted on
   /dev/mapper/rhel-root ext4  310G   12G  283G   4% /

Additional information from dmidecode:
  BIOS HP I36 07/11/2014
  Memory:
```

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Platform Notes (Continued)

```
      7x HP NOT AVAILABLE 32 GB 2133 MHz 4 rank
      8x UNKNOWN NOT AVAILABLE
      1x UNKNOWN NOT AVAILABLE 32 GB 2133 MHz 4 rank

  (End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of
memory is 256 GB and the dmidecode description should have two lines reading as:
  7x HP NOT AVAILABLE 32 GB 2133 MHz 4 rank
  1x UNKNOWN NOT AVAILABLE 32 GB 2133 MHz 4 rank
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RedHat EL 6.4
```

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-auto-ilp32`

401.bzip2: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `basepeak = yes`

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Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revA.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Sep 24 16:19:16 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 24 September 2014.