# SPEC® CINT2006 Result

## Hewlett-Packard Company

ProLiant DL380 Gen9  
(3.40 GHz, Intel Xeon E5-2643 v3)  

<table>
<thead>
<tr>
<th>SPECint_rate2006</th>
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<tr>
<td>SPECint_rate_base2006</td>
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</tbody>
</table>

### CPU2006 license: 3  
Test sponsor: Hewlett-Packard Company  
Tested by: Hewlett-Packard Company  

| Test date: | Oct-2014  |
| Hardware Availability: | Sep-2014  |
| Software Availability: | Sep-2014  |

### Hardware

| CPU Name: | Intel Xeon E5-2643 v3  |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.70 GHz  |
| CPU MHz: | 3400  |
| FPU: | Integrated  |
| CPU(s) enabled: | 12 cores, 2 chips, 6 cores/chip, 2 threads/core  |
| CPU(s) orderable: | 1,2 chip  |
| Primary Cache: | 32 KB I + 32 KB D on chip per core  |
| Secondary Cache: | 256 KB I+D on chip per core  |
| L3 Cache: | 20 MB I+D on chip per chip  |
| Other Cache: | None  |
| Memory: | 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  |
| Disk Subsystem: | 1 x 400 GB SAS SSD, RAID 0  |

### Software

| Operating System: | Red Hat Enterprise Linux Server release 7.0 (Maipo)  |
| Compiler: | C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  |
| Auto Parallel: | No  |
| File System: | ext4  |
| System State: | Run level 3 (multi-user)  |
| Base Pointers: | 32-bit  |
| Peak Pointers: | 32/64-bit  |
| Other Software: | Microquill SmartHeap V10.0  |
Hewlett-Packard Company
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SPECint_rate2006 = 691
SPECint_rate_base2006 = 662

Results Table

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<td>24</td>
<td>225</td>
<td>736</td>
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</tbody>
</table>

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
  echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>

Platform Notes

BIOS Configuration:
HP Power Profile set to Custom
HP Power Regulator to HP Static High Performance Mode
Minimum Processor Idle Power Core State set to C6 State
Minimum Processor Idle Power Package State set to No Package State
QPI Snoop Configuration set to Home Snoop
Thermal Configuration set to Maximum Cooling
Collaborative Power Control set to Disabled
Processor Power and Utilization Monitoring set to Disabled
Memory Double Refresh Rate set to 1x Refresh
Hewlett-Packard Company
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**SPEC CINT2006 Result**

| SPECint_rate2006 | 691 |
| SPECint_rate_base2006 | 662 |

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**Platform Notes (Continued)**

Sysinfo program /cpu2006/config/sysinfo.rev6914

$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1

running on DL380-Gen9 Tue Oct 21 04:40:21 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2643 v3 @ 3.40GHz

  2 "physical id"s (chips)

  24 "processors"

  cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

  cpu cores : 6
  siblings : 12
  physical 0: cores 0 1 2 3 4 5
  physical 1: cores 0 1 2 3 4 5
  cache size : 20480 KB

From /proc/meminfo

MemTotal: 263845844 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.0 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.0"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
  redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
  system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:

Linux DL380-Gen9 3.10.0-121.el7.x86_64 #1 SMP Tue Apr 8 10:48:19 EDT 2014
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 20 17:34

SPEC is set to: /cpu2006

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 ext4 362G 203G 141G 60% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program

Continued on next page
Platform Notes (Continued)

reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P89 07/11/2014
Memory:
  2x HP 752369-081 16 GB 2 rank 2133 MHz
  14x HP NOT AVAILABLE 16 GB 2 rank 2133 MHz
  8x UNKNOWN NOT AVAILABLE

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have two lines reading as:
  2x HP 752369-081 16 GB 2 rank 2133 MHz
  14x HP NOT AVAILABLE 16 GB 2 rank 2133 MHz

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Base Compiler Invocation

C benchmarks:
  icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
C++ benchmarks:
  icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
  -opt-mem-layout-trans=3

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Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
  icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
  400.perlbench: icc -m64
  401.bzip2: icc -m64
  456.hmmer: icc -m64
  458.sjeng: icc -m64
C++ benchmarks:
  icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:
  400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
  -auto-ilp32

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Peak Optimization Flags (Continued)

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<td>401.bzip2</td>
<td>-xCORE-AVX2 -prof-gen -ipo -O3 -no-prec-div -opt-prefetch -auto-ilp32 -ansi-alias</td>
</tr>
<tr>
<td>403.gcc</td>
<td>-xCORE-AVX2 -ipo -O3 -no-prec-div</td>
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<td>429.mcf</td>
<td>basepeak = yes</td>
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</tr>
<tr>
<td>456.hmmer</td>
<td>-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32</td>
</tr>
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Peak Other Flags

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The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml
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