SPEC has determined that this result is not in compliance with the SPEC OSG Guidelines for General Availability and the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a memory configuration that is not supported by Hewlett-Packard with the given processor configuration.

Non-Compliant
SPEC has determined that this result is not in compliance with the SPEC OSG Guidelines for General Availability and the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a memory configuration that is not supported by Hewlett-Packard with the given processor configuration.

The config file option 'submit' was used.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transient_hugepage/enabled
**SPEC CINT2006 Result**

**Hewlett-Packard Company**

ProLiant BL460c Gen9  
(3.40 GHz, Intel Xeon E5-2643 v3)

**SPECint2006 = NC**  
**SPECint_base2006 = NC**

CPU2006 license: 3  
Test date: Nov-2014  
Test sponsor: Hewlett-Packard Company  
Tested by: Hewlett-Packard Company  
Hardware Availability: Sep-2014  
Software Availability: Sep-2014

SPEC has determined that this result is not in compliance with the SPEC OSG Guidelines for General Availability and the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a memory configuration that is not supported by Hewlett-Packard with the given processor configuration.

**Platform Notes**

BIOS Configuration:
- HP Power Profile set to Custom
- HP Power Regulator to HP Static High Performance Mode
- Minimum Processor Idle Power Core State set to C6 State
- Minimum Processor Idle Power Package State set to No Package State
- QPI Snoop Configuration set to Early Snoop
- Thermal Configuration set to Maximum Cooling
- Collaborative Power Control set to Disabled
- Processor Power and Utilization Monitoring set to Disabled
- Intel Hyperthreading Options set to Disabled
- Sysinfo program /cpu2006/config/sysinfo.rev6914

Running on A-BL460cGen9-VP2 Thu Nov 6 09:12:08 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) CPU E5-2643 v3 @ 3.40GHz
- 2 "physical 1"s (chips)
- 2 "processor"
- cores: siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
- physical 0: cores 0 1 2 3 4 5
- physical 1: cores 0 1 2 3 4 5
- cache size : 20480 KB

From /proc/meminfo
- MemTotal: 263846716 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

Continued on next page

Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
Hewlett-Packard Company
ProLiant BL460c Gen9
(3.40 GHz, Intel Xeon E5-2643 v3)

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Platform Notes (Continued)

NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux A-BL460cGen9-VP2 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Nov 6 18:23

SPEC is set to: /cpu2006

Filesystemer Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-root ext4 310G 116G 179G 40% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP I36 08/26/2014
Memory:
16x 8752369-081 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)
SPEC CINT2006 Result

Hewlett-Packard Company

ProLiant BL460c Gen9
(3.40 GHz, Intel Xeon E5-2643 v3)

<table>
<thead>
<tr>
<th>CPU2006 license:</th>
<th>3</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test sponsor:</td>
<td>Hewlett-Packard Company</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Hewlett-Packard Company</td>
</tr>
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Environment variables set by runspec before the start of the run:
- KMP_AFFINITY = "granularity=fine,scatter"
- LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"
- OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

**Base Compiler Invocation**

- C benchmarks: icc -m64
- C++ benchmarks: icpc -m64

**Base Portability Flags**

- 400.perlbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
- 401.bzip2: -DSPEC_CPU_LP64
- 403.gcc: -DSPEC_CPU_LP64
- 429.mcf: -DSPEC_CPU_LP64
- 445.gobmk: -DSPEC_CPU_LP64
- 456.hmmer: -DSPEC_CPU_LP64
- 458.sjeng: -DSPEC_CPU_LP64
- 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
- 464.h264ref: -DSPEC_CPU_LP64
- 471.omnetpp: -DSPEC_CPU_LP64
- 473.astar: -DSPEC_CPU_LP64
- 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
SPEC has determined that this result is not in compliance with the SPEC OSG Guidelines for General Availability and the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a memory configuration that is not supported by Hewlett-Packard with the given processor configuration.
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Peak Portability Flags (Continued)

401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-o3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-o3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-o3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

Continued on next page
SPEC CINT2006 Result

Hewlett-Packard Company
ProLiant BL460c Gen9
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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes
464.h264ref: basepeak = yes

C++ benchmarks:
471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap
473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,-muldefs -L/sh -lsmartheap64
483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml
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