Huawei

Huawei RH5885H V3 (Intel Xeon E7-4850 v2)

SPECfp®2006 = 70.4
SPECfp_base2006 = 66.4

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Oct-2014
Hardware Availability: Feb-2014
Software Availability: Jun-2014

410.bwaves
416.gamess
433.milc
434.zeusmp
435.gromacs
436.cactusADM
437.leslie3d
444.namd
447.dealII
450.soplex
453.povray
454.calculix
459.GemsFDTD
465.tonto
470.lbm
481.wrf
482.sphinx3

SPECfp_base2006 = 66.4
SPECfp2006 = 70.4

Hardware
CPU Name: Intel Xeon E7-4850 v2
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz: 2300
FPU: Integrated
CPU(s) enabled: 48 cores, 4 chips, 12 cores/chip
CPU(s) orderable: 2, 4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software
Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
Compiler: CIC++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
            Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: ext4

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L3 Cache: 24 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (64 x 16 GB 2Rx4 PC3-12800R-11, ECC, running at 1066 MHz and CL7)
Disk Subsystem: 2 x 300 GB SAS, 10K RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds Base</th>
<th>Ratio</th>
<th>Seconds Base</th>
<th>Ratio</th>
<th>Seconds Base</th>
<th>Ratio</th>
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<td>459.GemsFDTD</td>
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<td>56.6</td>
<td>343</td>
<td>56.8</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Lock_step to disabled
Baseboard Management Controller used to adjust the fan speed to 100%
Set Intel Hyper Threading to disabled
Sysinfo program /spec/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Tue Oct 14 08:44:25 2014

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Huawei RH5885H V3 (Intel Xeon E7-4850 v2)

SPECfn2006 = 70.4
SPECfp_base2006 = 66.4

Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name: Intel(R) Xeon(R) CPU E7-4850 v2 @ 2.30GHz
- 4 "physical id"s (chips)
- 48 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores: 12
  - siblings: 12
  - physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
  - physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
  - physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13
  - physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13
- cache size: 24576 KB

From /proc/meminfo
- MemTotal: 1056475676 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
- os-release:
  - NAME="Red Hat Enterprise Linux Server"
  - VERSION="7.0 (Maipo)"
  - ID="rhel"
  - ID_LIKE="fedora"
  - VERSION_ID="7.0"
  - PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
    ANSI_COLOR="0;31"
  - CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
- redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
- system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
- system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
- Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 14 01:52

SPEC is set to: /spec
- Filesystem Type Size Used Avail Use% Mounted on
  - /dev/sda3 ext4 349G 12G 320G 4% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to
Continued on next page
Huawei RH5885H V3 (Intel Xeon E7-4850 v2)

**SPECfp2006 = 70.4**

**SPECfp_base2006 = 66.4**

**CPU2006 license:** 3175

**Test sponsor:** Huawei

**Tested by:** Huawei

**Test date:** Oct-2014

**Hardware Availability:** Feb-2014

**Software Availability:** Jun-2014

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### Platform Notes (Continued)

Hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. BLISV395 07/25/2014

Memory:

- 61x Hynix HMT42GR7AFR4C-PB 16 GB 2 rank 1600 MHz, configured at 1066 MHz
- 3x Hynix HMT42GR7MFR4C-PB 16 GB 2 rank 1600 MHz, configured at 1066 MHz
- 32x NO DIMM NO DIMM

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 1 TB and the dmidecode description should have two lines reading as:

- 61x Hynix HMT42GR7AFR4C-PB 16 GB 2 rank 1600 MHz, configured at 1066 MHz
- 3x Hynix HMT42GR7MFR4C-PB 16 GB 2 rank 1600 MHz, configured at 1066 MHz

---

### General Notes

Environment variables set by runspec before the start of the run:

- `KMP_AFFINITY = "granularity=fine,compact,1,0"
- `LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"
- `OMP_NUM_THREADS = "48"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

- `echo always > /sys/kernel/mm/transparent_hugepage/enabled`
- `runspec command invoked through numactl i.e.:
  `numactl --interleave=all runspec <etc>

### Base Compiler Invocation

**C benchmarks:**

- `icc -m64`

**C++ benchmarks:**

- `icpc -m64`

**Fortran benchmarks:**

- `ifort -m64`

**Benchmarks using both Fortran and C:**

- `icc -m64 ifort -m64`

### Base Portability Flags

- `410.bwaves: -DSPEC_CPU_LP64`
- `416.gamess: -DSPEC_CPU_LP64`

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Huawei RH5885H V3 (Intel Xeon E7-4850 v2)

SPECfp2006 = 70.4
SPECfp_base2006 = 66.4

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei
Test date: Oct-2014
Hardware Availability: Feb-2014
Software Availability: Jun-2014

Base Portability Flags (Continued)

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64 -nofor_main
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64
Huawei

Huawei RH5885H V3 (Intel Xeon E7-4850 v2)

SPECfp2006 = 70.4
SPECfp_base2006 = 66.4

CPU2006 license: 3175
Test date: Oct-2014
Test sponsor: Huawei
Hardware Availability: Feb-2014
Tested by: Huawei
Software Availability: Jun-2014

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
   -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
   -parallel

C++ benchmarks:
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
   -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias

Fortran benchmarks:
410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
   -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
   -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
   -opt-malloc-options=3 -auto -unroll4

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Huawei
Huawei RH5885H V3 (Intel Xeon E7-4850 v2)

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| SPECfp_base2006 = 66.4 |

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Test date: Oct-2014
Hardware Availability: Feb-2014
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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml

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