Lenovo Group Limited
IBM Flex System x240 M5
(Intel Xeon E5-2630 v3, 2.40 GHz)

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

SPECint_rate2006 = 685
SPECint_rate_base2006 = 663

Test date: Dec-2014
Hardware Availability: Dec-2014
Software Availability: Nov-2013

Hardware

CPU Name: Intel Xeon E5-2630 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
Disk Subsystem: 1 x 400 GB SSD
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-431.el6.x86_64
Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0
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</tbody>
</table>

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### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>32</td>
<td>632</td>
<td>495</td>
<td>633</td>
<td>494</td>
<td>32</td>
<td>518</td>
<td>603</td>
<td>518</td>
<td>516</td>
<td>606</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>32</td>
<td>960</td>
<td>322</td>
<td>958</td>
<td>322</td>
<td>32</td>
<td>917</td>
<td>337</td>
<td>919</td>
<td>336</td>
<td>917</td>
</tr>
<tr>
<td>403.gcc</td>
<td>32</td>
<td>509</td>
<td>506</td>
<td>508</td>
<td>507</td>
<td>32</td>
<td>509</td>
<td>506</td>
<td>508</td>
<td>507</td>
<td>501</td>
</tr>
<tr>
<td>429.mcf</td>
<td>32</td>
<td>320</td>
<td>912</td>
<td>319</td>
<td>916</td>
<td>32</td>
<td>320</td>
<td>912</td>
<td>319</td>
<td>916</td>
<td>319</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>32</td>
<td>774</td>
<td>434</td>
<td>775</td>
<td>433</td>
<td>32</td>
<td>756</td>
<td>444</td>
<td>757</td>
<td>443</td>
<td>757</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>32</td>
<td>312</td>
<td>956</td>
<td>317</td>
<td>941</td>
<td>32</td>
<td>309</td>
<td>967</td>
<td>308</td>
<td>969</td>
<td>310</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>32</td>
<td>842</td>
<td>460</td>
<td>843</td>
<td>459</td>
<td>32</td>
<td>817</td>
<td>474</td>
<td>816</td>
<td>474</td>
<td>818</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>32</td>
<td>98.2</td>
<td>6750</td>
<td>98.2</td>
<td>6750</td>
<td>32</td>
<td>98.2</td>
<td>6750</td>
<td>98.2</td>
<td>6750</td>
<td>98.5</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>32</td>
<td>932</td>
<td>760</td>
<td>918</td>
<td>722</td>
<td>32</td>
<td>890</td>
<td>796</td>
<td>916</td>
<td>773</td>
<td>891</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>32</td>
<td>546</td>
<td>366</td>
<td>545</td>
<td>367</td>
<td>32</td>
<td>516</td>
<td>388</td>
<td>515</td>
<td>388</td>
<td>519</td>
</tr>
<tr>
<td>473.astar</td>
<td>32</td>
<td>613</td>
<td>366</td>
<td>614</td>
<td>366</td>
<td>32</td>
<td>613</td>
<td>366</td>
<td>614</td>
<td>366</td>
<td>615</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>32</td>
<td>298</td>
<td>740</td>
<td>299</td>
<td>740</td>
<td>32</td>
<td>298</td>
<td>740</td>
<td>299</td>
<td>740</td>
<td>299</td>
</tr>
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</table>

**Results appear in the order in which they were run. Bold underlined text indicates a median measurement.**

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### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

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### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

---

### Platform Notes

Fan speed set to 100%
Operating Mode set to Maximum Performance in BIOS
Enable COD Preference in BIOS
Disable Early Snoop Preference in BIOS
 Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 $$ e86d102572650a6e4d596a3cee98f191
running on Bonneville-SPECcpu Wed Dec 3 16:10:50 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) CPU E5-2630 v3 @ 2.40GHz
- 2 "physical id"s (chips)
- 32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The Continued on next page
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Platform Notes (Continued)

following excerpts from /proc/cpuinfo might not be reliable. Use with caution.

 cpu cores : 8
 siblings : 16
 physical 0: cores 0 1 2 3 4 5 6 7
 physical 1: cores 0 1 2 3 4 5 6 7
 cache size : 20480 KB

From /proc/meminfo
 MemTotal:   264121860 kB
 HugePages_Total:       0
 Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
 Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release*/etc/*version*
 redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)

uname -a:
 Linux Bonneville-SPECcpu 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 3 16:04 last=5

SPEC is set to: /cpu2006.1.2
 Filesystem Type Size Used Avail Use% Mounted on
 /dev/mapper/vg_bonnevillespe-lv_root ext4 356G 14G 325G 4% /

Additional information from dmidecode:
 BIOS IBM -[C4E103BUS-1.00]- 11/07/2014
 Memory:
  8x NO DIMM Unknown
  16x Samsung M393A2G40DB0-CPB 16 GB 1867 MHz 2 rank

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:

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General Notes (Continued)
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
  icc  -m32

C++ benchmarks:
  icpc  -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
  -xCORE-AVX2  -ipo  -O3  -no-prec-div  -opt-prefetch
  -opt-mem-layout-trans=3

C++ benchmarks:
  -xCORE-AVX2  -ipo  -O3  -no-prec-div  -opt-prefetch
  -opt-mem-layout-trans=3  -Wl,-z,muldefs  -L/sh  -lsmartheap

Base Other Flags

C benchmarks:
  403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
  icc  -m32

400.perlbench: icc  -m64

401.bzip2: icc  -m64

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Peak Compiler Invocation (Continued)

456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes
429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3
456.hmmer: -xCORE-AVX2 -ipo -03 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

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**Peak Optimization Flags (Continued)**

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes
483.xalancbmk: basepeak = yes

**Peak Other Flags**

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-B.20141021.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-B.20141021.xml

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For other inquiries, please contact webmaster@spec.org.

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