



SPEC® CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

Lenovo System x3250 M5
(Intel Xeon E3-1275L v3, 2.70 GHz)

SPECint®2006 = 61.1

SPECint_base2006 = 58.6

CPU2006 license: 9017

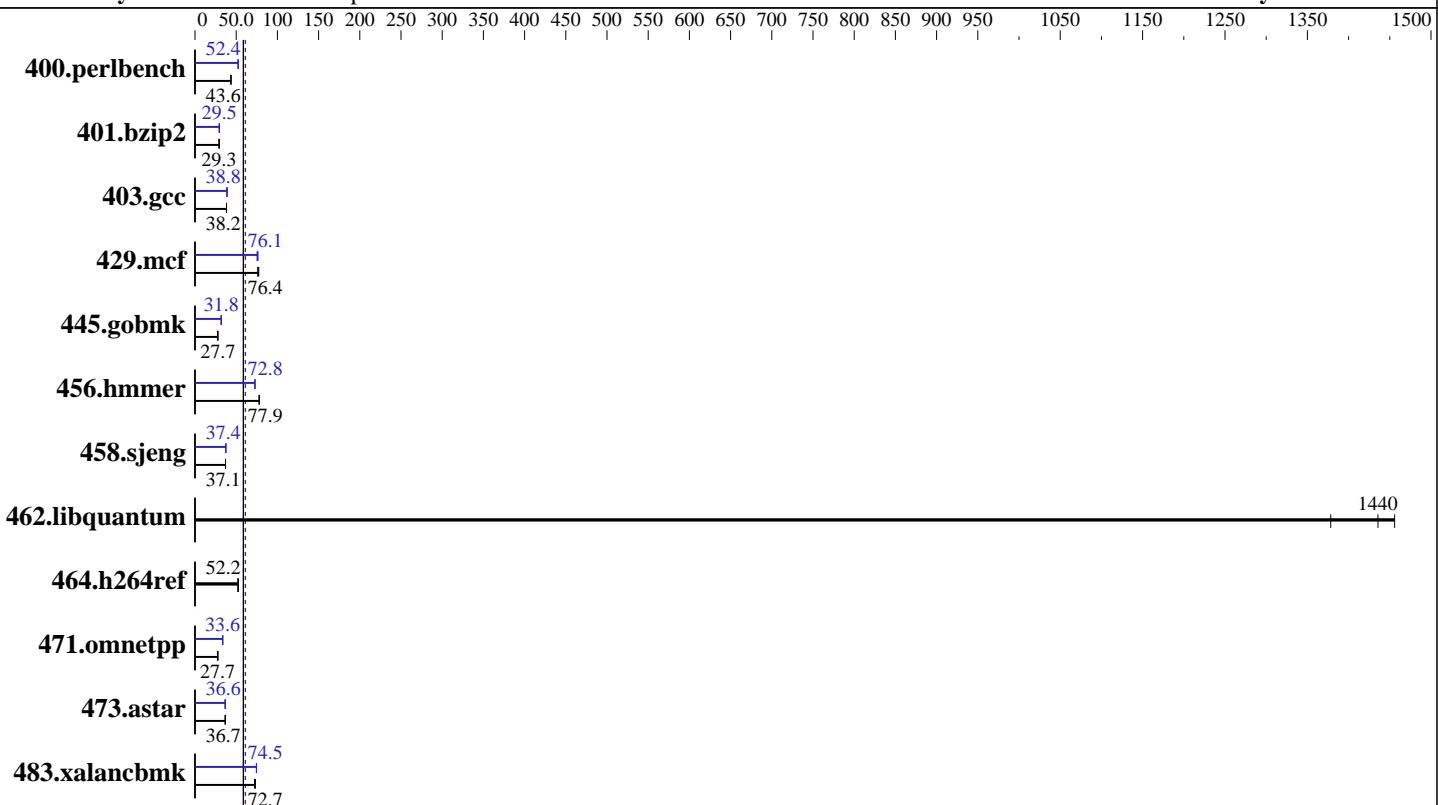
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Jan-2015

Hardware Availability: Jul-2014

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E3-1275L v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz
CPU MHz: 2700
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (4 x 4 GB 2Rx8 PC3-12800U-13)
Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
Compiler: 2.6.32-431.el6.x86_64
Auto Parallel: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

Lenovo System x3250 M5
(Intel Xeon E3-1275L v3, 2.70 GHz)

SPECint2006 = 61.1

SPECint_base2006 = 58.6

CPU2006 license: 9017

Test date: Jan-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: Jul-2014

Tested by: Lenovo Group Limited

Software Availability: Nov-2013

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|----------------|-------------|-------------|-------------|-------------|------------|-------------|-------------|-------------|-------------|-------------|------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 223 | 43.7 | 224 | 43.6 | 224 | 43.6 | 187 | 52.4 | 187 | 52.4 | 187 | 52.4 |
| 401.bzip2 | 329 | 29.3 | 330 | 29.3 | 329 | 29.3 | 327 | 29.5 | 327 | 29.5 | 327 | 29.5 |
| 403.gcc | 211 | 38.2 | 210 | 38.3 | 211 | 38.2 | 208 | 38.8 | 207 | 38.8 | 207 | 38.9 |
| 429.mcf | 120 | 76.3 | 118 | 77.4 | 119 | 76.4 | 121 | 75.3 | 119 | 76.4 | 120 | 76.1 |
| 445.gobmk | 378 | 27.7 | 379 | 27.7 | 378 | 27.7 | 330 | 31.8 | 330 | 31.8 | 330 | 31.8 |
| 456.hmmer | 119 | 78.1 | 120 | 77.9 | 120 | 77.8 | 128 | 72.6 | 128 | 72.8 | 128 | 72.8 |
| 458.sjeng | 327 | 37.1 | 327 | 37.1 | 327 | 37.1 | 323 | 37.5 | 324 | 37.4 | 324 | 37.4 |
| 462.libquantum | 14.2 | 1460 | 14.4 | 1440 | 15.0 | 1380 | 14.2 | 1460 | 14.4 | 1440 | 15.0 | 1380 |
| 464.h264ref | 425 | 52.1 | 424 | 52.2 | 420 | 52.7 | 425 | 52.1 | 424 | 52.2 | 420 | 52.7 |
| 471.omnetpp | 226 | 27.7 | 225 | 27.8 | 228 | 27.4 | 186 | 33.6 | 186 | 33.6 | 185 | 33.7 |
| 473.astar | 191 | 36.7 | 191 | 36.8 | 191 | 36.7 | 191 | 36.8 | 192 | 36.6 | 192 | 36.5 |
| 483.xalancbmk | 94.9 | 72.7 | 94.1 | 73.3 | 95.4 | 72.4 | 92.6 | 74.5 | 92.4 | 74.7 | 92.6 | 74.5 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS setting:

Operating Mode set to Maximum Performance

Sysinfo program /home/SPEC_ic14/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-07-17 #\\$ e86d102572650a6e4d596a3cee98f191

running on localhost.localdomain Sat Jan 10 03:01:52 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E3-1275L v3 @ 2.70GHz

1 "physical id"s (chips)

8 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 4

siblings : 8

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

Lenovo System x3250 M5
(Intel Xeon E3-1275L v3, 2.70 GHz)

SPECint2006 = 61.1

SPECint_base2006 = 58.6

CPU2006 license: 9017

Test date: Jan-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: Jul-2014

Tested by: Lenovo Group Limited

Software Availability: Nov-2013

Platform Notes (Continued)

```
physical 0: cores 0 1 2 3
cache size : 8192 KB

From /proc/meminfo
MemTotal:       16165980 kB
HugePages_Total:      0
Hugepagesize:     2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 8 22:14

SPEC is set to: /home/SPEC_ic14
Filesystem              Type  Size  Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_home ext4  860G  23G  793G   3% /home

Additional information from dmidecode:
BIOS IBM -[JUE115CUS-1.06]- 11/11/2014
Memory:
4x Hynix/Hyundai HMT351U7EFR8A-PB 4 GB 1600 MHz 2 rank

(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/home/SPEC_ic14/libs/32:/home/SPEC_ic14/libs/64:/home/SPEC_ic14/sh"

OMP_NUM_THREADS = "4"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

Lenovo System x3250 M5
(Intel Xeon E3-1275L v3, 2.70 GHz)

SPECint2006 = 61.1

SPECint_base2006 = 58.6

CPU2006 license: 9017

Test date: Jan-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: Jul-2014

Tested by: Lenovo Group Limited

Software Availability: Nov-2013

Base Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hammer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32`

C++ benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

Lenovo System x3250 M5
(Intel Xeon E3-1275L v3, 2.70 GHz)

SPECint2006 = 61.1

SPECint_base2006 = 58.6

CPU2006 license: 9017

Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Jan-2015

Hardware Availability: Jul-2014

Software Availability: Nov-2013

Peak Compiler Invocation (Continued)

400.perlbench: icc -m32

445.gobmk: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

456.hammer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

464.h264ref: -DSPEC_CPU_LP64

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hammer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

Lenovo System x3250 M5
(Intel Xeon E3-1275L v3, 2.70 GHz)

SPECint2006 = 61.1

SPECint_base2006 = 58.6

CPU2006 license: 9017

Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Jan-2015

Hardware Availability: Jul-2014

Software Availability: Nov-2013

Peak Optimization Flags (Continued)

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-A.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Jan 27 13:30:17 2015 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 27 January 2015.