SPEC® CFP2006 Result

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Hewlett-Packard Company

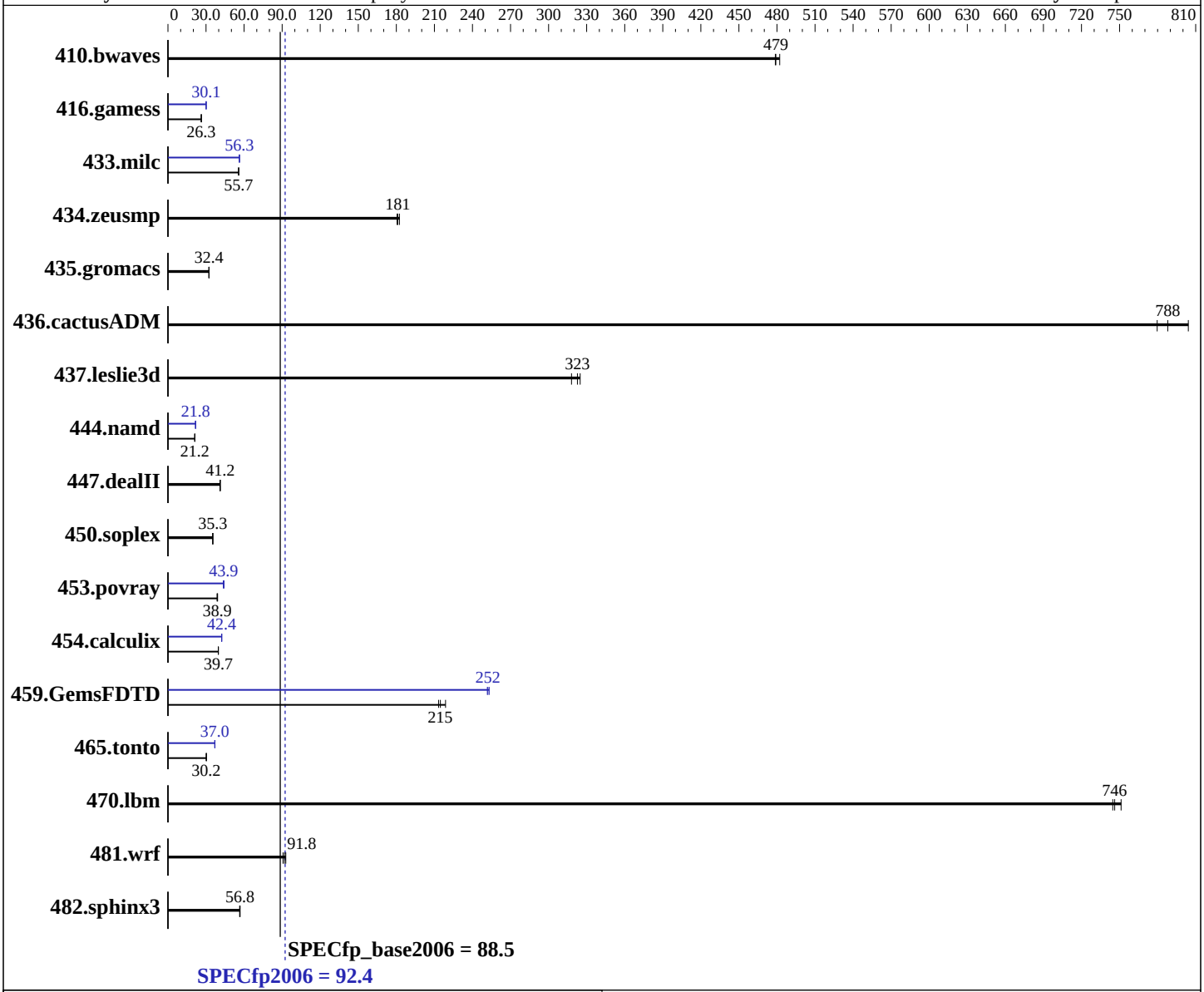
ProLiant BL460c Gen9
(1.80 GHz, Intel Xeon E5-2650L v3)

SPECfp®2006 = 92.4

SPECfp_base2006 = 88.5

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Dec-2014
Hardware Availability: Sep-2014
Software Availability: Sep-2014



Hardware

CPU Name:	Intel Xeon E5-2650L v3
CPU Characteristics:	Intel Turbo Boost Technology up to 2.50 GHz
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	24 cores, 2 chips, 12 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 7.0 (Maipo) Kernel 3.10.0-123.el7.x86_64
Compiler:	C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux; Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs

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L3 Cache:	30 MB I+D on chip per chip	System State:	Run level 3 (multi-user)
Other Cache:	None	Base Pointers:	64-bit
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)	Peak Pointers:	32/64-bit
Disk Subsystem:	2 x 400 GB SAS SSD, RAID 1	Other Software:	None
Other Hardware:	None		

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	28.2	482	28.4	479	**28.4**	**479**	28.2	482	28.4	479	**28.4**	**479**
416.gamess	**745**	**26.3**	743	26.3	747	26.2	651	30.1	650	30.1	**651**	**30.1**
433.milc	**165**	**55.7**	164	55.8	165	55.7	**163**	**56.3**	163	56.2	163	56.4
434.zeusmp	49.9	182	**50.2**	**181**	50.4	181	49.9	182	**50.2**	**181**	50.4	181
435.gromacs	221	32.4	221	32.3	**221**	**32.4**	221	32.4	221	32.3	**221**	**32.4**
436.cactusADM	**15.2**	**788**	15.3	780	14.9	804	**15.2**	**788**	15.3	780	14.9	804
437.leslie3d	**29.1**	**323**	29.6	318	28.9	325	**29.1**	**323**	29.6	318	28.9	325
444.namd	379	21.2	379	21.2	**379**	**21.2**	**369**	**21.8**	368	21.8	369	21.8
447.dealII	**278**	**41.2**	277	41.3	279	41.0	**278**	**41.2**	277	41.3	279	41.0
450.soplex	236	35.4	**236**	**35.3**	236	35.3	236	35.4	**236**	**35.3**	236	35.3
453.povray	**137**	**38.9**	137	38.7	136	39.1	122	43.7	120	44.2	**121**	**43.9**
454.calculix	208	39.7	**208**	**39.7**	208	39.7	195	42.3	195	42.4	**195**	**42.4**
459.GemsFDTD	48.5	219	**49.4**	**215**	49.7	213	42.1	252	41.9	253	**42.1**	**252**
465.tonto	324	30.4	**326**	**30.2**	326	30.2	266	37.1	**266**	**37.0**	267	36.9
470.lbm	18.5	745	18.3	751	**18.4**	**746**	18.5	745	18.3	751	**18.4**	**746**
481.wrf	120	92.9	**122**	**91.8**	123	90.6	120	92.9	**122**	**91.8**	123	90.6
482.sphinx3	343	56.8	**343**	**56.8**	345	56.5	343	56.8	**343**	**56.8**	345	56.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Platform Notes

```
BIOS Configuration:
 HP Power Profile set to Custom
 HP Power Regulator to HP Static High Performance Mode
 Minimum Processor Idle Power Core State set to C6 State
 Minimum Processor Idle Power Package State set to No Package State
 QPI Snoop Configuration set to Home Snoop
 Thermal Configuration set to Maximum Cooling
 Collaborative Power Control set to Disabled
```

Continued on next page

Platform Notes (Continued)

```
 Processor Power and Utilization Monitoring set to Disabled
 Memory Double Refresh Rate set to 1x Refresh
 Intel Hyperthreading Options set to Disabled
Sysinfo program /cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on W-bl460c_gen9-VP2.1 Fri Dec 19 12:20:50 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2650L v3 @ 1.80GHz
      2 "physical id"s (chips)
      24 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 12
      siblings  : 12
      physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
   cache size : 30720 KB

From /proc/meminfo
   MemTotal:       263845292 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.0 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.0"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
   Linux W-bl460c_gen9-VP2.1 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT
   2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 19 12:13

SPEC is set to: /cpu2006
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda4      xfs   277G   96G  182G  35% /
```

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Platform Notes (Continued)

```
 Additional information from dmidecode:

    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS HP I36 08/26/2014
   Memory:
    16x HP NOT AVAILABLE 16 GB 2 rank 2133 MHz

 (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"
OMP_NUM_THREADS = "24"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html`
`http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml`

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For other inquiries, please contact webmaster@spec.org.

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