SPEC® CINT2006 Result

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Lenovo Group Limited

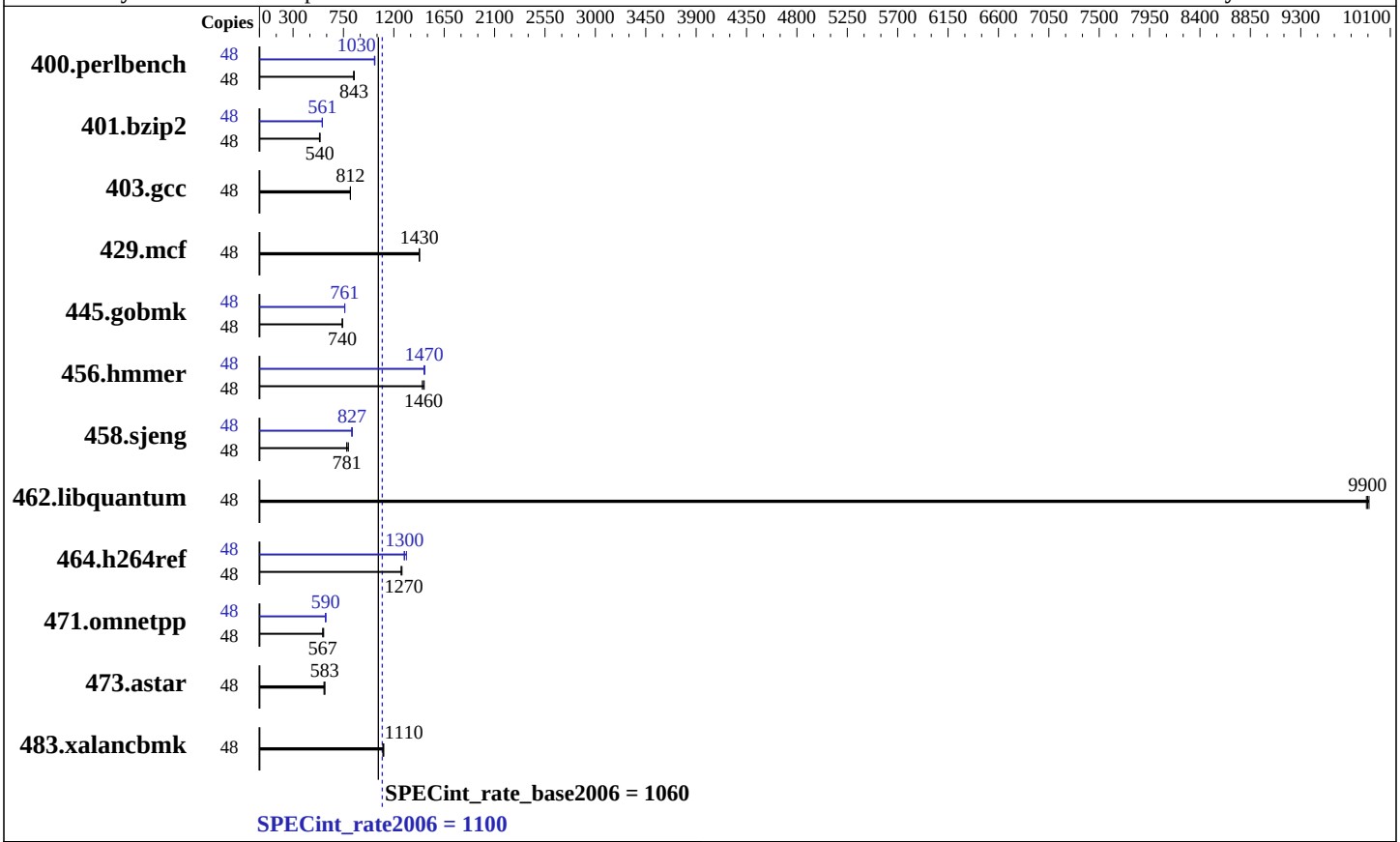
IBM Flex System x240 M5
(Intel Xeon E5-2690 v3, 2.60 GHz)

SPECint®_rate2006 = 1100

SPECint_rate_base2006 = 1060

CPU2006 license: 11
Test sponsor: Lenovo Group Limited
Tested by: IBM Corporation

Test date: Jan-2015
Hardware Availability: Dec-2014
Software Availability: Nov-2013



Hardware

CPU Name:	Intel Xeon E5-2690 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	24 cores, 2 chips, 12 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	30 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem:	1 x 1 TB SAS, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-424.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	557	841	554	847	**556**	**843**	48	**456**	**1030**	455	1030	457	1030
401.bzip2	48	865	536	857	540	**858**	**540**	48	**825**	**561**	826	561	825	561
403.gcc	48	**476**	**812**	476	811	476	813	48	**476**	**812**	476	811	476	813
429.mcf	48	307	1430	306	1430	**306**	**1430**	48	307	1430	306	1430	**306**	**1430**
445.gobmk	48	680	741	**680**	**740**	680	740	48	662	761	**661**	**761**	660	762
456.hmmer	48	**306**	**1460**	304	1470	308	1450	48	305	1470	**304**	**1470**	303	1480
458.sjeng	48	**743**	**781**	731	794	745	779	48	701	828	705	824	**702**	**827**
462.libquantum	48	101	9890	**100**	**9900**	100	9910	48	101	9890	**100**	**9900**	100	9910
464.h264ref	48	834	1270	839	1270	**838**	**1270**	48	**820**	**1300**	822	1290	809	1310
471.omnetpp	48	523	574	531	565	**530**	**567**	48	**508**	**590**	509	589	505	594
473.astar	48	578	583	585	576	**578**	**583**	48	578	583	585	576	**578**	**583**
483.xalancbmk	48	**300**	**1110**	301	1100	298	1110	48	**300**	**1110**	301	1100	298	1110

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Operating Mode set to Maximum Performance in BIOS
Fan speed set to 100%
Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on Bonneville-SPECcpu Thu Jan  1 15:21:36 2015

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2690 v3 @ 2.60GHz
      2 "physical id"s (chips)
      48 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
```

Continued on next page

Platform Notes (Continued)

```
        cpu cores : 12
        siblings  : 24
        physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
        physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
     cache size : 15360 KB

  From /proc/meminfo
     MemTotal:       264119260 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     Red Hat Enterprise Linux Server release 6.5 (Santiago)

  From /etc/*release* /etc/*version*
     redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
     system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

  uname -a:
     Linux Bonneville-SPECcpu 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST
     2013 x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Jan 1 15:21 last=5

  SPEC is set to: /cpu2006.1.2
     Filesystem                     Type  Size  Used Avail Use% Mounted on
     /dev/mapper/vg_bonnevillespe-lv_root ext4  356G   14G  324G   5% /

  Additional information from dmidecode:
    BIOS IBM -[C4E103EUS-1.00]- 11/25/2014
    Memory:
     8x NO DIMM Unknown
     16x Samsung M393A2G40DB0-CPB 16 GB 2133 MHz 2 rank

  (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RedHat EL 6.4
  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
  Filesystem page cache cleared with:
  echo 1>       /proc/sys/vm/drop_caches
  runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

Continued on next page

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

471.omnetpp: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html`
`http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-B.20141021.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml`
`http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-B.20141021.xml`

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