# SPEC® CINT2006 Result

## Hewlett-Packard Company
ProLiant DL180 Gen9
(1.80 GHz, Intel Xeon E5-2650L v3)

### SPECint_rate2006 = 776
SPECint_rate_base2006 = 743

<table>
<thead>
<tr>
<th>CPU2006 license:</th>
<th>3</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test sponsor:</td>
<td>Hewlett-Packard Company</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Hewlett-Packard Company</td>
</tr>
<tr>
<td>Test date:</td>
<td>Jan-2015</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Sep-2014</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Sep-2014</td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon E5-2650L v3</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 2.50 GHz</td>
</tr>
<tr>
<td>CPU MHZ:</td>
<td>1800</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>24 cores, 2 chips, 12 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>1,2 chips</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>256 KB I+D on chip per core</td>
</tr>
<tr>
<td>L3 Cache:</td>
<td>30 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other Cache:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)</td>
</tr>
<tr>
<td>Disk Subsystem:</td>
<td>1 x 400 GB SAS SSD, RAID 0</td>
</tr>
<tr>
<td>Other Hardware:</td>
<td>None</td>
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### Software

<table>
<thead>
<tr>
<th>Operating System:</th>
<th>Red Hat Enterprise Linux Server release 7.0 (Maipo)</th>
</tr>
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<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 15.0.0.0.090 of Intel C++ Studio XE for Linux</td>
</tr>
<tr>
<td>Auto Parallel:</td>
<td>No</td>
</tr>
<tr>
<td>File System:</td>
<td>xfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>32-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other Software:</td>
<td>Microquill SmartHeap V10.0</td>
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### Copies

<table>
<thead>
<tr>
<th>400.perlbench</th>
<th>48</th>
</tr>
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<tbody>
<tr>
<td>401.bzip2</td>
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<tr>
<td>403.gcc</td>
<td>48</td>
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<tr>
<td>429.mcf</td>
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<td>445.gobmk</td>
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<td>456.hmmer</td>
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<td>458.sjeng</td>
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<tr>
<td>462.libquantum</td>
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<td>464.h264ref</td>
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<tr>
<td>471.omnetpp</td>
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<tr>
<td>473.astar</td>
<td>48</td>
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<tr>
<td>483.xalancbmk</td>
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### SPECint_rate_base2006 = 743

![Graph](image-url)
Hewlett-Packard Company

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SPECint_rate_base2006 = 743

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
<th>PEAK</th>
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<td>408</td>
<td>812</td>
<td>407</td>
<td>814</td>
<td>813</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
  echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>

Platform Notes

BIOS Configuration:
  HP Power Profile set to Maximum Performance
  Collaborative Power Control set to Disabled
  QPI Snoop Configuration set to Cluster on Die
  Thermal Configuration set to Maximum Cooling
  Processor Power and Utilization Monitoring set to Disabled
  Memory Refresh Rate set to 1x Refresh
Sysinfo program /home/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 $$ e3fbb8667b5a285932ceab81e28219e1
running on kokomotop Fri Jan 16 13:46:43 2015

Continued on next page
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SPECint_rate2006 = 776
SPECint_rate_base2006 = 743

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650L v3 @ 1.80GHz
2 "physical id"s (chips)
48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblings : 12
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 15360 KB

From /proc/meminfo
MemTotal: 263711272 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*, /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:ga:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux kokomotop 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 15 12:21

SPEC is set to: /home/cpu2006

Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 318G 6.1G 312G 2% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
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Test date: Jan-2015
Hardware Availability: Sep-2014
Software Availability: Sep-2014

Platform Notes (Continued)

BIOS HP U20 12/10/2014
Memory:
16x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

C++ benchmarks:
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Base Portability Flags

400.perlbmk: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

Continued on next page
SPEC CINT2006 Result

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Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

    400.perlbench: icc -m64
    401.bzip2: icc -m64
    456.hmmer: icc -m64
    458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Peak Portability Flags

    400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
    401.bzip2: -DSPEC_CPU_LP64
    456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

    400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -auto-ilp32

    401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                -opt-prefetch -auto-ilp32 -ansi-alias

    403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

    429.mcf: basepeak = yes

Continued on next page
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Peak Optimization Flags (Continued)

445.gobmk: \text{-xCORE-AVX2} (pass 2) \text{-prof-gen} (pass 1) \text{-prof-use} (pass 2) \text{-ansi-alias} \text{-opt-mem-layout-trans=3}

456.hmmer: \text{-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32}

458.sjeng: \text{-xCORE-AVX2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)} \text{-O3 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)} \text{-unroll14 -auto-ilp32}

462.libquantum: basepeak = yes

464.h264ref: \text{-xCORE-AVX2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)} \text{-O3 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)} \text{-unroll2 -ansi-alias}

C++ benchmarks:

471.omnetpp: \text{-xCORE-AVX2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)} \text{-O3 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)} \text{-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap}

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: \text{-Dalloca=_alloca}

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/Flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml
Hewlett-Packard Company

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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