



# SPEC<sup>®</sup> CFP2006 Result

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## ACTION S.A.

ACTINA SOLAR 210 S6 (Intel Xeon E5-2630 v3, 2.40 GHz)

SPECfp<sup>®</sup>\_rate2006 = 577

SPECfp\_rate\_base2006 = 563

CPU2006 license: 9008

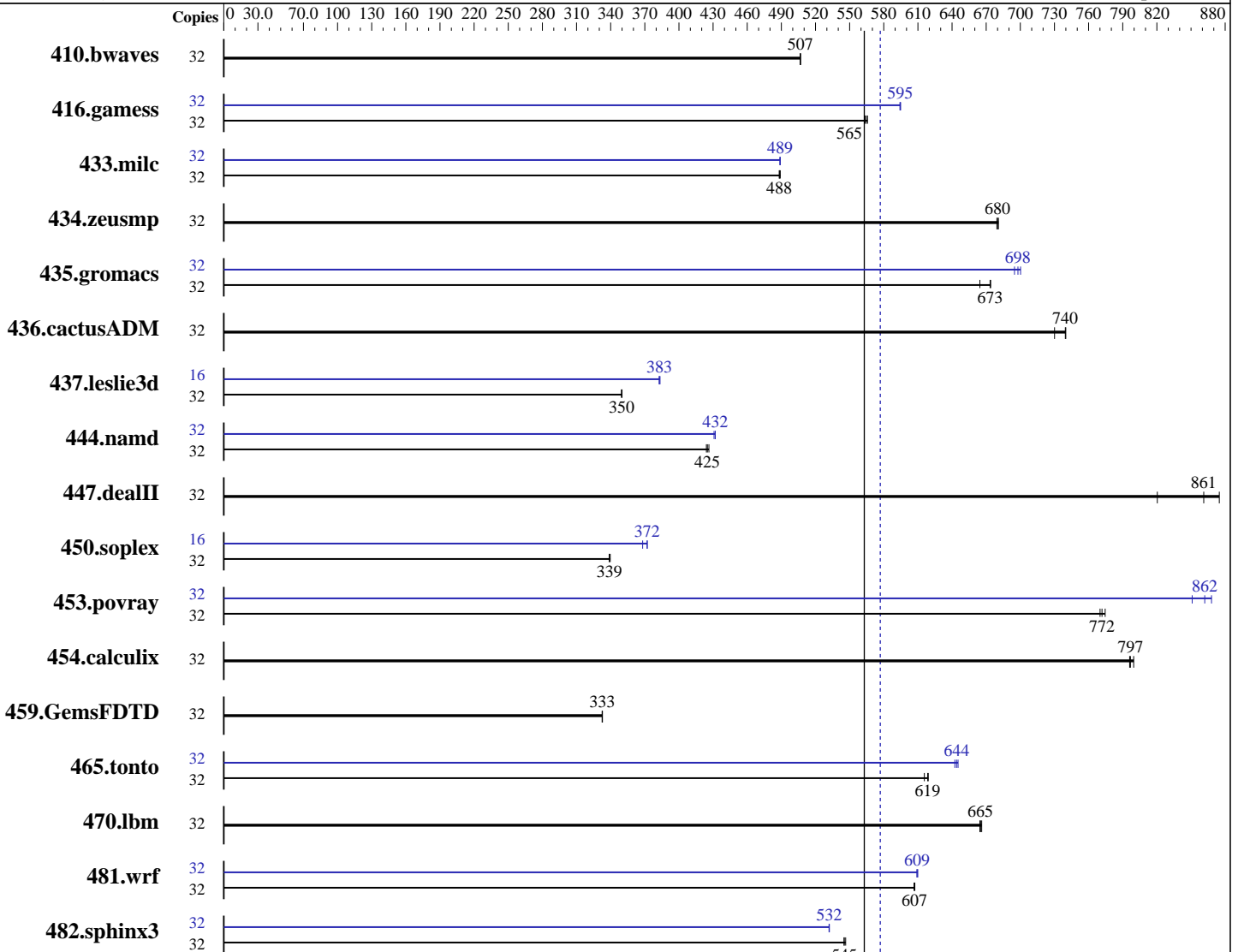
Test sponsor: ACTION S.A.

Tested by: ACTION S.A.

Test date: Jan-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014



SPECfp\_rate\_base2006 = 563

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### Hardware

CPU Name: Intel Xeon E5-2630 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 2.6.32-358.11.1.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

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L3 Cache: 20 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)  
Disk Subsystem: 1 x 240 GB SATA II SSD  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	32	859	507	858	507	<b>858</b>	<b>507</b>	32	859	507	858	507	<b>858</b>	<b>507</b>		
416.gamess	32	1108	566	<b>1109</b>	<b>565</b>	1112	564	32	1053	595	1054	594	<b>1054</b>	<b>595</b>		
433.milc	32	601	489	602	488	<b>602</b>	<b>488</b>	32	601	489	601	489	<b>601</b>	<b>489</b>		
434.zeusmp	32	428	681	<b>428</b>	<b>680</b>	429	679	32	428	681	<b>428</b>	<b>680</b>	429	679		
435.gromacs	32	<b>339</b>	<b>673</b>	339	674	344	664	32	326	700	329	695	<b>327</b>	<b>698</b>		
436.cactusADM	32	<b>517</b>	<b>740</b>	517	740	524	730	32	<b>517</b>	<b>740</b>	517	740	524	730		
437.leslie3d	32	<b>860</b>	<b>350</b>	859	350	861	350	16	392	383	<b>393</b>	<b>383</b>	393	383		
444.namd	32	605	424	<b>604</b>	<b>425</b>	602	426	32	594	432	<b>594</b>	<b>432</b>	596	431		
447.dealII	32	446	820	418	875	<b>425</b>	<b>861</b>	32	446	820	418	875	<b>425</b>	<b>861</b>		
450.soplex	32	788	339	<b>787</b>	<b>339</b>	786	340	16	<b>359</b>	<b>372</b>	363	368	359	372		
453.povray	32	<b>221</b>	<b>772</b>	220	775	221	770	32	196	868	200	851	<b>197</b>	<b>862</b>		
454.calculix	32	330	800	<b>331</b>	<b>797</b>	332	796	32	330	800	<b>331</b>	<b>797</b>	332	796		
459.GemsFDTD	32	<b>1021</b>	<b>333</b>	1021	333	1020	333	32	<b>1021</b>	<b>333</b>	1021	333	1020	333		
465.tonto	32	511	616	<b>509</b>	<b>619</b>	509	619	32	490	642	488	645	<b>489</b>	<b>644</b>		
470.lbm	32	662	664	<b>661</b>	<b>665</b>	660	666	32	662	664	<b>661</b>	<b>665</b>	660	666		
481.wrf	32	<b>589</b>	<b>607</b>	589	607	589	607	32	587	609	<b>587</b>	<b>609</b>	586	610		
482.sphinx3	32	1141	547	<b>1144</b>	<b>545</b>	1144	545	32	1173	532	<b>1172</b>	<b>532</b>	1172	532		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Bios Settings  
Power Technology = Energy Efficient

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### Platform Notes (Continued)

Enforce POR = Disabled

BMC Setting

Fan Mode = Full Speed

Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191

running on localhost.localdomain Fri Jan 16 12:37:57 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2630 v3 @ 2.40GHz

2 "physical id"s (chips)

32 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 8

siblings : 16

physical 0: cores 0 1 2 3 4 5 6 7

physical 1: cores 0 1 2 3 4 5 6 7

cache size : 20480 KB

From /proc/meminfo

MemTotal: 264424128 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d

Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/\*release\* /etc/\*version\*

redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)

system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:6server:ga:server

uname -a:

Linux localhost.localdomain 2.6.32-358.11.1.el6.x86\_64 #1 SMP Tue Nov 19

17:43:04 CET 2013 x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Jan 16 12:34

SPEC is set to: /cpu2006.1.2

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sdal	ext4	193G	43G	141G	24%	/

Additional information from dmidecode:

BIOS American Megatrends Inc. 1.0b 10/29/2014

Memory:

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## Platform Notes (Continued)

16x 16 GB

2x Samsung (date:14/33) M393A2G40DB0-CPB 16 GB 1866 MHz 2 rank

14x Samsung (date:14/4p) M393A2G40DB0-CPB 16 GB 1866 MHz 2 rank

(End of data from sysinfo program)

dmidecode does not properly detect memory modules

16 modules of 16 GB were used to run the test (256 GB total)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Binaries compiled on a system with 2x Xeon E5-2670 v3 chips + 128 GB memory using RedHat EL 6.4

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
482.sphinx3: icc -m32
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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**Software Availability:** Sep-2014

## Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2)
         -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
         -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
            -unroll2

```

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2)
         -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
         -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2)
         -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
         -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2)
         -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
         -ansi-alias

```

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## Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevC-jan-2015-For-Supermicro-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevC-jan-2015-For-Supermicro-Platform.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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