Cisco Systems

Cisco UCS C240 M4 (Intel Xeon E5-2695 v3 @ 2.30GHz)

SPECint®2006 = 61.3
SPECint_base2006 = 59.1

CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

Test date: Feb-2015
Hardware Availability: Sep-2014
Software Availability: Jul-2014

Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon E5-2695 v3</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 3.30 GHz</td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>2300</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>28 cores, 2 chips, 14 cores/chip</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>1.2 chips</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>256 KB I+D on chip per core</td>
</tr>
<tr>
<td>L3 Cache:</td>
<td>35 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other Cache:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)</td>
</tr>
<tr>
<td>Disk Subsystem:</td>
<td>1 x 400GB SSD SAS</td>
</tr>
<tr>
<td>Other Hardware:</td>
<td>None</td>
</tr>
</tbody>
</table>

Software

| Operating System: | Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64 |
| Compiler: | C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux |
| Auto Parallel: | Yes |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32/64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V10.0 |
Spec CINT2006 Result

Cisco Systems
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>400.perlbench</td>
<td>257</td>
<td>38.1</td>
<td>257</td>
<td>38.0</td>
<td>258</td>
<td>37.9</td>
<td>223</td>
<td>43.9</td>
<td>222</td>
<td>43.9</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>421</td>
<td>22.9</td>
<td>423</td>
<td>22.8</td>
<td>419</td>
<td>23.0</td>
<td>415</td>
<td>23.2</td>
<td>416</td>
<td>23.2</td>
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<tr>
<td>403.gcc</td>
<td>249</td>
<td>32.3</td>
<td>249</td>
<td>32.3</td>
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<td>32.3</td>
<td>246</td>
<td>32.7</td>
<td>245</td>
<td>32.9</td>
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<tr>
<td>429.mcf</td>
<td>163</td>
<td>56.0</td>
<td>163</td>
<td>55.8</td>
<td>164</td>
<td>55.5</td>
<td>163</td>
<td>56.0</td>
<td>164</td>
<td>55.7</td>
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<td>445.gobmk</td>
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<td>26.9</td>
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<td>27.0</td>
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<td>26.9</td>
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<td>27.0</td>
<td>387</td>
<td>27.1</td>
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<tr>
<td>456.hmmer</td>
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<td>143</td>
<td>65.2</td>
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<td>65.0</td>
<td>148</td>
<td>62.8</td>
<td>148</td>
<td>63.0</td>
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<td>458.sjeng</td>
<td>376</td>
<td>32.1</td>
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<td>32.1</td>
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<td>32.2</td>
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<td>32.3</td>
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<td>32.4</td>
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<tr>
<td>462.libquantum</td>
<td>3.08</td>
<td>6740</td>
<td>3.27</td>
<td>6340</td>
<td>3.07</td>
<td>6760</td>
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<td>6740</td>
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<tr>
<td>464.h264ref</td>
<td>510</td>
<td>43.4</td>
<td>512</td>
<td>43.2</td>
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<td>510</td>
<td>43.4</td>
<td>512</td>
<td>43.2</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>170</td>
<td>36.8</td>
<td>167</td>
<td>37.5</td>
<td>167</td>
<td>37.4</td>
<td>126</td>
<td>49.7</td>
<td>127</td>
<td>49.3</td>
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<tr>
<td>473.astar</td>
<td>228</td>
<td>30.7</td>
<td>226</td>
<td>31.1</td>
<td>227</td>
<td>30.9</td>
<td>226</td>
<td>31.0</td>
<td>224</td>
<td>31.4</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>114</td>
<td>60.7</td>
<td>114</td>
<td>60.4</td>
<td>114</td>
<td>60.7</td>
<td>112</td>
<td>61.7</td>
<td>112</td>
<td>61.8</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The config file option 'submit' was used.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes
CPU performance set to Enterprise
Power Technology set to Performance
Energy Performance BIAS setting set to Performance
Memory RAS configuration set to Maximum Performance
Intel Hyper-Threading Technology option set to Disabled
QPI Snoop Mode set to Early Snoop
Sysinfo program /opt/cpu2006-1.2/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Thu Feb 5 02:24:32 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2695 v3 @ 2.30GHz
  2 "physical id"s (chips)
  28 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
Continued on next page
Cisco Systems
Cisco UCS C240 M4 (Intel Xeon E5-2695 v3 @ 2.30GHz)  

| SPECint2006 | 61.3 |
| SPECint_base2006 | 59.1 |

CPU2006 license: 9019  
Test sponsor: Cisco Systems  
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Test date: Feb-2015  
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Platform Notes (Continued)

following excerpts from /proc/cpuinfo might not be reliable. Use with caution.

cpu cores : 14
siblings : 14
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14

cache size : 35840 KB

From /proc/meminfo
MemTotal: 263706032 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="#31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:ga:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 4 20:45

SPEC is set to: /opt/cpu2006-1.2

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. C240M4.2.0.3d.0.111120141515 11/11/2014
Memory:
16x 0xCE00 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
8x NO DIMM NO DIMM

(End of data from sysinfo program)
Cisco Systems
Cisco UCS C240 M4 (Intel Xeon E5-2695 v3 @ 2.30GHz)

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General Notes
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity= fine, compact, 1, 0"
LD_LIBRARY_PATH = "/opt/cpu2006-1.2/libs/32:/opt/cpu2006-1.2/libs/64:/opt/cpu2006-1.2/sh"
OMP_NUM_THREADS = "28"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation
C benchmarks:
   icc -m64
C++ benchmarks:
   icpc -m64

Base Portability Flags
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags
C benchmarks:
   -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
C++ benchmarks:
   -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
   -Wl,-z,muldefs -L/sh -lsmartheap64

Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
Cisco Systems
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SPECint_base2006 = 59.1

CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

Base Other Flags
C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation
C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
445.gobmk: icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

C++ benchmarks (except as noted below):
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
473.astar: icpc -m64

Peak Portability Flags
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags
C benchmarks:
400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

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| Hardware Availability: | Sep-2014 |
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**SPEC CINT2006 Result**

**SPECint2006 =** 61.3

**SPECint_base2006 =** 59.1

---

**Peak Optimization Flags (Continued)**

403.gcc:
- -xCORE-AVX2
- -ipo
- -O3
- -no-prec-div
- -inline-calloc
- -opt-malloc-options=3
- -auto-ilp32

429.mcf:
- -xCORE-AVX2
- -ipo
- -O3
- -no-prec-div
- -parallel
- -opt-prefetch
- -auto-p32

445.gobmk:
- -xCORE-AVX2
  (pass 2)
- -prof-gen
  (pass 1)
- -prof-use
  (pass 2)
- -ansi-alias

456.hmmer:
- -xCORE-AVX2
  (pass 2)
- -prof-gen
  (pass 1)
- -ipo
  (pass 2)
- -O3
  (pass 2)
- -no-prec-div
  (pass 2)
- -prof-use
  (pass 2)
- -unroll4

462.libquantum:
  basepeak = yes

464.h264ref:
  basepeak = yes

**C++ benchmarks:**

471.omnetpp:
- -xCORE-AVX2
  (pass 2)
- -prof-gen
  (pass 1)
- -ipo
  (pass 2)
- -O3
  (pass 2)
- -no-prec-div
  (pass 2)
- -prof-use
  (pass 2)
- -opt-ra-region-strategy=block
- -ansi-alias
- -Wl,-z,muldefs
- -L/sh
- -lsmartheap

473.astar:
- -xCORE-AVX2
- -ipo
- -O3
- -no-prec-div
- -opt-prefetch
- -auto-p32
- -Wl,-z,muldefs
- -L/sh
- -lsmartheap64

483.xalancbmk:
- -xCORE-AVX2
- -ipo
- -O3
- -no-prec-div
- -opt-prefetch
- -ansi-alias
- -Wl,-z,muldefs
- -L/sh
- -lsmartheap

---

**Peak Other Flags**

**C benchmarks:**

403.gcc:
- -Dalloca=_alloca

---

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html

http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revC.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml

http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revC.xml
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For questions about this result, please contact the tester. For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.