



# SPEC<sup>®</sup> CFP2006 Result

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## ZTE

SPECfp<sup>®</sup>2006 = 114

## R5300 G3 (Intel Xeon E5-2660 v3)

SPECfp\_base2006 = 109

CPU2006 license: 3834

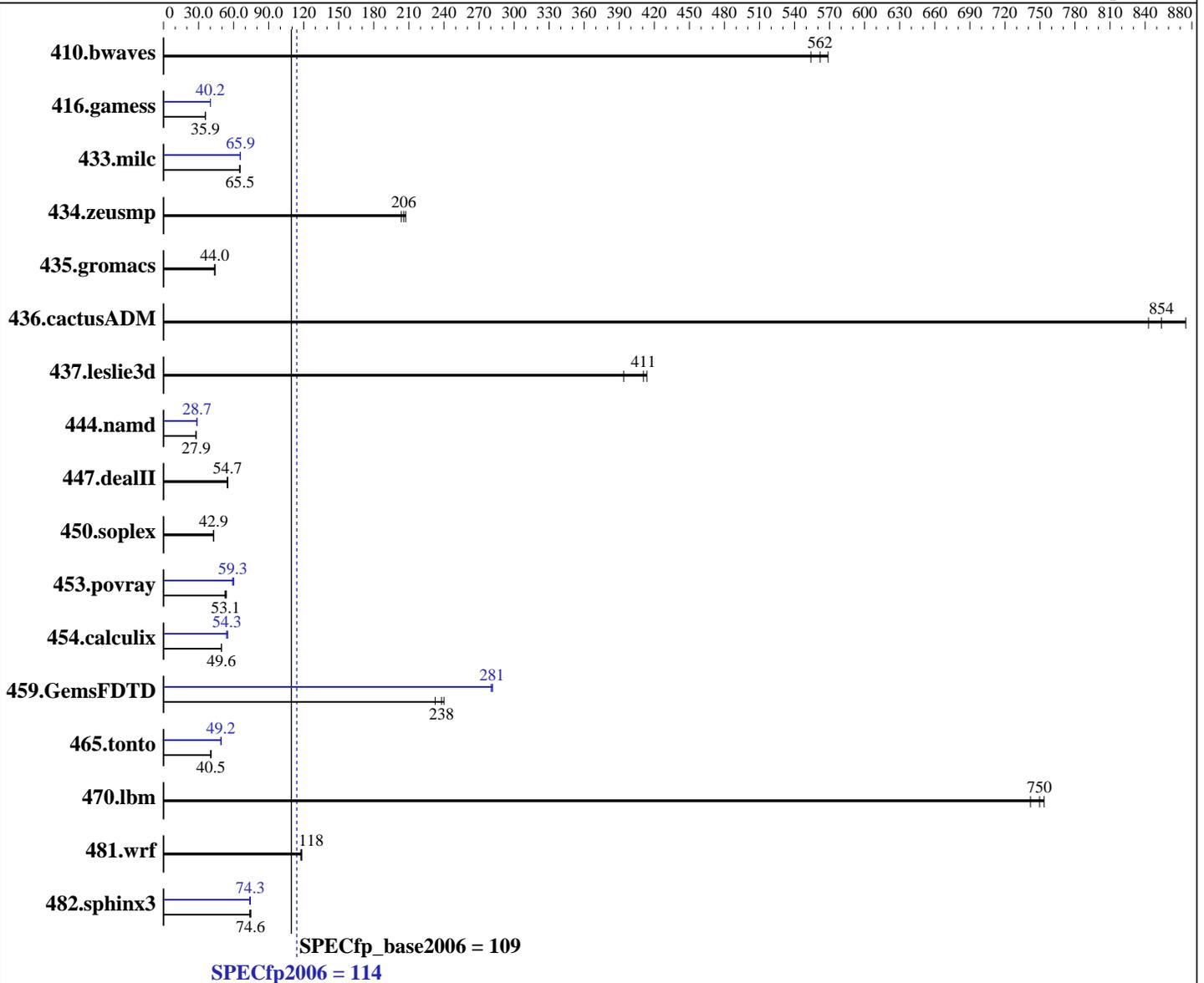
Test sponsor: ZTE

Tested by: ZTE

Test date: Mar-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E5-2660 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 7.0(Maipo) 3.10.0-121.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;  
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 25 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
Disk Subsystem: 1 x 300 GB SAS, 10K RPM  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

### Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	23.9	569	24.5	554	<u>24.2</u>	<u>562</u>	23.9	569	24.5	554	<u>24.2</u>	<u>562</u>
416.gamess	547	35.8	544	36.0	<u>546</u>	<u>35.9</u>	487	40.2	<u>487</u>	<u>40.2</u>	487	40.2
433.milc	<u>140</u>	<u>65.5</u>	140	65.5	141	65.0	139	65.9	<u>139</u>	<u>65.9</u>	140	65.5
434.zeusmp	44.7	204	43.9	207	<u>44.3</u>	<u>206</u>	44.7	204	43.9	207	<u>44.3</u>	<u>206</u>
435.gromacs	164	43.6	<u>162</u>	<u>44.0</u>	162	44.1	164	43.6	<u>162</u>	<u>44.0</u>	162	44.1
436.cactusADM	13.7	875	<u>14.0</u>	<u>854</u>	14.2	843	13.7	875	<u>14.0</u>	<u>854</u>	14.2	843
437.leslie3d	22.7	414	<u>22.9</u>	<u>411</u>	23.9	394	22.7	414	<u>22.9</u>	<u>411</u>	23.9	394
444.namd	287	27.9	<u>287</u>	<u>27.9</u>	288	27.9	280	28.6	<u>280</u>	<u>28.7</u>	280	28.7
447.dealII	<u>209</u>	<u>54.7</u>	209	54.6	208	55.1	<u>209</u>	<u>54.7</u>	209	54.6	208	55.1
450.soplex	195	42.9	194	43.0	<u>194</u>	<u>42.9</u>	195	42.9	194	43.0	<u>194</u>	<u>42.9</u>
453.povray	<u>100</u>	<u>53.1</u>	101	52.6	98.7	53.9	<u>89.7</u>	<u>59.3</u>	88.3	60.2	89.8	59.2
454.calculix	166	49.6	166	49.6	<u>166</u>	<u>49.6</u>	150	55.0	153	54.1	<u>152</u>	<u>54.3</u>
459.GemsFDTD	<u>44.6</u>	<u>238</u>	45.6	232	44.2	240	<u>37.7</u>	<u>281</u>	37.7	282	37.8	280
465.tonto	244	40.4	<u>243</u>	<u>40.5</u>	242	40.6	199	49.4	<u>200</u>	<u>49.2</u>	200	49.2
470.lbm	<u>18.3</u>	<u>750</u>	18.2	753	18.5	742	<u>18.3</u>	<u>750</u>	18.2	753	18.5	742
481.wrf	94.5	118	<u>94.7</u>	<u>118</u>	95.1	117	94.5	118	<u>94.7</u>	<u>118</u>	95.1	117
482.sphinx3	261	74.7	<u>261</u>	<u>74.6</u>	264	73.8	<u>262</u>	<u>74.3</u>	264	73.7	262	74.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

BIOS settings:  
Turbo boost Technology enabled  
Virtualization Technology disabled  
Hyper Threading Technology enabled  
Sysinfo program /home/speccpu/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ 2b55956e7c0e338e808a36a21505f13a  
running on localhost.localdomain Sun Mar 15 07:03:45 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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## Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-2660 v3 @ 2.60GHz
 2 "physical id"s (chips)
 40 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 10
siblings   : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB
```

From /proc/meminfo

```
MemTotal:      263642568 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
```

uname -a:

```
Linux localhost.localdomain 3.10.0-121.el7.x86_64 #1 SMP Tue Apr 8 10:48:19 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Mar 14 16:41

SPEC is set to: /home/speccpu

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   225G  66G  159G  30% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. UBF10.01.07\_SVN0 02/12/2015

Memory:

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## Platform Notes (Continued)

16x Micron 36ADS2G72PZ-2G1A1 16 GB 2 rank 2133 MHz  
8x NO DIMM Unknown

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/home/speccpu/libs/32:/home/speccpu/libs/64:/home/speccpu/sh"

OMP\_NUM\_THREADS = "20"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB  
memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags



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Software Availability: Sep-2014

## Peak Optimization Flags

### C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

### C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-inline-calloc -opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

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## Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/ZTE-Platform-Flags-V2.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/ZTE-Platform-Flags-V2.0.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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