Dell Inc.

PowerEdge M830 (Intel Xeon E5-4627 v3, 2.60 GHz)

SPECint\_rate2006 = 1590

SPECint\_rate\_base2006 = 1540

Hardware

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
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<tbody>
<tr>
<td>CPU Name:</td>
<td>Intel Xeon E5-4627 v3</td>
</tr>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 3.20 GHz</td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>2600</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>40 cores, 4 chips, 10 cores/chip</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>4 chip</td>
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<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
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<tr>
<td>Secondary Cache:</td>
<td>256 KB I+D on chip per core</td>
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<tr>
<td>L3 Cache:</td>
<td>25 MB I+D on chip per chip</td>
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<tr>
<td>Other Cache:</td>
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<tr>
<td>Memory:</td>
<td>512 GB (32 x 16 GB 2Rx4 PC4-2133P-R)</td>
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<tr>
<td>Disk Subsystem:</td>
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<td>Other Hardware:</td>
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Software

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<tr>
<td>Operating System:</td>
<td>SUSE Linux Enterprise Server 12 3.12.28-4-default</td>
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<tr>
<td>Compiler:</td>
<td>C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux</td>
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<td>Auto Parallel:</td>
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<tr>
<td>File System:</td>
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<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
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<tr>
<td>Base Pointers:</td>
<td>32-bit</td>
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<tr>
<td>Peak Pointers:</td>
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<td>Other Software:</td>
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Results Table

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<tr>
<th>Benchmark</th>
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<td>462.libquantum</td>
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<td>473.astar</td>
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<td>847</td>
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<td>483.xalancbmk</td>
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<td>1820</td>
<td>152</td>
<td>1820</td>
<td>152</td>
<td>1820</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Virtualization Technology disabled
Execute Disable disabled
System Profile set to Performance
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-4bm6 Tue Feb 3 09:55:12 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-4627 v3 @ 2.60GHz
    4 "physical id"s (chips)
    40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The Continued on next page
Dell Inc.

PowerEdge M830 (Intel Xeon E5-4627 v3, 2.60 GHz)

SPECint_rate2006 = 1590
SPECint_rate_base2006 = 1540

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Platform Notes (Continued)

following excerpts from /proc/cpuinfo might not be reliable. Use with caution.

- cpu cores : 10
- siblings : 10
- physical 0: cores 0 1 2 3 4 8 9 10 11 12
- physical 1: cores 0 1 2 3 4 8 9 10 11 12
- physical 2: cores 0 1 2 3 4 8 9 10 11 12
- physical 3: cores 0 1 2 3 4 8 9 10 11 12
- cache size : 25600 KB

From /proc/meminfo
- MemTotal: 529334380 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12

From /etc/*release* /etc/*version*
SuSE-release:
- SUSE Linux Enterprise Server 12 (x86_64)
- VERSION = 12
- PATCHLEVEL = 0
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.

os-release:
- NAME="SLES"
- VERSION="12"
- VERSION_ID="12"
- PRETTY_NAME="SUSE Linux Enterprise Server 12"
- ID="sles"
- ANSI_COLOR="0;32"
- CPE_NAME="cpe:/o:suse:sles:12"

uname -a:
- Linux linux-4bm6 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
  (9879bd4) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 3 09:54

SPEC is set to: /root/cpu2006-1.2
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 ext4 359G 8.7G 350G 3% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 0.3.8 12/18/2014

Continued on next page
**SPEC CINT2006 Result**

**Dell Inc.**

PowerEdge M830 (Intel Xeon E5-4627 v3, 2.60 GHz)

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<tr>
<th>SPECint_rate2006 = 1590</th>
<th>SPECint_rate_base2006 = 1540</th>
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</thead>
<tbody>
<tr>
<td><strong>CPU2006 license:</strong> 55</td>
<td><strong>Test date:</strong> Feb-2015</td>
</tr>
<tr>
<td><strong>Test sponsor:</strong> Dell Inc.</td>
<td><strong>Hardware Availability:</strong> Jun-2015</td>
</tr>
<tr>
<td><strong>Tested by:</strong> Dell Inc.</td>
<td><strong>Software Availability:</strong> Jun-2015</td>
</tr>
</tbody>
</table>

**Platform Notes (Continued)**

Memory:
- 32x 00CE00B300CE M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
- 16x Not Specified Not Specified

(End of data from sysinfo program)

**General Notes**

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0
Transparent Huge Pages enabled with:
- echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
- echo 1> /proc/sys/vm/drop_caches
rusespec command invoked through numactl i.e.:
- numactl --interleave=all runspec <etc>

**Base Compiler Invocation**

C benchmarks:
- icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

C++ benchmarks:
- icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

**Base Portability Flags**

- 400.perlbench: -DSPEC_CPU_LINUX_IA32
- 462.libquantum: -DSPEC_CPU_LINUX
- 483.xalancbmk: -DSPEC_CPU_LINUX

**Base Optimization Flags**

C benchmarks:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
- -opt-mem-layout-trans=3

C++ benchmarks:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
- -opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
Dell Inc.

PowerEdge M830 (Intel Xeon E5-4627 v3, 2.60 GHz)

**SPECint_rate2006 = 1590**

**SPECint_rate_base2006 = 1540**

- **CPU2006 license:** 55
- **Test date:** Feb-2015
- **Test sponsor:** Dell Inc.
- **Hardware Availability:** Jun-2015
- **Tested by:** Dell Inc.
- **Software Availability:** Jun-2015

### Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

### Peak Compiler Invocation

C benchmarks (except as noted below):

```text
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64
```

C++ benchmarks:

```text
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

### Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`

401.bzip2: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LINUX`

483.xalancbmk: `-DSPEC_CPU_LINUX`

### Peak Optimization Flags

C benchmarks:

```text
400.perlbench: `xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32`

401.bzip2: `xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `xCORE-AVX2 -ipo -O3 -no-prec-div`
```

Continued on next page
Dell Inc.

PowerEdge M830 (Intel Xeon E5-4627 v3, 2.60 GHz)

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CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.
Test date: Feb-2015
Hardware Availability: Jun-2015
Software Availability: Jun-2015

Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
  -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
  -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
  -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
  -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
  -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revE.20150421.xml
## Dell Inc.

PowerEdge M830 (Intel Xeon E5-4627 v3, 2.60 GHz)

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For other inquiries, please contact webmaster@spec.org.

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