



# SPEC® CINT2006 Result

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## Lenovo Group Limited

Lenovo NeXtScale nx360 M5  
(Intel Xeon E5-2609 v3, 1.90 GHz)

**SPECint\_rate2006 = 318**

**SPECint\_rate\_base2006 = 308**

CPU2006 license: 9017

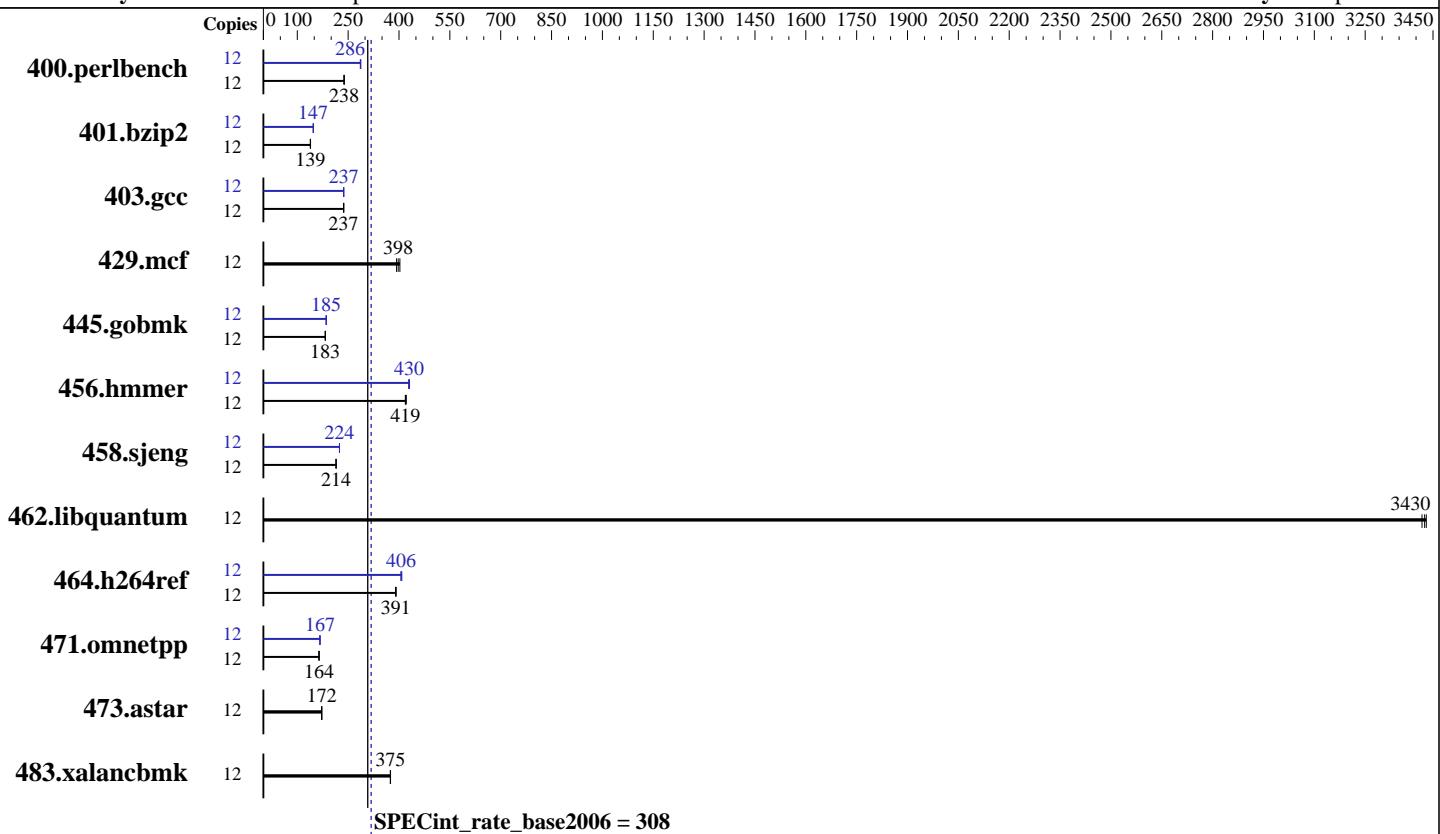
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

**Test date:** May-2015

**Hardware Availability:** Nov-2014

**Software Availability:** Sep-2014



### Hardware

CPU Name: Intel Xeon E5-2609 v3  
CPU Characteristics:  
CPU MHz: 1900  
FPU: Integrated  
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core  
L3 Cache: 15 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM  
Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
Compiler: 3.10.0-123.el7.x86\_64  
Auto Parallel: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 32-bit  
Peak Pointers: 32/64-bit  
Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	492	239	493	238	<b>492</b>	<b>238</b>	12	<b>409</b>	<b>286</b>	408	288	410	286
401.bzip2	12	832	139	836	138	<b>834</b>	<b>139</b>	12	<b>787</b>	<b>147</b>	786	147	<b>786</b>	<b>147</b>
403.gcc	12	407	237	<b>408</b>	<b>237</b>	408	237	12	<b>408</b>	<b>237</b>	407	237	408	237
429.mcf	12	279	393	272	403	<b>275</b>	<b>398</b>	12	279	393	272	403	<b>275</b>	<b>398</b>
445.gobmk	12	690	183	689	183	<b>689</b>	<b>183</b>	12	<b>679</b>	<b>185</b>	679	185	<b>679</b>	185
456.hmmer	12	267	419	266	422	<b>267</b>	<b>419</b>	12	260	431	261	429	<b>261</b>	<b>430</b>
458.sjeng	12	677	215	<b>677</b>	<b>214</b>	677	214	12	647	224	<b>647</b>	<b>224</b>	647	225
462.libquantum	12	72.5	3430	<b>72.6</b>	<b>3430</b>	72.8	3420	12	72.5	3430	<b>72.6</b>	<b>3430</b>	72.8	3420
464.h264ref	12	681	390	<b>679</b>	<b>391</b>	678	391	12	650	408	654	406	<b>654</b>	<b>406</b>
471.omnetpp	12	458	164	<b>457</b>	<b>164</b>	456	165	12	<b>449</b>	<b>167</b>	448	167	<b>451</b>	166
473.astar	12	488	172	491	172	<b>489</b>	<b>172</b>	12	488	172	491	172	<b>489</b>	<b>172</b>
483.xalancbmk	12	221	375	<b>221</b>	<b>375</b>	221	374	12	221	375	<b>221</b>	<b>375</b>	221	374

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS setting:

Operating Mode set to "Efficiency-Favor Performance"

Sysinfo program /home/SPEC/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date::: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on wilykat-2.labs.lenovo.com Mon May 11 06:10:08 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2609 v3 @ 1.90GHz  
2 "physical id"s (chips)

12 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

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## Platform Notes (Continued)

```
cpu cores : 6
siblings : 6
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB

From /proc/meminfo
MemTotal:      263588292 kB
HugePages_Total:       0
Hugepagesize:     2048 kB

From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.0 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.0"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux wilykat-2.labs.lenovo.com 3.10.0-123.el7.x86_64 #1 SMP Mon May 5
11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

SPEC is set to: /home/SPEC
Filesystem           Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-root xfs   927G  9.2G  918G  1% /
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[THE106CUS-1.11]- 02/16/2015
Memory:
 10x Hynix HMA42GR7MFR4N-TF 16 GB 2 rank 2133 MHz
 6x Hynix HMA42GR7MFR4N-TFT1 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)
```



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## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/SPEC/libs/32:/home/SPEC/libs/64:/home/SPEC/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB

memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll14 -auto-ilp32

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll12 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-B.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-B.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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