## Lenovo Group Limited

**Lenovo System x3500 M5**  
(Intel Xeon E5-2637 v3, 3.50 GHz)

| SPECfp®2006 | 108 |
| SPECfp_base2006 | 104 |

**CPU2006 license:** 9017  
**Test date:** May-2015  
**Test sponsor:** Lenovo Group Limited  
**Tested by:** Lenovo Group Limited  

### SPECfp2006 Results

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Score</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>46.3</td>
</tr>
<tr>
<td>416.gamess</td>
<td>73.8</td>
</tr>
<tr>
<td>433.milc</td>
<td>184</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>51.8</td>
</tr>
<tr>
<td>435.gromacs</td>
<td></td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>32.1</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>41.6</td>
</tr>
<tr>
<td>444.namd</td>
<td>68.4</td>
</tr>
<tr>
<td>447.dealII</td>
<td>61.9</td>
</tr>
<tr>
<td>450.soplex</td>
<td>58.6</td>
</tr>
<tr>
<td>453.povray</td>
<td>56.7</td>
</tr>
<tr>
<td>454.calculix</td>
<td>48.2</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>188</td>
</tr>
<tr>
<td>465.tonto</td>
<td>59.8</td>
</tr>
<tr>
<td>470.lbm</td>
<td>31.3</td>
</tr>
<tr>
<td>481.wrf</td>
<td>103</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>87.4</td>
</tr>
</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Xeon E5-2637 v3  
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.70 GHz  
- **CPU MHz:** 3500  
- **FPU:** Integrated  
- **CPU(s) enabled:** 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
- **CPU(s) orderable:** 1,2 chips  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 256 KB I+D on chip per core

**Software**

- **Operating System:** Red Hat Enterprise Linux Server release 7.0 (Maipo)  
- **Compiler:** C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux; Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
- **Auto Parallel:** Yes  
- **File System:** xfs

---

Continued on next page
Lenovo Group Limited

Lenovo System x3500 M5
(Intel Xeon E5-2637 v3, 3.50 GHz)

SPECfp2006 = 108
SPECfp_base2006 = 104

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited
Test date: May-2015
Hardware Availability: Jan-2015
Software Availability: Sep-2014

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Pointers: None

L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem: 1 x 960 GB SATA SSD
Other Hardware: None

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>29.6</td>
<td>459</td>
<td>29.6</td>
<td>459</td>
<td>29.4</td>
<td>463</td>
</tr>
<tr>
<td>416.gamess</td>
<td>462</td>
<td>42.3</td>
<td>463</td>
<td>42.3</td>
<td>462</td>
<td>42.4</td>
</tr>
<tr>
<td>433.milc</td>
<td>125</td>
<td>73.2</td>
<td>125</td>
<td>73.2</td>
<td>126</td>
<td>73.1</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>49.3</td>
<td>184</td>
<td>49.4</td>
<td>184</td>
<td>49.2</td>
<td>185</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>138</td>
<td>51.8</td>
<td>138</td>
<td>51.6</td>
<td>138</td>
<td>51.9</td>
</tr>
<tr>
<td>436.cactus</td>
<td>21.9</td>
<td>545</td>
<td>22.0</td>
<td>542</td>
<td>21.9</td>
<td>545</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>36.0</td>
<td>261</td>
<td>35.9</td>
<td>261</td>
<td>35.4</td>
<td>266</td>
</tr>
<tr>
<td>444.namd</td>
<td>256</td>
<td>31.3</td>
<td>257</td>
<td>31.2</td>
<td>256</td>
<td>31.3</td>
</tr>
<tr>
<td>447.dealII</td>
<td>193</td>
<td>59.2</td>
<td>191</td>
<td>60.0</td>
<td>191</td>
<td>59.8</td>
</tr>
<tr>
<td>450.soplex</td>
<td>201</td>
<td>41.6</td>
<td>197</td>
<td>42.3</td>
<td>201</td>
<td>41.4</td>
</tr>
<tr>
<td>453.povray</td>
<td>86.9</td>
<td>61.2</td>
<td>86.8</td>
<td>61.3</td>
<td>86.5</td>
<td>61.5</td>
</tr>
<tr>
<td>454.calculix</td>
<td>141</td>
<td>58.6</td>
<td>141</td>
<td>58.5</td>
<td>141</td>
<td>58.6</td>
</tr>
<tr>
<td>459.GemDFDTD</td>
<td>56.5</td>
<td>188</td>
<td>56.4</td>
<td>188</td>
<td>57.5</td>
<td>184</td>
</tr>
<tr>
<td>465.tonto</td>
<td>203</td>
<td>48.4</td>
<td>204</td>
<td>48.2</td>
<td>205</td>
<td>48.1</td>
</tr>
<tr>
<td>470.lbm</td>
<td>28.8</td>
<td>477</td>
<td>28.7</td>
<td>479</td>
<td>28.1</td>
<td>488</td>
</tr>
<tr>
<td>481.wrf</td>
<td>109</td>
<td>103</td>
<td>111</td>
<td>101</td>
<td>107</td>
<td>104</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>224</td>
<td>87.1</td>
<td>224</td>
<td>87.0</td>
<td>224</td>
<td>86.9</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS setting:
Operating Mode set to "Efficiency-Favor Performance"
Sysinfo program /home/SPEC/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on x3500M5 Fri May 15 08:47:04 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

Continued on next page
Lenovo Group Limited
Lenovo System x3500 M5
(Intel Xeon E5-2637 v3, 3.50 GHz)

SPECfp2006 = 108
SPECfp_base2006 = 104

CPU2006 license: 9017
Test date: May-2015
Test sponsor: Lenovo Group Limited
Hardware Availability: Jan-2015
Tested by: Lenovo Group Limited
Software Availability: Sep-2014

Platform Notes (Continued)

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2637 v3 @ 3.50GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings : 8
  physical 0: cores 0 1 4 5
  physical 1: cores 0 1 4 5
  cache size : 15360 KB

From /proc/meminfo
MemTotal:       263457684 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.0 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.0"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
  Linux x3500M5 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64
  x86_64 x86_64 GNU/Linux

SPEC is set to: /home/SPEC
Filesystem            Type  Size  Used Avail Use% Mounted on
  /dev/mapper/rhel-root xfs   927G  140G  787G  16% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[TAE103F-1.02]- 12/05/2014
Memory:
  8x Hynix HMA42GR7MFR4N-TFT1 16 GB 2 rank 2133 MHz
  8x NO DIMM HMA42GR7MFR4N-TFT1 16 GB 2 rank 2133 MHz
  8x NO DIMM Unknown

Continued on next page
Lenovo Group Limited

Lenovo System x3500 M5
(Intel Xeon E5-2637 v3, 3.50 GHz)

<table>
<thead>
<tr>
<th>SPECfp2006</th>
<th>108</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECfp_base2006</td>
<td>104</td>
</tr>
</tbody>
</table>

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Platform Notes (Continued)

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = ":/home/SPEC/libs/32:/home/SPEC/libs/64:/home/SPEC/sh"
OMP_NUM_THREADS = "8"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
memory using RedHat EL 7.0
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
```bash
icc   -m64
```

C++ benchmarks:
```bash
icpc  -m64
```

Fortran benchmarks:
```bash
ifort -m64
```

Benchmarks using both Fortran and C:
```bash
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.game5: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zesmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64

Continued on next page
Lenovo Group Limited

Lenovo System x3500 M5
(Intel Xeon E5-2637 v3, 3.50 GHz)

SPECfp2006 = 108
SPECfp_base2006 = 104

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Test date: May-2015
Tested by: Lenovo Group Limited
Hardware Availability: Jan-2015
Software Availability: Sep-2014

Base Portability Flags (Continued)

<table>
<thead>
<tr>
<th>Base Portability Flags</th>
</tr>
</thead>
<tbody>
<tr>
<td>481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX</td>
</tr>
<tr>
<td>482.sphinx3: -DSPEC_CPU_LP64</td>
</tr>
</tbody>
</table>

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc  -m64

C++ benchmarks:
icpc  -m64

Fortran benchmarks:
ifort  -m64

Benchmarks using both Fortran and C:
icc  -m64 ifort  -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

Continued on next page
Lenovo Group Limited

Lenovo System x3500 M5
(Intel Xeon E5-2637 v3, 3.50 GHz)

SPECfp2006 = 108
SPECfp_base2006 = 104

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Test date: May-2015
Hardware Availability: Jan-2015
Software Availability: Sep-2014

Peak Optimization Flags (Continued)

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes
SPEC CFP2006 Result

Lenovo Group Limited
Lenovo System x3500 M5
(Intel Xeon E5-2637 v3, 3.50 GHz)

SPECfp2006 = 108
SPECfp_base2006 = 104

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Test date: May-2015
Hardware Availability: Jan-2015
Software Availability: Sep-2014

Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-B.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-B.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Originally published on 2 June 2015.