**Fujitsu**

PRIMERGY TX2560 M1, Intel Xeon E5-2620 v3, 2.4 GHz

**CPU2006 license**: 19  
**Test sponsor**: Fujitsu  
**Tested by**: Fujitsu  
**Hardware Availability**: Apr-2015  
**Software Availability**: Sep-2014

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### SPECfp

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**SPECfp_rate2006** = 477  
**SPECfp_rate_base2006** = 466

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### Hardware

- **CPU Name**: Intel Xeon E5-2620 v3  
- **CPU Characteristics**: Intel Turbo Boost Technology up to 3.20 GHz  
- **CPU MHz**: 2400  
- **FPU**: Integrated  
- **CPU(s) enabled**: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
- **CPU(s) orderable**: 1.2 chip  
- **Primary Cache**: 32 KB I + 32 KB D on chip per core  
- **Secondary Cache**: 256 KB I+D on chip per core

---

### Software

- **Operating System**: Red Hat Enterprise Linux Server release 7.1 (Maipo)  
- **Kernel**: 3.10.0-229.el7.x86_64  
- **Compiler**: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux; Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
- **Auto Parallel**: No  
- **File System**: xfs

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**http://www.spec.org/**
SPEC CFP2006 Result

Fujitsu

PRIMERGY TX2560 M1, Intel Xeon E5-2620 v3, 2.4 GHz

SPECfp_rate2006 = 477
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CPU2006 license: 19
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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Energy Performance = Performance
Utilization Profile = Unbalanced
Fujitsu
PRIMERGY TX2560 M1, Intel Xeon E5-2620 v3, 2.4 GHz

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Platform Notes (Continued)

QPI snoop mode: Early Snoop
COD Enable = Disabled, Early Snoop = Enabled
CPU C1E Support = Disabled

General Notes

Environment variables set by runspec before the start of the run:
LD\_LIBRARY\_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i7-4670k CPU + 16GB memory using RedHat EL 7.0
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enable
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
umactl --interleave=all runspec <etc>

This result was measured on the PRIMERGY RX2560 M1. The PRIMERGY RX2560 M1 and the PRIMERGY TX2560 M1 are electronically equivalent.
For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
icc \ -m64

C++ benchmarks:
icpc \ -m64

Fortran benchmarks:
ifort \ -m64

Benchmarks using both Fortran and C:
icc \ -m64 ifort \ -m64

Base Portability Flags

410.bwaves: \-DSPEC\_CPU\_LP64
416.gamess: \-DSPEC\_CPU\_LP64
433.milc: \-DSPEC\_CPU\_LP64
434.zeusmp: \-DSPEC\_CPU\_LP64 \-nofor\_main
435.gromacs: \-DSPEC\_CPU\_LP64 \-nofor\_main
436.cactusADM: \-DSPEC\_CPU\_LP64 \-nofor\_main
437.leslie3d: \-DSPEC\_CPU\_LP64
444.namd: \-DSPEC\_CPU\_LP64

Continued on next page
SPEC CFP2006 Result

Fujitsu

PRIMERGY TX2560 M1, Intel Xeon E5-2620 v3, 2.4 GHz

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Base Portability Flags (Continued)

447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc  -m64

C++ benchmarks (except as noted below):
icpc  -m64

450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc  -m64 ifort -m64
Fujitsu
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Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64 -nofor_main
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454.cactusADM: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
-unroll2

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
-auto-llp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias
SPEC CFP2006 Result

Fujitsu

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes
416.games: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
            -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2)
             -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
             -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes
454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml
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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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