## SPEC® CINT2006 Result

**Hewlett-Packard Company**

ProLiant DL560 Gen9 (2.10 GHz, Intel Xeon E5-4669 v3)

<table>
<thead>
<tr>
<th>SPECint®_rate2006</th>
<th>1230</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECint_rate_base2006</td>
<td>1180</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CPU2006 license</th>
<th>3</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test sponsor</td>
<td>Hewlett-Packard Company</td>
</tr>
<tr>
<td>Tested by</td>
<td>Hewlett-Packard Company</td>
</tr>
<tr>
<td>Test date</td>
<td>Apr-2015</td>
</tr>
<tr>
<td>Hardware Availability</td>
<td>Jun-2015</td>
</tr>
<tr>
<td>Software Availability</td>
<td>Oct-2014</td>
</tr>
</tbody>
</table>

### Hardware
- **CPU Name:** Intel Xeon E5-4669 v3
- **CPU Characteristics:** Intel Turbo Boost Technology up to 2.90 GHz
- **CPU MHz:** 2100
- **FPU:** Integrated
- **CPU(s) enabled:** 36 cores, 2 chips, 18 cores/chip, 2 threads/core
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 256 KB I+D on chip per core
- **L3 Cache:** 45 MB I+D on chip per chip
- **Other Cache:** None
- **Memory:** 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
- **Disk Subsystem:** 1 x 400 GB SAS SSD, RAID 0
- **Other Hardware:** None

### Software
- **Operating System:** SUSE Linux Enterprise Server 12 (x86_64)
- **Kernel:** 3.12.28-4-default
- **Compiler:** C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
- **Auto Parallel:** No
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 32-bit
- **Peak Pointers:** 32/64-bit
- **Other Software:** Microquill SmartHeap V10.0
## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Base</td>
<td></td>
<td></td>
<td>Peak</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>400.perlbench</td>
<td>72</td>
<td>734</td>
<td>958</td>
<td>737</td>
<td>954</td>
<td>738</td>
<td>953</td>
<td>72</td>
<td>592</td>
<td>1190</td>
<td>594</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>72</td>
<td>1147</td>
<td>606</td>
<td>1145</td>
<td>607</td>
<td>1145</td>
<td>607</td>
<td>72</td>
<td>1098</td>
<td>633</td>
<td>1101</td>
</tr>
<tr>
<td>403.gcc</td>
<td>72</td>
<td>675</td>
<td>859</td>
<td>681</td>
<td>852</td>
<td>673</td>
<td>861</td>
<td>72</td>
<td>673</td>
<td>861</td>
<td>674</td>
</tr>
<tr>
<td>429.mcf</td>
<td>72</td>
<td>437</td>
<td>1500</td>
<td>437</td>
<td>1500</td>
<td>437</td>
<td>1500</td>
<td>72</td>
<td>437</td>
<td>1500</td>
<td>437</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>72</td>
<td>847</td>
<td>892</td>
<td>849</td>
<td>890</td>
<td>849</td>
<td>890</td>
<td>72</td>
<td>841</td>
<td>899</td>
<td>842</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>72</td>
<td>384</td>
<td>1750</td>
<td>391</td>
<td>1720</td>
<td>384</td>
<td>1750</td>
<td>72</td>
<td>359</td>
<td>1870</td>
<td>358</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>72</td>
<td>927</td>
<td>940</td>
<td>928</td>
<td>939</td>
<td>927</td>
<td>940</td>
<td>72</td>
<td>887</td>
<td>983</td>
<td>885</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>72</td>
<td>135</td>
<td>11000</td>
<td>135</td>
<td>11000</td>
<td>135</td>
<td>11000</td>
<td>72</td>
<td>135</td>
<td>11000</td>
<td>135</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>72</td>
<td>1031</td>
<td>1550</td>
<td>1061</td>
<td>1500</td>
<td>1027</td>
<td>1550</td>
<td>72</td>
<td>1009</td>
<td>1580</td>
<td>1005</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>72</td>
<td>732</td>
<td>615</td>
<td>731</td>
<td>615</td>
<td>728</td>
<td>618</td>
<td>72</td>
<td>710</td>
<td>634</td>
<td>704</td>
</tr>
<tr>
<td>473.astar</td>
<td>72</td>
<td>764</td>
<td>661</td>
<td>767</td>
<td>659</td>
<td>764</td>
<td>662</td>
<td>72</td>
<td>764</td>
<td>661</td>
<td>767</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>72</td>
<td>498</td>
<td>997</td>
<td>500</td>
<td>993</td>
<td>500</td>
<td>993</td>
<td>72</td>
<td>498</td>
<td>997</td>
<td>500</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runcase command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

## Platform Notes

BIOS Configuration:
HP Power Profile set to Custom
HP Power Regulator to HP Static High Performance Mode
Minimum Processor Idle Power Core State set to C6 State
Minimum Processor Idle Power Package State set to No Package State
Energy/Performance Bias set to Maximum Performance
Collaborative Power Control set to Disabled
Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh
Sysinfo program /home/cpu2006/config/sysinfo.rev6914

Continued on next page
Hewlett-Packard Company

ProLiant DL560 Gen9
(2.10 GHz, Intel Xeon E5-4669 v3)

SPECint_rate2006 = 1230
SPECint_rate_base2006 = 1180

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Software Availability: Oct-2014
Hardware Availability: Jun-2015
Test date: Apr-2015

Platform Notes (Continued)

$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667bSa285932ceab81e28219e1
running on d1560gen9sles12cpu Thu Apr 23 14:46:27 2015

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-4669 v3 @ 2.10GHz
  2 "physical id"s (chips)
  72 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
cautions.)
    cpu cores : 18
    siblings : 36
    physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
    physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
    cache size : 46080 KB

From /proc/meminfo
  MemTotal:       264544024 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
  SuSE-release:
     SUSE Linux Enterprise Server 12 (x86_64)
     VERSION = 12
     PATCHLEVEL = 0
     # This file is deprecated and will be removed in a future service pack or
     # release.
     # Please check /etc/os-release for details about this release.
  os-release:
     NAME="SLES"
     VERSION="12"
     VERSION_ID="12"
     PRETTY_NAME="SUSE Linux Enterprise Server 12"
     ID="sles"
     ANSI_COLOR="0;32"
     CPE_NAME="cpe:/o:suse:sles:12"

uname -a:
  Linux d1560gen9sles12cpu 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC
  2014 (9879bd4) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Apr 23 14:04

SPEC is set to: /home/cpu2006

     Filesystem  Type  Size  Used Avail Use% Mounted on
/dev/sda4  xfs  331G  6.0G  325G   2%  /home

Additional information from dmidecode:
Continued on next page
SPEC CINT2006 Result

Hewlett-Packard Company
ProLiant DL560 Gen9
(2.10 GHz, Intel Xeon E5-4669 v3)

SPECint_rate2006 = 1230
SPECint_rate_base2006 = 1180

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company
Test date: Apr-2015
Hardware Availability: Jun-2015
Software Availability: Oct-2014

Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P85 03/05/2015
Memory:
16x HP 752369-081 16 GB 2 rank 2133 MHz
32x UNKNOWN NOT AVAILABLE

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have one line reading as:
16x HP 752369-081 16 GB 2 rank 2133 MHz

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

C++ benchmarks:
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

Continued on next page
**SPEC CINT2006 Result**

Hewlett-Packard Company

ProLiant DL560 Gen9

(2.10 GHz, Intel Xeon E5-4669 v3)

<table>
<thead>
<tr>
<th>SPECint_rate2006 =</th>
<th>1230</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECint_rate_base2006 =</td>
<td>1180</td>
</tr>
</tbody>
</table>

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

**Base Optimization Flags (Continued)**

C++ benchmarks:
- `-xCORE-AVX2`
- `-ipo`
- `-O3`
- `-no-prec-div`
- `-opt-prefetch`
- `-opt-mem-layout-trans=3`
- `-Wl,-z,muldefs`
- `-L/sh`
- `-lsmartheap`

**Base Other Flags**

C benchmarks:

403.gcc: `-Dalloca=_alloca`

**Peak Compiler Invocation**

C benchmarks (except as noted below):

- `icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`
- `400.perlbench: icc -m64`
- `401.bzip2: icc -m64`
- `456.hmmer: icc -m64`
- `458.sjeng: icc -m64`

C++ benchmarks:

- `icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

**Peak Portability Flags**

- `400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
- `401.bzip2: -DSPEC_CPU_LP64`
- `456.hmmer: -DSPEC_CPU_LP64`
- `458.sjeng: -DSPEC_CPU_LP64`
- `462.libquantum: -DSPEC_CPU_LINUX`
- `483.xalancbmk: -DSPEC_CPU_LINUX`

**Peak Optimization Flags**

C benchmarks:

- `400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
- `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32`

Continued on next page
Peak Optimization Flags (Continued)

401.bzip2: -xcORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xcORE-AVX2 -ipo -03 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xcORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xcORE-AVX2 -ipo -03 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xcORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xcORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xcORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-za-region-strategy=block -Wl,-z,muldefs
-L/sh -lsamartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml
## SPEC CINT2006 Result

**Hewlett-Packard Company**

ProLiant DL560 Gen9  
(2.10 GHz, Intel Xeon E5-4669 v3)

<table>
<thead>
<tr>
<th>SPECint_rate2006 = 1230</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECint_rate_base2006 = 1180</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CPU2006 license: 3</th>
<th>Test date: Apr-2015</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test sponsor: Hewlett-Packard Company</td>
<td>Hardware Availability: Jun-2015</td>
</tr>
<tr>
<td>Tested by: Hewlett-Packard Company</td>
<td>Software Availability: Oct-2014</td>
</tr>
</tbody>
</table>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.  
Originally published on 2 June 2015.