Hewlett-Packard Company

ProLiant DL560 Gen9
(2.10 GHz, Intel Xeon E5-4660 v3)

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

SPECfp®2006 = 102
SPECfp_base2006 = 96.2

Hardware

CPU Name: Intel Xeon E5-4660 v3
CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
CPU MHz: 2100
FPU: Integrated
CPU(s) enabled: 56 cores, 4 chips, 14 cores/chip
CPU(s) orderable: 2, 4 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Operating System: SuSE Linux Enterprise Server 12 (x86_64)
Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
          Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)

Software

Test date: May-2015
Hardware Availability: Jun-2015
Software Availability: Oct-2014

Continued on next page

Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
Hewlett-Packard Company
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>15.8</td>
<td>859</td>
<td>16.0</td>
<td>849</td>
<td>15.9</td>
<td>853</td>
<td>15.8</td>
<td>859</td>
<td>16.0</td>
<td>849</td>
<td>15.9</td>
<td>853</td>
</tr>
<tr>
<td>416.gamess</td>
<td>645</td>
<td>30.3</td>
<td>643</td>
<td>30.5</td>
<td>645</td>
<td>30.4</td>
<td>529</td>
<td>37.0</td>
<td>529</td>
<td>37.0</td>
<td>530</td>
<td>36.9</td>
</tr>
<tr>
<td>433.milc</td>
<td>149</td>
<td>61.7</td>
<td>151</td>
<td>60.9</td>
<td>149</td>
<td>61.7</td>
<td>144</td>
<td>63.7</td>
<td>144</td>
<td>63.5</td>
<td>149</td>
<td>61.5</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>59.9</td>
<td>152</td>
<td>59.1</td>
<td>154</td>
<td>59.2</td>
<td>154</td>
<td>59.9</td>
<td>152</td>
<td>59.1</td>
<td>154</td>
<td>59.2</td>
<td>154</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>217</td>
<td>32.9</td>
<td>216</td>
<td>33.1</td>
<td>216</td>
<td>33.1</td>
<td>217</td>
<td>32.9</td>
<td>216</td>
<td>33.1</td>
<td>216</td>
<td>33.1</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>17.3</td>
<td>690</td>
<td>18.5</td>
<td>645</td>
<td>17.2</td>
<td>695</td>
<td>17.3</td>
<td>690</td>
<td>18.5</td>
<td>645</td>
<td>17.2</td>
<td>695</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>40.7</td>
<td>231</td>
<td>39.7</td>
<td>237</td>
<td>41.8</td>
<td>225</td>
<td>40.7</td>
<td>231</td>
<td>39.7</td>
<td>237</td>
<td>41.8</td>
<td>225</td>
</tr>
<tr>
<td>444.namd</td>
<td>327</td>
<td>24.5</td>
<td>327</td>
<td>24.5</td>
<td>327</td>
<td>24.5</td>
<td>318</td>
<td>25.2</td>
<td>319</td>
<td>25.1</td>
<td>318</td>
<td>25.2</td>
</tr>
<tr>
<td>447.dealII</td>
<td>242</td>
<td>47.3</td>
<td>243</td>
<td>47.1</td>
<td>247</td>
<td>46.3</td>
<td>242</td>
<td>47.3</td>
<td>243</td>
<td>47.1</td>
<td>247</td>
<td>46.3</td>
</tr>
<tr>
<td>450.soplex</td>
<td>208</td>
<td>40.0</td>
<td>210</td>
<td>39.6</td>
<td>205</td>
<td>40.6</td>
<td>208</td>
<td>40.0</td>
<td>210</td>
<td>39.6</td>
<td>205</td>
<td>40.6</td>
</tr>
<tr>
<td>453.povray</td>
<td>108</td>
<td>49.4</td>
<td>107</td>
<td>49.5</td>
<td>108</td>
<td>49.4</td>
<td>95.5</td>
<td>56.0</td>
<td>95.9</td>
<td>55.5</td>
<td>96.2</td>
<td>55.3</td>
</tr>
<tr>
<td>454.calcuix</td>
<td>192</td>
<td>43.0</td>
<td>191</td>
<td>43.1</td>
<td>191</td>
<td>43.1</td>
<td>172</td>
<td>48.1</td>
<td>170</td>
<td>48.5</td>
<td>166</td>
<td>49.6</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>52.7</td>
<td>201</td>
<td>60.5</td>
<td>175</td>
<td>53.0</td>
<td>200</td>
<td>45.4</td>
<td>234</td>
<td>42.7</td>
<td>248</td>
<td>43.8</td>
<td>242</td>
</tr>
<tr>
<td>465.tonto</td>
<td>309</td>
<td>31.8</td>
<td>314</td>
<td>31.4</td>
<td>319</td>
<td>30.9</td>
<td>220</td>
<td>44.7</td>
<td>219</td>
<td>45.0</td>
<td>221</td>
<td>44.6</td>
</tr>
<tr>
<td>470.lbm</td>
<td>11.6</td>
<td>1180</td>
<td>12.5</td>
<td>1100</td>
<td>12.3</td>
<td>1120</td>
<td>11.6</td>
<td>1180</td>
<td>12.5</td>
<td>1100</td>
<td>12.3</td>
<td>1120</td>
</tr>
<tr>
<td>481.wrf</td>
<td>115</td>
<td>97.0</td>
<td>116</td>
<td>96.0</td>
<td>115</td>
<td>96.8</td>
<td>115</td>
<td>97.0</td>
<td>116</td>
<td>96.0</td>
<td>115</td>
<td>96.8</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>325</td>
<td>59.9</td>
<td>326</td>
<td>59.7</td>
<td>328</td>
<td>59.5</td>
<td>325</td>
<td>59.9</td>
<td>326</td>
<td>59.7</td>
<td>328</td>
<td>59.5</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

BIOS Configuration:
Intel Hyperthreading set to Disabled
HP Power Profile set to Custom
HP Power Regulator to HP Static High Performance Mode
Minimum Processor Idle Power Core State set to C6 State
Energy/Performance Bias set to Maximum Performance
Collaborative Power Control set to Disabled
Thermal Configuration set to Maximum Cooling

Continued on next page
SPEC CFP2006 Result

Hewlett-Packard Company

ProLiant DL560 Gen9
(2.10 GHz, Intel Xeon E5-4660 v3)

SPECfp2006 = 102
SPECfp_base2006 = 96.2

CPU2006 license: 3
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Tested by: Hewlett-Packard Company

Test date: May-2015
Hardware Availability: Jun-2015
Software Availability: Oct-2014

Platform Notes (Continued)

Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh
Sysinfo program /home/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on dl560gen9jks Wed May 20 12:31:48 2015

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-4660 v3 @ 2.10GHz
4 "physical id"s (chips)
56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 14
siblings : 14
physical 0: cores 0 2 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 2 4 5 6 8 9 10 11 12 13 14
physical 2: cores 0 2 4 5 6 8 9 10 11 12 13 14
physical 3: cores 0 2 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB

From /proc/meminfo
MemTotal: 529311072 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.

os-release:
NAME="SLES"
VERSION="12"
VERSION_ID="12"
PRETTY_NAME="SUSE Linux Enterprise Server 12"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12"

uname -a:
Linux dl560gen9jks 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014

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Hewlett-Packard Company
ProLiant DL560 Gen9
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<table>
<thead>
<tr>
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Platform Notes (Continued)

(9879bd4) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 19 13:18

SPEC is set to: /home/cpu2006
Filesystem   Type      Size  Used  Avail  Use%  Mounted on
/dev/sda4   xfs  331G   6.1G  325G   2%  /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMIOS" standard.

BIOS HP P85 03/05/2015
Memory:
24x HP 752369-081 16 GB 2 rank 2133 MHz
16x UNKNOWN NOT AVAILABLE
8x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have two lines reading as:
24x HP 752369-081 16 GB 2 rank 2133 MHz
8x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2133 MHz

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "56"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Base Compiler Invocation

C benchmarks:
  icc  -m64

C++ benchmarks:
  icpc -m64

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  icc  -m64 ifort -m64
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Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

```
icc  -m64 ifort -m64```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32 -ansi-alias
```

```
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32
```

```
447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-
```

```
434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
```

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Peak Optimization Flags (Continued)

459.GemsFDXTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: -xCORE-AVX2 -ipo -03 -no-prec-div -auto-ilp32 -ansi-alias
481.wrf: basepeak = yes

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

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For other inquiries, please contact webmaster@spec.org.

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