SPEC® CFP2006 Result

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Huawei

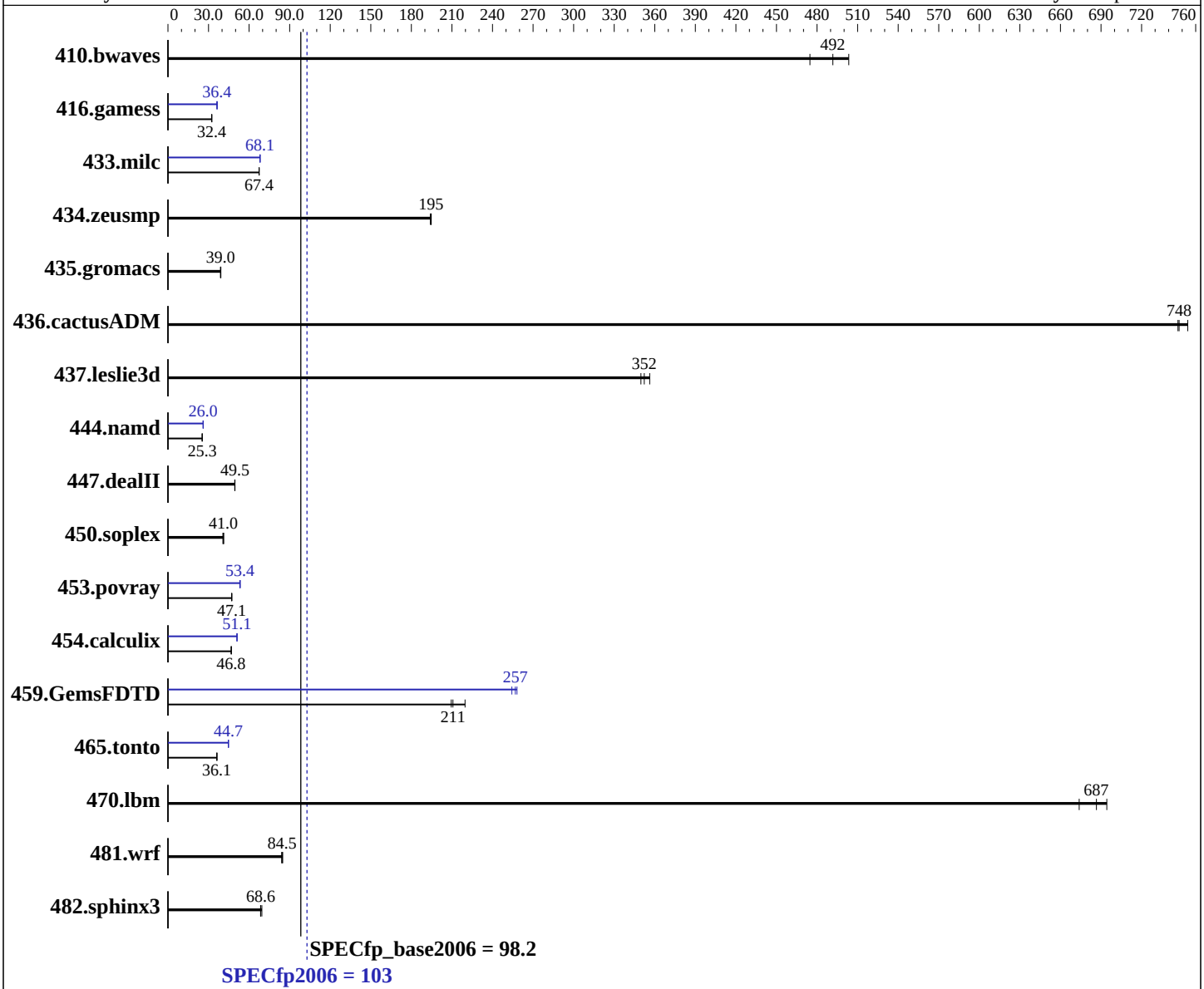
Huawei XH620 V3 (Intel Xeon E5-2650 v3)

SPECfp®2006 = 103

SPECfp_base2006 = 98.2

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: May-2015
Hardware Availability: Sep-2014
Software Availability: Sep-2014



Hardware

CPU Name:	Intel Xeon E5-2650 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.00 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	20 cores, 2 chips, 10 cores/chip
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64
Compiler:	C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux; Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem:	1 x 500 GB SATA, 7200 RPM
Other Hardware:	None

System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	28.6	475	**27.6**	**492**	27.0	503	28.6	475	**27.6**	**492**	27.0	503
416.gamess	604	32.4	**604**	**32.4**	607	32.2	538	36.4	**538**	**36.4**	542	36.1
433.milc	**136**	**67.4**	136	67.5	136	67.3	135	68.0	135	68.2	**135**	**68.1**
434.zeusmp	**46.8**	**195**	46.9	194	46.7	195	**46.8**	**195**	46.9	194	46.7	195
435.gromacs	184	38.8	**183**	**39.0**	182	39.1	184	38.8	**183**	**39.0**	182	39.1
436.cactusADM	15.8	754	16.0	747	**16.0**	**748**	15.8	754	16.0	747	**16.0**	**748**
437.leslie3d	**26.7**	**352**	26.4	356	26.9	350	**26.7**	**352**	26.4	356	26.9	350
444.namd	316	25.4	**317**	**25.3**	317	25.3	**308**	**26.0**	308	26.0	308	26.0
447.dealII	**231**	**49.5**	231	49.5	232	49.4	**231**	**49.5**	231	49.5	232	49.4
450.soplex	205	40.6	202	41.2	**203**	**41.0**	205	40.6	202	41.2	**203**	**41.0**
453.povray	**113**	**47.1**	112	47.3	113	47.0	100	53.1	99.6	53.4	**99.7**	**53.4**
454.calculix	176	46.7	176	46.9	**176**	**46.8**	162	51.0	161	51.1	**162**	**51.1**
459.GemsFDTD	50.6	209	**50.3**	**211**	48.3	220	**41.3**	**257**	41.1	258	41.7	254
465.tonto	271	36.3	**272**	**36.1**	272	36.1	220	44.8	220	44.7	**220**	**44.7**
470.lbm	**20.0**	**687**	20.4	674	19.8	694	**20.0**	**687**	20.4	674	19.8	694
481.wrf	132	84.9	133	84.1	**132**	**84.5**	132	84.9	133	84.1	**132**	**84.5**
482.sphinx3	**284**	**68.6**	285	68.5	281	69.4	**284**	**68.6**	285	68.5	281	69.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
Set Power Efficiency Mode to Custom
Set Snoop Mode to HS mode
Set Patrol Scrub to Disable
Set Intel HT Technology to Disable
Sysinfo program /spec/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Fri May  8 12:51:34 2015

This section contains SUT (System Under Test) info as seen by
```

Continued on next page

Platform Notes (Continued)

```
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2650 v3 @ 2.30GHz
      2 "physical id"s (chips)
      20 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 10
      siblings  : 10
      physical 0: cores 0 1 2 3 4 8 9 10 11 12
      physical 1: cores 0 1 2 3 4 8 9 10 11 12
   cache size : 25600 KB

From /proc/meminfo
   MemTotal:       263578912 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.0 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.0"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
   Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
   EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 8 12:50

SPEC is set to: /spec
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda1      ext4  385G   22G  343G   6% /
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS Insyde Corp. 1.36 04/09/2015
```

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Platform Notes (Continued)

```
      Memory:
       8x Micron 36ASF2G72PZ-2G1A2 16 GB 1 rank 2133 MHz
       8x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz

    (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"
OMP_NUM_THREADS = "20"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html`
`http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.xml`

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For other inquiries, please contact webmaster@spec.org.

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