Cisco Systems
Cisco UCS B420 M4 (Intel Xeon E5-4667 v3, 2.00 GHz)

CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

SPECint\_rate2006 = 2280
SPECint\_rate\_base2006 = 2200

| Test date: | Jul-2015 |
| Hardware Availability: | Software Availability: |
| Test date: | Jul-2015 |
| Jun-2015 | Nov-2014 |

Software

Operating System: SUSE Linux Enterprise Server 12 (x86-64) 3.12.28-4-default
Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0

Hardware

CPU Name: Intel Xeon E5-4667 v3
CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
CPU MHZ: 2000
FPU: Integrated
CPU(s) enabled: 64 cores, 4 chips, 16 cores/chip, 2 threads/core
CPU(s) orderable: 2,4 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 40 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem: 1 x 300 GB SAS, 15K RPM
Other Hardware: None

Graph showing SPECint\_rate2006 = 2280 and SPECint\_rate\_base2006 = 2200
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>:-----------------:</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>128</td>
<td>765</td>
<td>1630</td>
<td>768</td>
<td>1630</td>
<td>766</td>
<td>1630</td>
<td></td>
<td>128</td>
<td>606</td>
<td>2060</td>
<td>607</td>
<td>2060</td>
<td>607</td>
<td>2060</td>
<td></td>
<td></td>
</tr>
<tr>
<td>401.bzip2</td>
<td>128</td>
<td>1123</td>
<td>1100</td>
<td>1121</td>
<td>1100</td>
<td>1122</td>
<td>1100</td>
<td></td>
<td>128</td>
<td>1072</td>
<td>1150</td>
<td>1075</td>
<td>1150</td>
<td>1075</td>
<td>1150</td>
<td></td>
<td></td>
</tr>
<tr>
<td>403.gcc</td>
<td>128</td>
<td>609</td>
<td>1690</td>
<td>613</td>
<td>1680</td>
<td>611</td>
<td>1690</td>
<td></td>
<td>128</td>
<td>611</td>
<td>1690</td>
<td>609</td>
<td>1690</td>
<td>612</td>
<td>1680</td>
<td></td>
<td></td>
</tr>
<tr>
<td>429.mcf</td>
<td>128</td>
<td>394</td>
<td>2960</td>
<td>393</td>
<td>2970</td>
<td>393</td>
<td>2970</td>
<td></td>
<td>128</td>
<td>394</td>
<td>2960</td>
<td>393</td>
<td>2970</td>
<td>393</td>
<td>2970</td>
<td></td>
<td></td>
</tr>
<tr>
<td>445.gobmk</td>
<td>128</td>
<td>879</td>
<td>1530</td>
<td>880</td>
<td>1530</td>
<td>881</td>
<td>1520</td>
<td></td>
<td>128</td>
<td>872</td>
<td>1540</td>
<td>874</td>
<td>1540</td>
<td>873</td>
<td>1540</td>
<td></td>
<td></td>
</tr>
<tr>
<td>456.hmmer</td>
<td>128</td>
<td>382</td>
<td>3130</td>
<td>381</td>
<td>3130</td>
<td>382</td>
<td>3130</td>
<td></td>
<td>128</td>
<td>353</td>
<td>3380</td>
<td>352</td>
<td>3390</td>
<td>352</td>
<td>3390</td>
<td></td>
<td></td>
</tr>
<tr>
<td>458.sjeng</td>
<td>128</td>
<td>956</td>
<td>1620</td>
<td>955</td>
<td>1620</td>
<td>955</td>
<td>1620</td>
<td></td>
<td>128</td>
<td>916</td>
<td>1690</td>
<td>917</td>
<td>1690</td>
<td>917</td>
<td>1690</td>
<td></td>
<td></td>
</tr>
<tr>
<td>462.libquantum</td>
<td>128</td>
<td>126</td>
<td>21100</td>
<td>126</td>
<td>21100</td>
<td>126</td>
<td>21100</td>
<td></td>
<td>128</td>
<td>126</td>
<td>21100</td>
<td>126</td>
<td>21100</td>
<td>126</td>
<td>21100</td>
<td></td>
<td></td>
</tr>
<tr>
<td>464.h264ref</td>
<td>128</td>
<td>1075</td>
<td>2630</td>
<td>1053</td>
<td>2690</td>
<td>1060</td>
<td>2670</td>
<td></td>
<td>128</td>
<td>1037</td>
<td>2730</td>
<td>1051</td>
<td>2700</td>
<td>1061</td>
<td>2670</td>
<td></td>
<td></td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>128</td>
<td>669</td>
<td>1200</td>
<td>665</td>
<td>1200</td>
<td>664</td>
<td>1210</td>
<td></td>
<td>128</td>
<td>642</td>
<td>1250</td>
<td>642</td>
<td>1250</td>
<td>640</td>
<td>1250</td>
<td></td>
<td></td>
</tr>
<tr>
<td>473.astar</td>
<td>128</td>
<td>749</td>
<td>1200</td>
<td>750</td>
<td>1200</td>
<td>747</td>
<td>1200</td>
<td></td>
<td>128</td>
<td>749</td>
<td>1200</td>
<td>750</td>
<td>1200</td>
<td>747</td>
<td>1200</td>
<td></td>
<td></td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>128</td>
<td>389</td>
<td>2270</td>
<td>389</td>
<td>2270</td>
<td>387</td>
<td>2280</td>
<td></td>
<td>128</td>
<td>389</td>
<td>2270</td>
<td>389</td>
<td>2270</td>
<td>387</td>
<td>2280</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option "submit" was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Configuration:
CPU performance set to Enterprise
Power Technology set to Energy-Efficient
Energy Performance Bias setting set to Balanced Performance
Memory RAS configuration set to Maximum Performance
LV DDR Mode set to Performance-mode
Sysinfo program /opt/cpu2006-1.2/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-6160 Thu Jul 9 13:52:57 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-4667 v3 @ 2.00GHz
4 "physical id"s (chips)

Continued on next page
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CPU2006 license: 9019
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SPECint_rate2006 = 2280
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Platform Notes (Continued)

128 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 16
  siblings  : 32
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  cache size : 40960 KB

From /proc/meminfo
  MemTotal:      529325812 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
  SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 0
    # This file is deprecated and will be removed in a future service pack or release.
    # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12"
    VERSION_ID="12"
    PRETTY_NAME="SUSE Linux Enterprise Server 12"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12"

uname -a:
  Linux linux-616o 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
  (9879bd4) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 9 13:51

SPEC is set to: /opt/cpu2006-1.2

Filesystem  Type  Size  Used  Avail  Use%  Mounted on
/dev/sdc2    xfs  250G  11G  240G   5%  /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. B420M4.2.2.5.0.043020152304 04/30/2015
Memory:

Continued on next page
SPEC CINT2006 Result
Cisco Systems
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CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

Test date: Jul-2015
Hardware Availability: Jun-2015
Software Availability: Nov-2014

Platform Notes (Continued)
32x 0xCE00 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
16x NO DIMM NO DIMM

(End of data from sysinfo program)

General Notes
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/opt/cpu2006-1.2/libs/32:/opt/cpu2006-1.2/libs/64:/opt/cpu2006-1.2/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation
C benchmarks:
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
C++ benchmarks:
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Base Portability Flags
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags
C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
SPEC CINT2006 Result

Cisco Systems
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SPECint_rate2006 = 2280
SPECint_rate_base2006 = 2200

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
```plaintext
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```
400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
```plaintext
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:
```plaintext
400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32
```
```plaintext
401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
```
```plaintext
403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div
```
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Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2 (pass 2) -prof-gen (pass 1) -prof-use (pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-O3 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-O3 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-O3 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revC.20150729.xml
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<table>
<thead>
<tr>
<th>SPECint_rate2006</th>
<th>2280</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECint_rate_base2006</td>
<td>2200</td>
</tr>
</tbody>
</table>

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