Fujitsu
PRIMERGY RX4770 M2, Intel Xeon E7-8880 v3, 2.30 GHz

SPECint_rate2006 = 2650
SPECint_rate_base2006 = 2580

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Apr-2015
Hardware Availability: May-2015

Tested by: Fujitsu
Software Availability: Oct-2014

400.perlbench
401.bzip2
403.gcc
429.mcf
445.gobmk
456.hmmer
458.sjeng
462.libquantum
464.h264ref
471.omnetpp
473.astar
483.xalancbmk

CINT2006 Result
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Specifications:
- CPU Name: Intel Xeon E7-8880 v3
- CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
- CPU MHz: 2300
- FPU: Integrated
- CPU(s) enabled: 72 cores, 4 chips, 18 cores/chip, 2 threads/core
- CPU(s) orderable: 2,4 chips
- Primary Cache: 32 KB I + 32 KB D on chip per core
- Secondary Cache: 256 KB I+D on chip per core
- L3 Cache: 45 MB I+D on chip per chip
- Other Cache: None
- Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
- Disk Subsystem: 1 x SATA, 600 GB, 10000 RPM
- Other Hardware: None

Operating System: Red Hat Enterprise Linux Server release 6.6 (Santiago)
2.6.32-504.el6.x86_64
Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0
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Results Table

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<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
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<tr>
<td>400.perlbench</td>
<td>144</td>
<td>2120</td>
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<td></td>
</tr>
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<td>1290</td>
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<td></td>
</tr>
<tr>
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<td>1900</td>
<td>610</td>
<td>1900</td>
</tr>
<tr>
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<td>144</td>
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<td>811</td>
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<td>1860</td>
</tr>
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<td>359</td>
<td>3740</td>
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<td>3720</td>
</tr>
<tr>
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<td>865</td>
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<td>2010</td>
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</tr>
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<td>25000</td>
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</tr>
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<td>3130</td>
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<td>3120</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>144</td>
<td>737</td>
<td>1220</td>
<td>731</td>
<td>1230</td>
<td>735</td>
<td>1220</td>
</tr>
<tr>
<td>473.astar</td>
<td>144</td>
<td>726</td>
<td>1390</td>
<td>726</td>
<td>1390</td>
<td>721</td>
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</tr>
<tr>
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<td>144</td>
<td>365</td>
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<td>366</td>
<td>2720</td>
<td>365</td>
<td>2720</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes
BIOS configuration:
Energy Performance = Performance

General Notes
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Continued on next page
General Notes (Continued)

For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
icc  -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2  -ipo -O3  -no-prec-div  -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2  -ipo -O3  -no-prec-div  -opt-prefetch
-opt-mem-layout-trans=3  -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc  -m32

400.perlbench: icc  -m64
401.bzip2: icc  -m64

Continued on next page
Peak Compiler Invocation (Continued)

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes
429.mcf: basepeak = yes
445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)

C++ benchmarks:

Continued on next page
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Peak Optimization Flags (Continued)
471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-o3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes
483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:
403gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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