Huawei RH5885 V3 (Intel Xeon E7-8860 v3)

**SPECint®_rate2006 = 2120**

**SPECint_rate_base2006 = 2050**

<table>
<thead>
<tr>
<th>Spec Benchmark</th>
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<tr>
<td>400.perlbench</td>
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<tr>
<td>401.bzip2</td>
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<td>403.gcc</td>
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<tr>
<td>429.mcf</td>
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<tr>
<td>445.gobmk</td>
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<tr>
<td>456.hmmer</td>
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<td>128</td>
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<tr>
<td>458.sjeng</td>
<td>128</td>
<td>128</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>128</td>
<td>128</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>128</td>
<td>128</td>
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<tr>
<td>471.omnetpp</td>
<td>128</td>
<td>128</td>
</tr>
<tr>
<td>473.astar</td>
<td>128</td>
<td>128</td>
</tr>
<tr>
<td>483.xalancbmk</td>
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</table>

**CPU2006 license:** 3175  
**Test sponsor:** Huawei  
**Test date:** Oct-2015  
**Hardware Availability:** May-2015  
**Tested by:** Huawei  
**Software Availability:** Oct-2014

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**Hardware**

- **CPU Name:** Intel Xeon E7-8860 v3  
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.20 GHz  
- **CPU MHz:** 2200  
- **FPU:** Integrated  
- **CPU(s) enabled:** 64 cores, 4 chips, 16 cores/chip, 2 threads/core  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 256 KB I+D on chip per core  
- **L3 Cache:** 40 MB I+D on chip per chip  
- **Memory:** 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)  
- **Disk Subsystem:** 2 x 300 GB SAS, 10K RPM  
- **Other Cache:** None

---

**Software**

- **Operating System:** SUSE Linux Enterprise Server 12 (x86_64) 3.12.28-4-default  
- **Compiler:** C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
- **Auto Parallel:** No  
- **File System:** ext4  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 32-bit  
- **Peak Pointers:** 32/64-bit  
- **Other Software:** Microquill SmartHeap V10.0
Huawei RH5885 V3 (Intel Xeon E7-8860 v3) SPECint_rate2006 = 2120
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CPU2006 license: 3175
Test sponsor: Huawei
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Results Table

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<thead>
<tr>
<th>Benchmark</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<tr>
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<td>471.omnetpp</td>
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<td>473.astar</td>
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<tr>
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<td>1840</td>
<td>479</td>
<td>1840</td>
<td>479</td>
<td>1840</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Lock_step to disabled
Baseboard Management Controller used to adjust the fan speed to 100%
Set Memory Power Saving to disabled
Sysinfo program /zsn/spec1/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-f8l8 Sat Oct 10 14:09:28 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8860 v3 @ 2.20GHz
4 "physical id"s (chips)
128 "processors"
Huawei RH5885 V3 (Intel Xeon E7-8860 v3)

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Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
- cpu cores : 16
- siblings : 32
- physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
- physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
- physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
- physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
- cache size : 40960 KB

From /proc/meminfo
- MemTotal: 529108808 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
- SuSE-release:
  - NAME="SLES"
  - VERSION="12"
  - VERSION_ID="12"
  - PRETTY_NAME="SUSE Linux Enterprise Server 12"
  - ID="sles"
  - ANSI_COLOR="0;32"
  - CPE_NAME="cpe:/o:suse:sles:12"

uname -a:
- Linux linux-f818 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
  (9879bd4) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 10 13:31

SPECIAL is set to: /zsn/spec1

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. BLISQ954 09/19/2015
Memory:
- 32x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz, configured at 1600 MHz

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### Platform Notes (Continued)

16x NO DIMM NO DIMM

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have two lines reading as:

32x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz, configured at 1600 MHz
16x NO DIMM NO DIMM

### General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "~/zsn/spec1/libs/32:/zsn/spec1/libs/64:/zsn/spec1/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1 > /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

### Base Compiler Invocation

C benchmarks:

```bash
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

C++ benchmarks:

```bash
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

### Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

### Base Optimization Flags

C benchmarks:

```bash
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
```

C++ benchmarks:

```bash
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
```
Huawei

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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

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Peak Optimization Flags (Continued)

429.mcf: basepeak = yes
445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
  -ansi-alias -opt-mem-layout-trans=3
456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
  -unroll4 -auto-ilp32
462.libquantum: basepeak = yes
464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
  -unroll2 -ansi-alias

C++ benchmarks:
471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
  -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
  -L/sh -lsmartheap
473.astar: basepeak = yes
483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-HSW-RevG.xml
Huawei

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For other inquiries, please contact webmaster@spec.org.

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