Dell Inc. PowerEdge T330 (Intel Xeon E3-1280 v5, 3.70 GHz)

```
SPECint®2006 = 75.2
SPECint_base2006 = 72.6
```

- CPU2006 license: 55
- Test sponsor: Dell Inc.
- Tested by: Dell Inc.
- Test date: Sep-2015
- Hardware Availability: Nov-2015
- Software Availability: Sep-2015

**Hardware**

- CPU Name: Intel Xeon E3-1280 v5
- CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz
- CPU MHZ: 3700
- FPU: Integrated
- CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
- CPU(s) orderable: 4 cores, 1 chip
- Primary Cache: 32 KB I + 32 KB D on chip per core
- Secondary Cache: 256 KB I+D on chip per core
- L3 Cache: 8 MB I+D on chip per core
- Other Cache: None
- Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2133P-U)
- Disk Subsystem: 1 x 500 GB 7200 RPM SATA
- Other Hardware: None

**Software**

- Operating System: SUSE Linux Enterprise Server 12 3.12.28-4-default
- Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
- Auto Parallel: Yes
- File System: ext4
- System State: Run level 3 multi-user
- Base Pointers: 32/64-bit
- Peak Pointers: 32/64-bit
- Other Software: Microquill SmartHeap V10.2
SPEC CINT2006 Result

Dell Inc.

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Results Table

<table>
<thead>
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<th>Benchmark</th>
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<th>Ratio</th>
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<td>51.4</td>
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<td>56.7</td>
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<td>31.2</td>
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<td>40.6</td>
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<tr>
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<tr>
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<td>94.7</td>
<td>68.0</td>
<td>101</td>
<td>67.7</td>
<td>102</td>
</tr>
</tbody>
</table>

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Virtualization Technology disabled
System Profile set to Performance
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-16fb Wed Sep 16 11:03:44 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1280 v5 @ 3.70GHz
  1 "physical id"s (chips)
  8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
Dell Inc.

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Platform Notes (Continued)

siblings : 8
physical 0: cores 0 1 2 3
cache size : 8192 KB

From /proc/meminfo
MemTotal: 66066084 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12

From /etc/*release*/etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
oS-release:
NAME="SLES"
VERSION="12"
VERSION_ID="12"
PRETTY_NAME="SUSE Linux Enterprise Server 12"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12"

uname -a:
Linux linux-l6fb 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 16 11:02

SPEC is set to: /root/cpu2006-1.2

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 ext4 451G 8.4G 442G 2% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 0.3.16 09/09/2015
Memory:
1x 00AD00000000 HMA82GU7MFR8N-TF 16 GB 2 rank 2133 MHz
2x 00AD00000020B HMA82GU7MFR8N-TF 16 GB 2 rank 2133 MHz
1x 00AD000000800 HMA82GU7MFR8N-TF 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)
Dell Inc.                      SPECint2006 = 75.2
PowerEdge T330 (Intel Xeon E3-1280 v5, 3.70 GHz) SPECint_base2006 = 72.6

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General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"
OMP_NUM_THREADS = "4"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc  -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64
Dell Inc.

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Software Availability: Sep-2015

**Base Other Flags**

C benchmarks:

403.gcc: -Dalloca=_alloca

**Peak Compiler Invocation**

C benchmarks (except as noted below):

```shell
icc -m64
```

400.perlbench: `icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

C++ benchmarks (except as noted below):

```shell
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

473.astar: `icpc -m64`

**Peak Portability Flags**

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

445.gobmk: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

464.h264ref: `-DSPEC_CPU_LP64`

471.omnetpp: `-D_FILE_OFFSET_BITS=64`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

**Peak Optimization Flags**

C benchmarks:

```shell
400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafepass (pass 1)
        -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2)
        -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
        -ansi-alias
```

```shell
401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafepass (pass 1)
        -ipo(pass 2) -03(pass 2) -no-prec-div
        -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32
        -opt-prefetch -ansi-alias
```

Continued on next page
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Peak Optimization Flags (Continued)

403gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
           -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes
445.gobmk: basepeak = yes
456.hmmer: basepeak = yes
458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes
464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -prof-use(pass 2)
             -opt-ra-region-strategy=block -ansi-alias
             -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
            -auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
                  -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

C benchmarks:

403gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.xml
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For other inquiries, please contact webmaster@spec.org.

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