Dell Inc.

PowerEdge T130 (Intel Xeon E3-1240 v5, 3.50 GHz)

| Test date: | Sep-2015 |
| Test sponsor: | Dell Inc. |
| Tested by: | Dell Inc. |

| SPECint_rate2006 | 255 |
| SPECint_rate_base2006 | 246 |

| SPECint_rate2006 | 255 |
| SPECint_rate_base2006 | 246 |

### Hardware

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name:</td>
<td>Intel Xeon E3-1240 v5</td>
</tr>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 3.90 GHz</td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>3500</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>4 cores, 1 chip, 4 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>1 chip</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>256 KB I+D on chip per core</td>
</tr>
<tr>
<td>L3 Cache:</td>
<td>8 MB I+D on chip per core</td>
</tr>
<tr>
<td>Other Cache:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>64 GB (4 x 16 GB 2Rx8 PC4-2133P-U)</td>
</tr>
<tr>
<td>Disk Subsystem:</td>
<td>1 x 500 GB 7200 RPM SATA</td>
</tr>
<tr>
<td>Other Hardware:</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating System:</td>
<td>SUSE Linux Enterprise Server 12 3.12.28-4-default</td>
</tr>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux</td>
</tr>
<tr>
<td>Auto Parallel:</td>
<td>No</td>
</tr>
<tr>
<td>File System:</td>
<td>ext4</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 multi-user</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>32-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other Software:</td>
<td>Microquill SmartHeap V10.2</td>
</tr>
</tbody>
</table>
Dell Inc.  
PowerEdge T130 (Intel Xeon E3-1240 v5, 3.50 GHz)  

**SPEC CINT2006 Result**  

**Dell Inc.**  
PowerEdge T130 (Intel Xeon E3-1240 v5, 3.50 GHz)  

**SPECint_rate2006 = 255**  
**SPECint_rate_base2006 = 246**  

**CPU2006 license:** 55  
**Test sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  

---  

**Results Table**  

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>8</td>
<td>390</td>
<td>201</td>
<td>390</td>
<td>200</td>
<td>391</td>
<td>200</td>
<td>8</td>
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<td>239</td>
<td>334</td>
<td>234</td>
<td>329</td>
<td>238</td>
<td></td>
<td></td>
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<tr>
<td>401.bzip2</td>
<td>8</td>
<td>702</td>
<td>110</td>
<td>712</td>
<td>108</td>
<td>711</td>
<td>109</td>
<td>8</td>
<td>681</td>
<td>113</td>
<td>680</td>
<td>114</td>
<td>683</td>
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<td>403.mcf</td>
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<td>196</td>
<td>333</td>
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<td>429.gobmk</td>
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<td>164</td>
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<td>164</td>
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<td>164</td>
<td>512</td>
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</tr>
<tr>
<td>456.hmmer</td>
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<td>164</td>
<td>456</td>
<td></td>
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</tr>
<tr>
<td>458.sjeng</td>
<td>8</td>
<td>544</td>
<td>178</td>
<td>545</td>
<td>178</td>
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<tr>
<td>462.libquantum</td>
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<td>2650</td>
<td>62.6</td>
<td>2650</td>
<td>62.5</td>
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<td>2650</td>
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<td>62.5</td>
<td>2650</td>
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</tr>
<tr>
<td>464.h264ref</td>
<td>8</td>
<td>556</td>
<td>318</td>
<td>557</td>
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<td>555</td>
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<td></td>
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</tr>
<tr>
<td>473.astar</td>
<td>8</td>
<td>430</td>
<td>131</td>
<td>431</td>
<td>130</td>
<td>429</td>
<td>131</td>
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<td>429</td>
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</tr>
<tr>
<td>483.xalancbmk</td>
<td>8</td>
<td>191</td>
<td>289</td>
<td>191</td>
<td>289</td>
<td>191</td>
<td>289</td>
<td>8</td>
<td>191</td>
<td>289</td>
<td>191</td>
<td>289</td>
<td>191</td>
<td>289</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

---  

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

---  

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

---  

**Platform Notes**

BIOS settings:
Virtualization Technology disabled
System Profile set to Performance
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb866b7b5a285932ceab81e28219e1
running on linux-gvm0 Sat Sep 19 20:37:36 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo

```plaintext
model name : Intel(R) Xeon(R) CPU E3-1240 v5 @ 3.50GHz
1 "physical id"s (chips)
8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with
```
Continued on next page
Dell Inc.
PowerEdge T130 (Intel Xeon E3-1240 v5, 3.50 GHz)

SPECint_rate2006 = 255
SPECint_rate_base2006 = 246

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Sep-2015
Hardware Availability: Nov-2015
Software Availability: Sep-2015

Platform Notes (Continued)

cautions.

From /proc/meminfo
MemTotal: 66066084 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0

# This file is deprecated and will be removed in a future service pack or
# release.

# Please check /etc/os-release for details about this release.

os-release:
NAME="SLES"
VERSION="12"
VERSION_ID="12"
PRETTY_NAME="SUSE Linux Enterprise Server 12"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12"
uname -a:
Linux linux-gvm0 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux
un-level 3 Sep 19 20:35

SPEC is set to: /root/cpu2006-1.2

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 0.3.16 09/09/2015
Memory:
1x 00AD00000000 HMA82GU7MFR8N-TF 16 GB 2 rank 2133 MHz
2x 00AD0000020B HMA82GU7MFR8N-TF 16 GB 2 rank 2133 MHz
1x 00AD00000800 HMA82GU7MFR8N-TF 16 GB 2 rank 2133 MHz

Continued on next page
Dell Inc. PowerEdge T130 (Intel Xeon E3-1240 v5, 3.50 GHz) SPECint_rate2006 = 255  
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Platform Notes (Continued)

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/cpu2006-1.2/32:/root/cpu2006-1.2/64:/root/cpu2006-1.2/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
umactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mc: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

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Base Optimization Flags (Continued)
C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags
C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation
C benchmarks (except as noted below):
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Peak Portability Flags
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
Dell Inc.

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**Software Availability:** Sep-2015

---

### Peak Optimization Flags

**C benchmarks:**

- 400.perlbench: `-xCORE-AVX2` (pass 2) `-prof-gen:threadsafepass1` `-ipo` (pass 2) `-O3` (pass 2) `-no-prec-div` (pass 2) `-par-num-threads=1` (pass 1) `-prof-use` (pass 2) `-auto-ilp32`
- 401.bzip2: `-xCORE-AVX2` (pass 2) `-prof-gen:threadsafepass1` `-ipo` (pass 2) `-O3` (pass 2) `-no-prec-div` (pass 2) `-par-num-threads=1` (pass 1) `-prof-use` (pass 2) `-opt-prefetch` `-auto-ilp32` `-ansi-alias`
- 403.gcc: `-xCORE-AVX2` `-ipo` `-O3` `-no-prec-div`
- 429.mcf: `basepeak = yes`
- 445.gobmk: `basepeak = yes`
- 456.hmmer: `-xCORE-AVX2` `-ipo` `-O3` `-no-prec-div` `-unroll2` `-auto-ilp32`
- 458.sjeng: `-xCORE-AVX2` (pass 2) `-prof-gen:threadsafepass1` `-ipo` (pass 2) `-O3` (pass 2) `-no-prec-div` (pass 2) `-par-num-threads=1` (pass 1) `-prof-use` (pass 2) `-unroll4` `-auto-ilp32`
- 462.libquantum: `basepeak = yes`
- 464.h264ref: `basepeak = yes`

**C++ benchmarks:**

- 471.omnetpp: `-xCORE-AVX2` (pass 2) `-prof-gen:threadsafepass1` `-ipo` (pass 2) `-O3` (pass 2) `-no-prec-div` (pass 2) `-par-num-threads=1` (pass 1) `-prof-use` (pass 2) `-ansi-alias` `-opt-ra-region-strategy=block` `-Wl,-z,muldefs` `-L/sh -lsmartheap`
- 473.astar: `basepeak = yes`
- 483.xalancbmk: `basepeak = yes`

---

### Peak Other Flags

**C benchmarks:**

- 403.gcc: `-Dalloca=_alloca`
Dell Inc.

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The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Nov 17 19:17:06 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 17 November 2015.