SPEC® CFP2006 Result

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Lenovo Group Limited

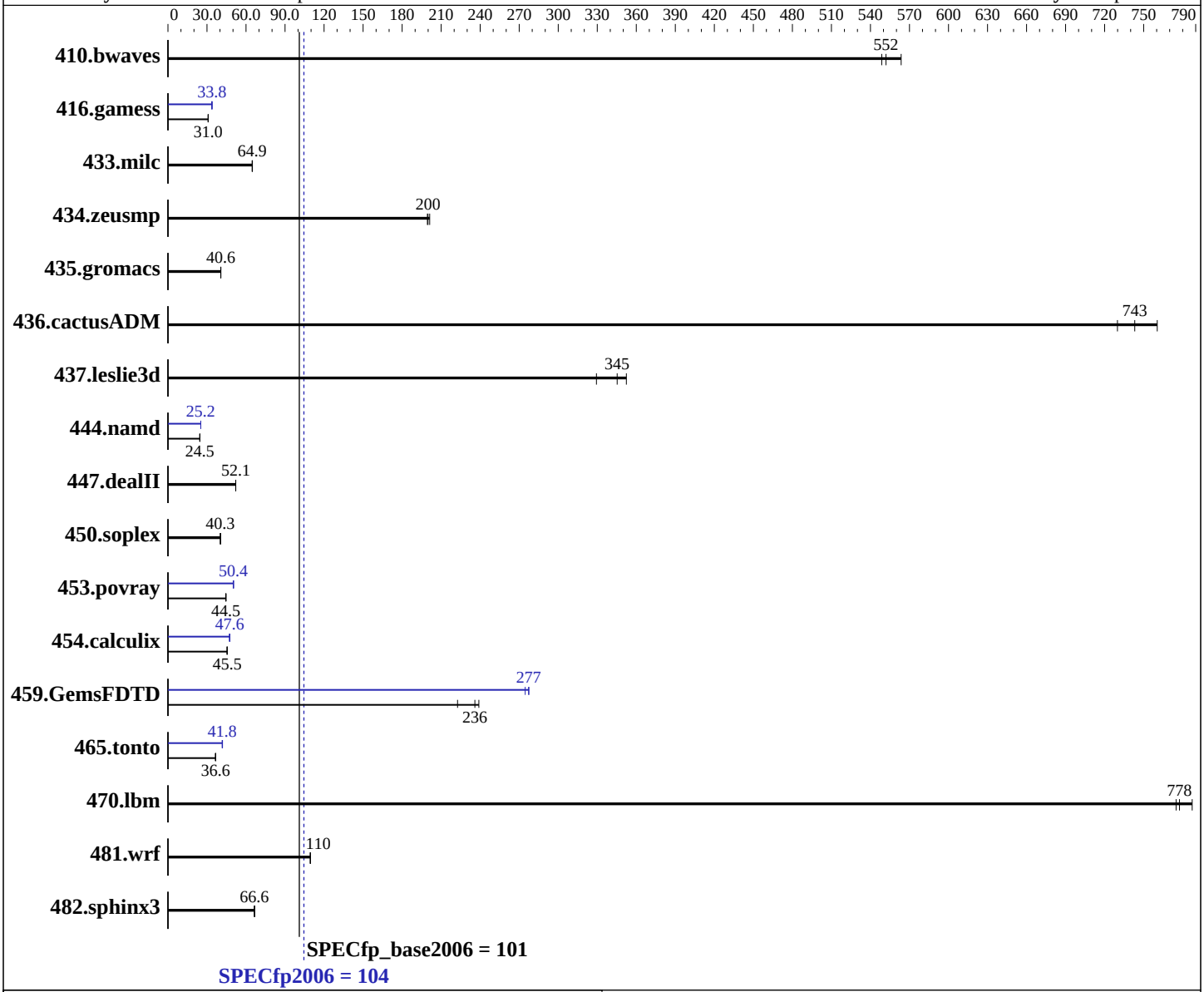
Lenovo System x3500 M5
(Intel Xeon E5-2658 v3, 2.20 GHz)

SPECfp®2006 = 104

SPECfp_base2006 = 101

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Test date: Nov-2015
Hardware Availability: Jan-2015
Software Availability: Sep-2014



Hardware

CPU Name:	Intel Xeon E5-2658 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 2.90 GHz
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	24 cores, 2 chips, 12 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs

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L3 Cache: 30 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem: 1 x 960 GB SATA SSD
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24.1	564	24.8	549	**24.6**	**552**	24.1	564	24.8	549	**24.6**	**552**
416.gamess	**633**	**31.0**	635	30.8	632	31.0	582	33.6	**579**	**33.8**	576	34.0
433.milc	**142**	**64.9**	141	64.9	142	64.7	**142**	**64.9**	141	64.9	142	64.7
434.zeusmp	45.6	199	**45.5**	**200**	45.2	201	45.6	199	**45.5**	**200**	45.2	201
435.gromacs	176	40.6	**176**	**40.6**	176	40.7	176	40.6	**176**	**40.6**	176	40.7
436.cactusADM	16.4	730	**16.1**	**743**	15.7	760	16.4	730	**16.1**	**743**	15.7	760
437.leslie3d	26.7	352	**27.2**	**345**	28.5	329	26.7	352	**27.2**	**345**	28.5	329
444.namd	327	24.5	327	24.5	**327**	**24.5**	318	25.2	318	25.2	**318**	**25.2**
447.dealII	219	52.1	**220**	**52.1**	220	52.1	219	52.1	**220**	**52.1**	220	52.1
450.soplex	**207**	**40.3**	207	40.2	206	40.5	**207**	**40.3**	207	40.2	206	40.5
453.povray	**120**	**44.5**	119	44.7	120	44.4	**106**	**50.4**	105	50.5	106	50.1
454.calculix	181	45.5	**181**	**45.5**	182	45.4	173	47.6	175	47.2	**173**	**47.6**
459.GemsFDTD	**45.0**	**236**	44.4	239	47.7	223	38.6	275	**38.3**	**277**	38.2	277
465.tonto	268	36.7	**269**	**36.6**	270	36.5	236	41.7	**235**	**41.8**	235	41.9
470.lbm	17.5	787	**17.7**	**778**	17.7	775	17.5	787	**17.7**	**778**	17.7	775
481.wrf	**102**	**110**	102	109	102	110	**102**	**110**	102	109	102	110
482.sphinx3	**293**	**66.6**	292	66.7	294	66.3	**293**	**66.6**	292	66.7	294	66.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS setting:
Operating Mode set to "Maximum Performance"
Hyper-threading set to "Enable"
Snoop mode set to "Early Snoop"
Sysinfo program /home/SPEC_ic16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on x3500M5 Fri Nov 13 00:30:15 2015

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
```

Continued on next page

Platform Notes (Continued)

```
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
       model name : Intel(R) Xeon(R) CPU E5-2658 v3 @ 2.20GHz
          2 "physical id"s (chips)
          48 "processors"
       cores, siblings (Caution: counting these is hw and system dependent.  The
       following excerpts from /proc/cpuinfo might not be reliable.  Use with
       caution.)
          cpu cores : 12
          siblings  : 24
          physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
          physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
       cache size : 30720 KB

    From /proc/meminfo
       MemTotal:       263453972 kB
       HugePages_Total:       0
       Hugepagesize:       2048 kB

    From /etc/*release* /etc/*version*
       os-release:
          NAME="Red Hat Enterprise Linux Server"
          VERSION="7.0 (Maipo)"
          ID="rhel"
          ID_LIKE="fedora"
          VERSION_ID="7.0"
          PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
          ANSI_COLOR="0;31"
          CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
       redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
       system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
       system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

    uname -a:
       Linux x3500M5 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64
       x86_64 x86_64 GNU/Linux


    SPEC is set to: /home/SPEC_ic16
       Filesystem            Type  Size  Used Avail Use% Mounted on
       /dev/mapper/rhel-root xfs   927G  149G  779G  16% /
    Additional information from dmidecode:

       Warning: Use caution when you interpret this section. The 'dmidecode' program
       reads system data which is "intended to allow hardware to be accurately
       determined", but the intent may not be met, as there are frequent changes to
       hardware, firmware, and the "DMTF SMBIOS" standard.

      BIOS IBM -[TAE105J-1.10]- 04/20/2015
      Memory:
       6x Hynix HMA42GR7MFR4N-TF 16 GB 2 rank 2133 MHz
```

Continued on next page

Platform Notes (Continued)

```
      10x Hynix HMA42GR7MFR4N-TFT1 16 GB 2 rank 2133 MHz
      8x NO DIMM Unknown

   (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/SPEC_ic16/libs/32:/home/SPEC_ic16/libs/64:/home/SPEC_ic16/sh"
OMP_NUM_THREADS = "24"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`
482.sphinx3: `-DSPEC_CPU_LP64`

Base Optimization Flags

C benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias`

C++ benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias`

Fortran benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

Continued on next page

Peak Optimization Flags (Continued)

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html`
`http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-D.20150923.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-D.20150923.xml`

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For other inquiries, please contact webmaster@spec.org.

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