Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant ML150 Gen9
(2.40 GHz, Intel Xeon E5-2630 v3)

SPECint_rate2006 = 694
SPECint_rate_base2006 = 662

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Test date: Nov-2015
Hardware Availability: Jul-2015
Software Availability: Aug-2015

CPU Name: Intel Xeon E5-2630 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L2 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
Disk Subsystem: 1 x 500 GB 7.2 K SATA, RAID 0
Other Hardware: None

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2
Hewlett Packard Enterprise  
ProLiant ML150 Gen9  
(2.40 GHz, Intel Xeon E5-2630 v3)  

**HPE**  
Hardware Availability: Jul-2015  
Software Availability: Aug-2015  

**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>Base</td>
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<td></td>
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<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>400.perlbench</td>
<td>32</td>
<td>672</td>
<td>465</td>
<td>678</td>
<td>461</td>
<td>672</td>
<td>465</td>
<td>32</td>
<td>534</td>
<td>586</td>
<td>534</td>
<td>586</td>
<td>534</td>
<td>585</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>32</td>
<td>970</td>
<td>318</td>
<td>971</td>
<td>318</td>
<td>973</td>
<td>317</td>
<td>32</td>
<td>942</td>
<td>328</td>
<td>940</td>
<td>329</td>
<td>939</td>
<td>329</td>
</tr>
<tr>
<td>403.gcc</td>
<td>32</td>
<td>307</td>
<td>508</td>
<td>510</td>
<td>505</td>
<td>508</td>
<td>507</td>
<td>32</td>
<td>507</td>
<td>508</td>
<td>509</td>
<td>506</td>
<td>504</td>
<td>511</td>
</tr>
<tr>
<td>429.mcf</td>
<td>32</td>
<td>322</td>
<td>905</td>
<td>322</td>
<td>900</td>
<td>320</td>
<td>913</td>
<td>32</td>
<td>322</td>
<td>905</td>
<td>322</td>
<td>907</td>
<td>320</td>
<td>913</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>32</td>
<td>774</td>
<td>434</td>
<td>775</td>
<td>433</td>
<td>774</td>
<td>433</td>
<td>32</td>
<td>754</td>
<td>445</td>
<td>756</td>
<td>444</td>
<td>753</td>
<td>444</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>32</td>
<td>321</td>
<td>931</td>
<td>323</td>
<td>924</td>
<td>321</td>
<td>929</td>
<td>32</td>
<td>281</td>
<td>1060</td>
<td>280</td>
<td>1070</td>
<td>280</td>
<td>1070</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>32</td>
<td>386</td>
<td>449</td>
<td>853</td>
<td>454</td>
<td>849</td>
<td>456</td>
<td>32</td>
<td>810</td>
<td>478</td>
<td>813</td>
<td>476</td>
<td>795</td>
<td>487</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>32</td>
<td>103</td>
<td>6460</td>
<td>103</td>
<td>6450</td>
<td>103</td>
<td>6460</td>
<td>32</td>
<td>103</td>
<td>6460</td>
<td>103</td>
<td>6450</td>
<td>103</td>
<td>6460</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>32</td>
<td>389</td>
<td>789</td>
<td>788</td>
<td>787</td>
<td>896</td>
<td>790</td>
<td>32</td>
<td>874</td>
<td>810</td>
<td>877</td>
<td>808</td>
<td>904</td>
<td>783</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>32</td>
<td>351</td>
<td>357</td>
<td>351</td>
<td>357</td>
<td>356</td>
<td>357</td>
<td>32</td>
<td>527</td>
<td>379</td>
<td>528</td>
<td>379</td>
<td>528</td>
<td>379</td>
</tr>
<tr>
<td>473.astar</td>
<td>32</td>
<td>386</td>
<td>383</td>
<td>390</td>
<td>381</td>
<td>590</td>
<td>381</td>
<td>32</td>
<td>586</td>
<td>383</td>
<td>590</td>
<td>381</td>
<td>590</td>
<td>381</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>32</td>
<td>269</td>
<td>820</td>
<td>270</td>
<td>818</td>
<td>269</td>
<td>821</td>
<td>32</td>
<td>269</td>
<td>820</td>
<td>270</td>
<td>818</td>
<td>269</td>
<td>821</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
    echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
    echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
    numactl --interleave=all runspec <etc>

**Platform Notes**

BIOS Configuration:
    HP Power Profile set to Custom
    HP Power Regulator to HP Static High Performance Mode
Minimum Processor Idle Power Core C-State set to C6 State
Minimum Processor Idle Power Package C-State set to No Package State
QPI Snoo Configuration set to Home Snoo
Collaborative Power Control set to Disabled
Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh

Continued on next page
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Platform Notes (Continued)

Sysinfo program /cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost Mon Nov 9 08:12:55 2015

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2630 v3 @ 2.40GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 8
  siblings : 16
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7
  cache size : 20480 KB

From /proc/meminfo
MemTotal:       263714392 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.0 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.0"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux localhost 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 9 08:12

SPEC is set to: /cpu2006
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 461G 66G 396G 15% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
Continued on next page
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Platform Notes (Continued)

reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

**BIOS HP P95 07/20/2015**
**Memory:**
12x HP 752369-081 16 GB 2 rank 2133 MHz, configured at 1866 MHz
4x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2133 MHz, configured at 1866 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Base Compiler Invocation

C benchmarks:
```
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

C++ benchmarks:
```
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

Base Portability Flags

```
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```
SPEC CINT2006 Result

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CPU2006 license: 3
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Base Optimization Flags
C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -W1,-z,muldefs -L/sh -lsmartheap

Base Other Flags
C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation
C benchmarks (except as noted below):
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Peak Portability Flags
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64

Continued on next page
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Peak Portability Flags (Continued)

483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias
-opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

Continued on next page
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**Peak Optimization Flags (Continued)**

483.xalancbmk: basepeak = yes

**Peak Other Flags**

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at:

http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html  
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml  
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml

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For other inquiries, please contact webmaster@spec.org.

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