Fujitsu
PRIMERGY TX1330 M2, Intel Xeon E3-1240L v5, 2.10 GHz

SPECfp®2006 = 86.1
SPECfp_base2006 = 84.0

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Nov-2015
Hardware Availability: Feb-2016
Software Availability: Sep-2015

Hardware
CPU Name: Intel Xeon E3-1240L v5
CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz: 2100
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software
Operating System: SUSE Linux Enterprise Server 12 (x86_64)
Kernel 3.12.48-52.27-default
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: ext4
System State: Run level 3 (multi-user)
## Results Table

<table>
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<tr>
<th>Benchmark</th>
<th>Base Seconds</th>
<th>Base Ratio</th>
<th>Peak Seconds</th>
<th>Peak Ratio</th>
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<td>milec</td>
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<td>zeusmp</td>
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<tr>
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<td>Leslie3d</td>
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<td>wrf</td>
<td>98.3</td>
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<td>98.3</td>
<td>114</td>
</tr>
<tr>
<td>sphinx3</td>
<td>240</td>
<td>81.2</td>
<td>240</td>
<td>81.2</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

BIOS configuration:
Sysinfo program /home/SPECcpu2006-RH72-ICC16-update/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on TX1330M2 Wed Nov 11 15:00:27 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
SPEC CFP2006 Result

Fujitsu

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Platform Notes (Continued)

model name : Intel(R) Xeon(R) CPU E3-1240L v5 @ 2.10GHz
  1 "physical id"s (chips)
  8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
cpu cores : 4
siblings : 8
  physical 0: cores 0 1 2 3
cache size : 8192 KB

From /proc/meminfo
MemTotal: 65902000 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12

From /etc/*release* /etc/*version*
SuSE-release:
  VERSION = 12
  PATCHLEVEL = 0
  name = "SLES"
  VERSION="12"
  VERSION_ID="12"
  PRETTY_NAME="SUSE Linux Enterprise Server 12"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12"

uname -a:
Linux TX1330M2 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux

run-level 5 Nov 11 09:59

SPEC is set to: /home/SPECcpu2006-RH72-ICC16-update
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 237G 23G 215G 10% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

Continued on next page
SPEC CFP2006 Result

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Platform Notes (Continued)

BIOS FUJITSU // American Megatrends Inc. V5.0.0.11 R1.1.0 for D3373-A1x
10/30/2015
Memory:
4x SK Hynix HMA82GU7MFR8N-TF 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
OMP_NUM_THREADS = "4"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/transparent_hugepage/enabled

For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64 -nofor_main
447.dealII: -DSPEC_CPU_LP64

Continued on next page
Base Portability Flags (Continued)

450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc   -m64

C++ benchmarks:
icpc  -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc   -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags
## Peak Optimization Flags

### C benchmarks:
- 433.milc: basepeak = yes
- 470.lbm: basepeak = yes
- 482.sphinx3: basepeak = yes

### C++ benchmarks:
- 444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
  -auto-ilp32
- 447.dealII: basepeak = yes
- 450.soplex: basepeak = yes
- 453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
  -ansi-alias

### Fortran benchmarks:
- 410.bwaves: basepeak = yes
- 416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
  -inline-level=0 -scalar-rep-
- 434.zeusmp: basepeak = yes
- 437.leslie3d: basepeak = yes
- 459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
  -inline-level=0 -opt-prefetch -parallel
- 465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
  -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:
Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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