Supermicro

SuperServer 4048B-TR4FT
(X10QBI, Intel Xeon E7-4830 v3)

SPEClnt®_rate2006 = 1750
SPEClnt_rate_base2006 = 1680

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Nov-2015
Hardware Availability: Sep-2015
Software Availability: Oct-2014

CPU Name: Intel Xeon E7-4830 v3
CPU Characteristics: Intel Turbo Boost Technology up to 2.70 GHz
CPU MHz: 2100
FPU: Integrated
CPU(s) enabled: 48 cores, 4 chips, 12 cores/chip, 2 threads/core
CPU(s) orderable: 1,2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 30 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (64 x 16 GB 2Rx4 PC4-2133P-R, running at 1333 MHz)
Disk Subsystem: 1 x 750 GB SATA III, 7200 RPM
Other Hardware: None

Operating System: SUSE Linux Enterprise Server 12, Kernel 3.12.28-4-default
Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE Studio XE for Linux
Auto Parallel: No
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2
**Supermicro**  
**SuperServer 4048B-TR4FT**  
(X10QBI, Intel Xeon E7-4830 v3)

**SPEC CINT2006 Result**  

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>96</td>
<td>749</td>
<td>1250</td>
<td>743</td>
<td>1260</td>
<td>739</td>
<td>1270</td>
<td>96</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>96</td>
<td>1151</td>
<td>805</td>
<td>1150</td>
<td>805</td>
<td>1152</td>
<td>804</td>
<td>96</td>
</tr>
<tr>
<td>403.gcc</td>
<td>96</td>
<td>607</td>
<td>1270</td>
<td>606</td>
<td>1270</td>
<td>599</td>
<td>1290</td>
<td>96</td>
</tr>
<tr>
<td>429.mcf</td>
<td>96</td>
<td>410</td>
<td>2140</td>
<td>411</td>
<td>2130</td>
<td>413</td>
<td>2120</td>
<td>96</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>96</td>
<td>845</td>
<td>1190</td>
<td>845</td>
<td>1190</td>
<td>846</td>
<td>1190</td>
<td>96</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>96</td>
<td>355</td>
<td>2520</td>
<td>349</td>
<td>2570</td>
<td>349</td>
<td>2570</td>
<td>96</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>96</td>
<td>931</td>
<td>1250</td>
<td>931</td>
<td>1250</td>
<td>932</td>
<td>1250</td>
<td>96</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>96</td>
<td>114</td>
<td>17500</td>
<td>114</td>
<td>17500</td>
<td>114</td>
<td>17500</td>
<td>96</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>96</td>
<td>1051</td>
<td>2020</td>
<td>1018</td>
<td>2090</td>
<td>1043</td>
<td>2040</td>
<td>96</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>96</td>
<td>741</td>
<td>809</td>
<td>742</td>
<td>809</td>
<td>738</td>
<td>813</td>
<td>96</td>
</tr>
<tr>
<td>473.astar</td>
<td>96</td>
<td>728</td>
<td>926</td>
<td>727</td>
<td>927</td>
<td>724</td>
<td>931</td>
<td>96</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>96</td>
<td>365</td>
<td>1820</td>
<td>366</td>
<td>1810</td>
<td>365</td>
<td>1810</td>
<td>96</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**Platform Notes**

BIOS Settings:
Power Technology = Custon  
C-State = C0/C1  
Demand Scrub = Disabled  
Sysinfo program /usr/cpu2006/config/sysinfo.rever6914  
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1  
running on 126-37 Sat Nov 7 02:03:45 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E7-4830 v3 @ 2.10GHz  
4 "physical id"s (chips)  
96 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The

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Platform Notes (Continued)

following excerpts from /proc/cpuinfo might not be reliable. Use with caution.

cpu cores : 12
siblings : 24
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB

From /proc/meminfo
MemTotal: 1058840408 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 0
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12"
    VERSION_ID="12"
    PRETTY_NAME="SUSE Linux Enterprise Server 12"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12"
uname -a:
  Linux 126-37 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014 (9879bd4)
  x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 7 02:02

SPEC is set to: /usr/cpu2006

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 2.0 10/27/2015
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Platform Notes (Continued)

Memory:
64x Hynix HMA42GR7MFR4N-TF 16 GB 2 rank 2133 MHz, configured at 1333 MHz
32x NO DIMM NO DIMM

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "'/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runcspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
 icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

C++ benchmarks:
  icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
  -opt-mem-layout-trans=3

C++ benchmarks:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
  -opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
## SPEC CINT2006 Result

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### Base Other Flags

C benchmarks:
- 403.gcc: -Dalloca=_alloca

### Peak Compiler Invocation

C benchmarks (except as noted below):
- icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
- 400.perlbench: icc -m64
- 401.bzip2: icc -m64
- 456.hmmer: icc -m64
- 458.sjeng: icc -m64

C++ benchmarks:
- icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

### Peak Portability Flags

- 400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
- 401.bzip2: -DSPEC_CPU_LP64
- 456.hmmer: -DSPEC_CPU_LP64
- 458.sjeng: -DSPEC_CPU_LP64
- 462.libquantum: -DSPEC_CPU_LINUX
- 483.xalancbmk: -DSPEC_CPU_LINUX

### Peak Optimization Flags

C benchmarks:
- 400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -prof-use(pass 2) -auto-ilp32
- 401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -prof-use(pass 2) -auto-ilp32
- 403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

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Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml
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<table>
<thead>
<tr>
<th>Specification</th>
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</tr>
</thead>
<tbody>
<tr>
<td>SPECint_rate2006</td>
<td>1750</td>
</tr>
<tr>
<td>SPECint_rate_base2006</td>
<td>1680</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Property</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU2006 license</td>
<td>001176</td>
</tr>
<tr>
<td>Test sponsor</td>
<td>Supermicro</td>
</tr>
<tr>
<td>Tested by</td>
<td>Supermicro</td>
</tr>
<tr>
<td>Test date</td>
<td>Nov-2015</td>
</tr>
<tr>
<td>Hardware Availability</td>
<td>Sep-2015</td>
</tr>
<tr>
<td>Software Availability</td>
<td>Oct-2014</td>
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</tbody>
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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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