



# SPEC® CFP2006 Result

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## Supermicro

Supermicro X11SSM-F motherboard  
(X11SSM-F, Intel Xeon E3-1235L v5)

**SPECfp®\_rate2006 = 164**

**SPECfp\_rate\_base2006 = 161**

CPU2006 license: 001176

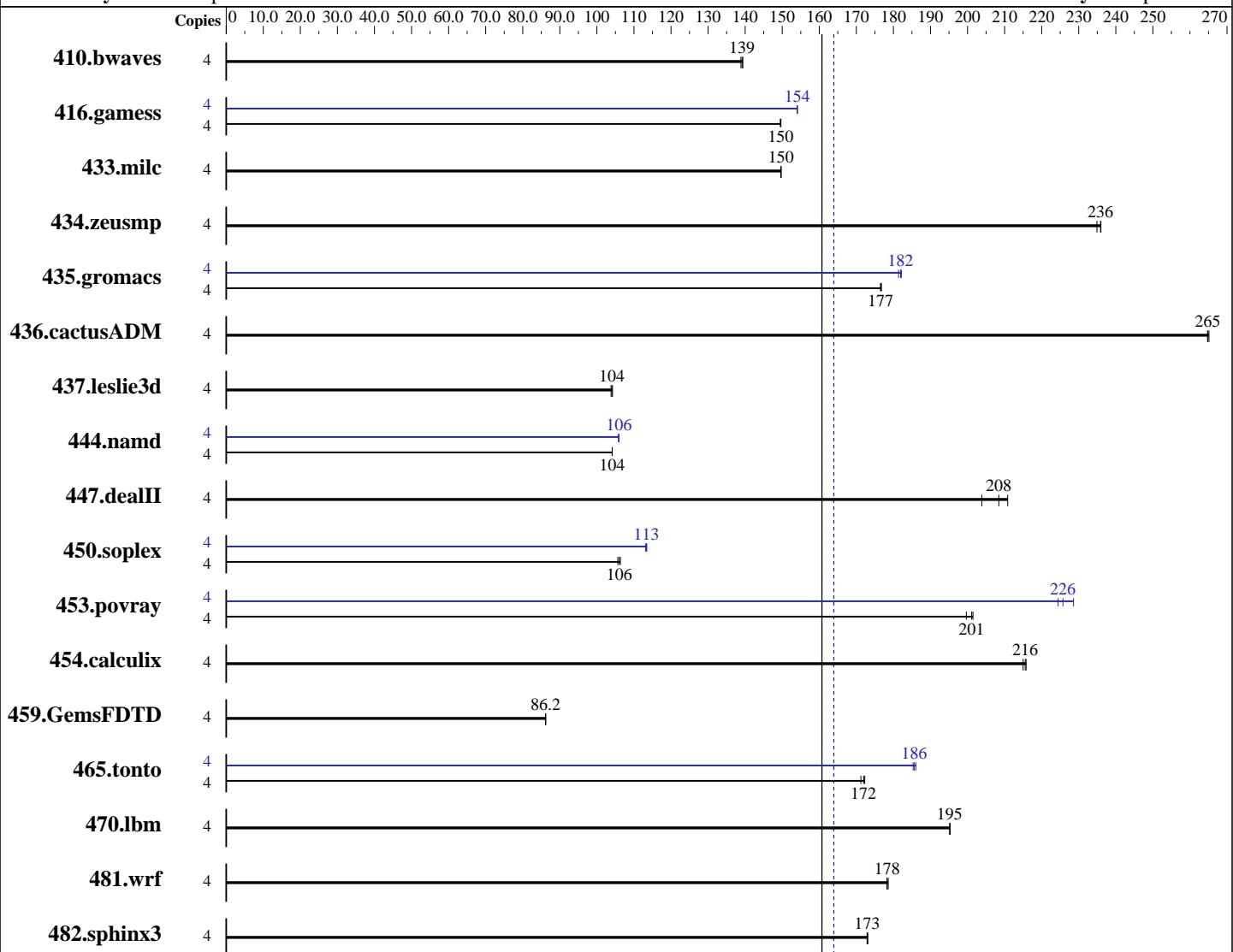
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jan-2016

Hardware Availability: Oct-2015

Software Availability: Sep-2015



**SPECfp\_rate\_base2006 = 161**

**SPECfp\_rate2006 = 164**

### Hardware

CPU Name: Intel Xeon E3-1235L v5  
CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
CPU MHz: 2000  
FPU: Integrated  
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
CPU(s) orderable: 1 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

### Software

Operating System: Red Hat Enterprise Linux Server release 7.1, Kernel 3.10.0-229.el7.x86\_64  
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
Auto Parallel: No  
File System: xfs  
System State: Run level 3 (multi-user)

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L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 32 GB (4 x 8 GB 2Rx8 PC4-2133P-E)  
Disk Subsystem: 1 x 200 GB SATA III SSD  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

| Benchmark     | Base   |            |            |            |             |            |            | Peak   |            |            |            |             |             |            |
|---------------|--------|------------|------------|------------|-------------|------------|------------|--------|------------|------------|------------|-------------|-------------|------------|
|               | Copies | Seconds    | Ratio      | Seconds    | Ratio       | Seconds    | Ratio      | Copies | Seconds    | Ratio      | Seconds    | Ratio       | Seconds     | Ratio      |
| 410.bwaves    | 4      | 391        | 139        | <u>391</u> | <u>139</u>  | 390        | 139        | 4      | 391        | 139        | <u>391</u> | <u>139</u>  | 390         | 139        |
| 416.gamess    | 4      | 523        | 150        | 524        | 149         | <u>524</u> | <u>150</u> | 4      | <u>508</u> | <u>154</u> | 508        | 154         | 508         | 154        |
| 433.milc      | 4      | 245        | 150        | <u>245</u> | <u>150</u>  | 245        | 150        | 4      | 245        | 150        | <u>245</u> | <u>150</u>  | 245         | 150        |
| 434.zeusmp    | 4      | <u>154</u> | <u>236</u> | 155        | 235         | 154        | 236        | 4      | <u>154</u> | <u>236</u> | 155        | 235         | 154         | 236        |
| 435.gromacs   | 4      | 162        | 177        | 162        | 177         | <u>162</u> | <u>177</u> | 4      | 157        | 181        | 157        | 182         | <u>157</u>  | <u>182</u> |
| 436.cactusADM | 4      | <u>180</u> | <u>265</u> | 181        | 265         | 180        | 265        | 4      | <u>180</u> | <u>265</u> | 181        | 265         | 180         | 265        |
| 437.leslie3d  | 4      | <u>361</u> | <u>104</u> | 362        | 104         | 361        | 104        | 4      | <u>361</u> | <u>104</u> | 362        | 104         | 361         | 104        |
| 444.namd      | 4      | 308        | 104        | <u>308</u> | <u>104</u>  | 308        | 104        | 4      | 303        | 106        | 303        | 106         | <u>303</u>  | <u>106</u> |
| 447.dealII    | 4      | 217        | 211        | 224        | 204         | <u>220</u> | <u>208</u> | 4      | 217        | 211        | 224        | 204         | <u>220</u>  | <u>208</u> |
| 450.soplex    | 4      | 316        | 106        | <u>315</u> | <u>106</u>  | 314        | 106        | 4      | 295        | 113        | 294        | 113         | <u>294</u>  | <u>113</u> |
| 453.povray    | 4      | <u>106</u> | <u>201</u> | 106        | 201         | 107        | 200        | 4      | 93.1       | 229        | 94.8       | 224         | <u>94.3</u> | <u>226</u> |
| 454.calculix  | 4      | <u>153</u> | <u>216</u> | 153        | 216         | 153        | 215        | 4      | <u>153</u> | <u>216</u> | 153        | 216         | 153         | 215        |
| 459.GemsFDTD  | 4      | 493        | 86.2       | <u>493</u> | <u>86.2</u> | 492        | 86.2       | 4      | 493        | 86.2       | <u>493</u> | <u>86.2</u> | 492         | 86.2       |
| 465.tonto     | 4      | <u>229</u> | <u>172</u> | 230        | 171         | 228        | 172        | 4      | 212        | 186        | 212        | 185         | <u>212</u>  | <u>186</u> |
| 470.lbm       | 4      | 282        | 195        | <u>282</u> | <u>195</u>  | 281        | 195        | 4      | 282        | 195        | <u>282</u> | <u>195</u>  | 281         | 195        |
| 481.wrf       | 4      | 250        | 179        | 251        | 178         | <u>251</u> | <u>178</u> | 4      | 250        | 179        | 251        | 178         | <u>251</u>  | <u>178</u> |
| 482.sphinx3   | 4      | <u>451</u> | <u>173</u> | 451        | 173         | 450        | 173        | 4      | <u>451</u> | <u>173</u> | 451        | 173         | 450         | 173        |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

As tested, the system used a Supermicro CSE-113MFAC2-R606CB chassis. The chassis is configured with 2 PWS-606P-1R redundant power supply, 1 SNK-P0046P heatsink, as well as 4 FAN-0154L4 middle cooling fan.

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**SPECfp\_rate\_base2006 = 161**

**CPU2006 license:** 001176

**Test date:** Jan-2016

**Test sponsor:** Supermicro

**Hardware Availability:** Oct-2015

**Tested by:** Supermicro

**Software Availability:** Sep-2015

## Platform Notes (Continued)

```
Sysinfo program /usr/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$
running on X11SSM-01 Sat Jan 2 17:09:09 2016
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1235L v5 @ 2.00GHz
        1 "physical id"s (chips)
        4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 4
siblings : 4
physical 0: cores 0 1 2 3
cache size : 8192 KB
```

```
From /proc/meminfo
MemTotal:      32769044 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.1 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.1"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server
```

```
uname -a:
Linux X11SSM-01 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38 EST 2015
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jan 2 17:06

```
SPEC is set to: /usr/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        xfs   183G   26G  157G  14% /
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately

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## Platform Notes (Continued)

determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.0b 12/29/2015

Memory:

4x Micron 18ASF1G72AZ-2G1A1 8 GB 2 rank 2133 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
```

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## Peak Portability Flags (Continued)

```

416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Peak Optimization Flags

C benchmarks:

```

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

```

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -fno-alias -auto-ilp32
447.dealII: basepeak = yes
450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -opt-malloc-options=3
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -unroll14 -ansi-alias

```

Fortran benchmarks:

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## Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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